

Nosheen Shahzad

Computer Engineer

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Profile

Portrayal:

I have versatile set of abilities to become part of a dynamic organization and to demonstrate my skills for the achievement of corporate objectives as well as personal and career growth. Talented in performance testing and benchmarking. Excellent at quickly identifying high level test strategies and problems. Collaborative but decisive personal style; effective communicator and problem solver.

Goal:

To secure a challenging position in a vibrant organization, which recognizes my individual creativity and flair, yet facilitates personal and professional growth of its individual members through teamwork.

Skills



Technical Skills

Defect/Bug Management and Test Management software

Microsoft Office Suite including Visio and Project

Java Application Server architecture and testing

Additional Skills:

Art and drawing

Photography

Gaming

Cooking

Badminton

Education

2015-2019 **Bachelors: (BS-Computer Engineering)**, COMSATS University Islamabad, Lahore Campus, Pakistan

2013-2015 **Intermediate: (ICS)**, Jinnah Islamia College, Lahore

2011-2013 **Matriculation:** Workers Welfare Girls High School, Lahore

Projects

Final Year Project

Sep 2018- July 2019

Electronic Voting Machine (In compliance with ECP)

Tools: Ubuntu, Netbeans, RHEL, JAVA

It was our Final Year Project. We have made Electronic Voting Machine according to the recommendation of Election Commission of Pakistan. It is a desktop based application

We have completed following objectives:

- Using a Local/Centralized server
- Encrypting traffic to/from centralized server
- Making voting system more secure, reliable, faster, cost & time efficient
- Maintain security using cryptography
- Printing Form-45 on completion of election

Development of software for Voting

1. Admin Panel
2. Authentication Module
3. Voting Module

We have divided the software into two different modules on recommendation of Director of Election Commission of Pakistan. The modules are:

1. Authentication Module
2. Voting Module

We used Fingerprint scanner and QR/Barcode scanner for authentication of the voter and Printer to print the vote after casting it on EVM.

We used Linux operating system instead of Windows because Linux is open source operating system and have security.

Semester Project

Sep 2018- Jan 2019

Live Streaming F1

Tools: HTML 5, Bootstrap, JavaScript, PHP, CSS

It was a semester project. It has completely covered web development and designing using HTML 5, Bootstrap, JavaScript and PHP.

Semester Project

Sep 2018- Jan 2019

Sine wave generator

Tools: Quartus

It was a semester project. It has completely covered the programming and debugging in VHDL. All the results are based on simulations from the Modelsim 10.1e from MadgeTech, using Test Bench Waveform Generator. Sin and cosine waves will be generated. We can also verify the wave by changing frequency and amplitude.

Semester Project

Feb 2018- May 2018

Noise Cancellation

Tools: MATLAB

It was a semester project. It has completely covered programming in MATLAB.

Semester Project

Feb 2018- May 2018

Automatic Railway Gate Control System

Tools: Proteous

It was a semester project. It was based on hardware and software. Automatic railway gate control system provides us an effective way to reduce the occurrence of accidents. This system can play an important role in railway management system. This system is designed to be worked automatically, it can be very useful in the remote areas where no station master and line man is present and gate will be opened close by using motor

Semester Project

Sep 2016- Jan 2017

4-Way Traffic Light

Tools: LabView

It was a project of 3rd Semester of Computer Architecture for Engineering. It has completely covered the designing system. It is made by using Software 'Lab view'.

Semester Project

Sep 2016- Jan 2017

Notepad

Tools: JAVA-Notepad++

It was the project of 3rd semester of Object Oriented Programming. It has completely covered the programming in JAVA (backend and Frontend Programming) by using tool Notepad++.

Semester Project

Feb 2016- May 2016

Digital Clock

Tools: Proteous

It was semester project of digital logic design. It has completely covered the designing system because the most difficult thing that is 7 segment use in this project.

Experience

- ✓ 2 months experience in **Front End designing** and **Quality Assurance** in i10studios

Achievements

- 3rd position in painting competition, TechFEST'18.
- Member of management committee in Young Engineering Council (YEC).
- Member of IEEE society.

Programming Languages

- C
- HTML
- VHDL
- Java
- MATLAB
- SQL

➤ MIPS Assembly

References

Available upon request.