

ECE 105: Introduction to Electrical Engineering

Lecture 4
Circuit Analysis 2
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Rehan Kapadia

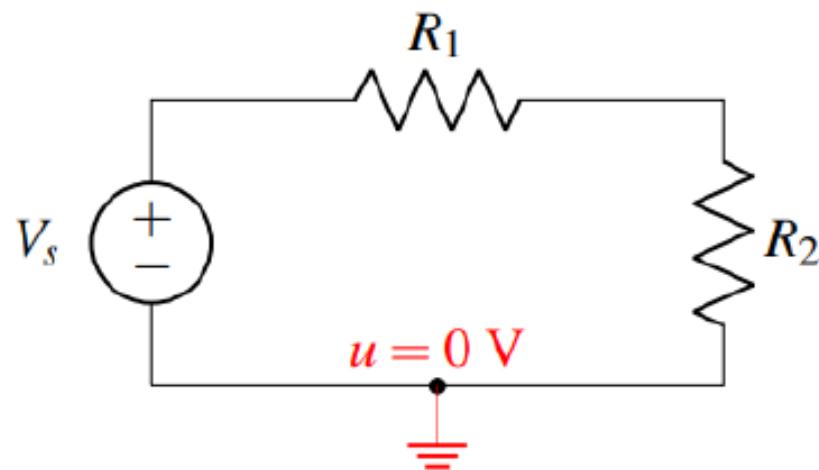
Voltage across a current source

2. True or False: A current source can have any voltage across it.

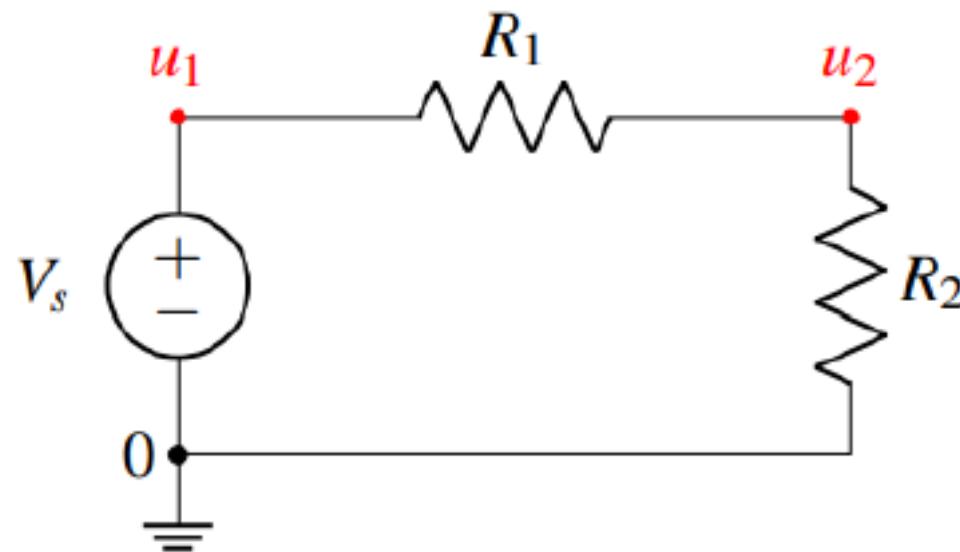
- The voltage across an ideal current source is completely determined by the circuit it is connected to.

Nodal analysis

Step 1: Pick a node and label it as $u = 0 \text{ V}$ ("reference"), meaning that we will measure all of the other node voltages in the circuit relative to this point. Any node can be the reference node, but conventionally its the negative terminal of a voltage source. On the schematic we can also use a **ground symbol** to denote the reference node.



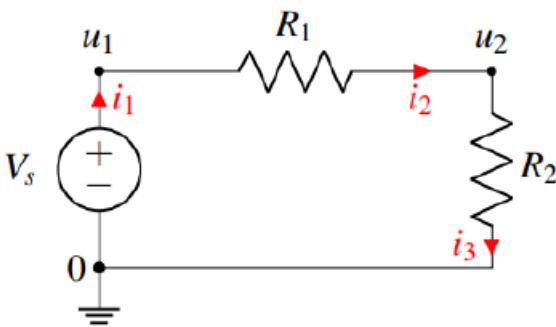
Step 2: Label all remaining nodes as some “ u_i ”, representing the voltage at each node relative to the zero/reference node.



Label currents

Step 3: Label the current through every element in the circuit “in” so that every element has a current label. The direction of the arrow indicates which direction of current flow you are considering to be positive. At this stage of the algorithm, you can pick the direction

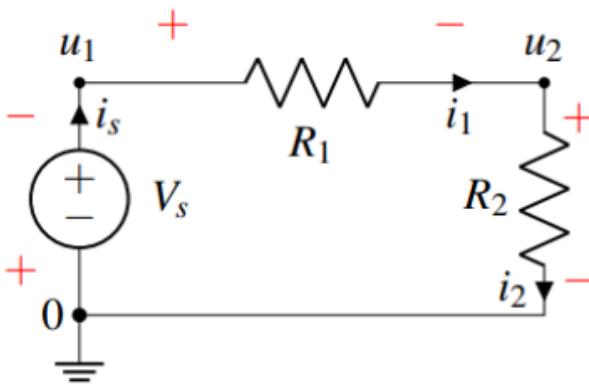
of all the current arrows arbitrarily. As long as you are consistent with this choice and follow the rules described in the rest of this algorithm, the math will work out correctly.



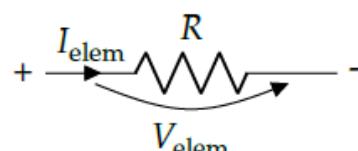
Note that we only label the current once for each element—for example, we can label i_3 as the current leaving the resistor (as is done in the diagram) or we can label it as the the current entering the resistor. These are equivalent because KCL also holds within the element itself—i.e., the current entering an element must be equal to the current exiting that same element.

Voltage drop directions

Step 4: Add $+/ -$ labels on each element to indicate positive/negative voltage, following the **passive sign convention** (defined below). These labels will indicate the direction with which voltage will be measured across that element.



Definition 3.4 (Passive Sign Convention): The **passive sign convention** dictates that positive current should *enter* the positive voltage terminal and *exit* the negative voltage terminal of an element. Below is an example for a resistor:



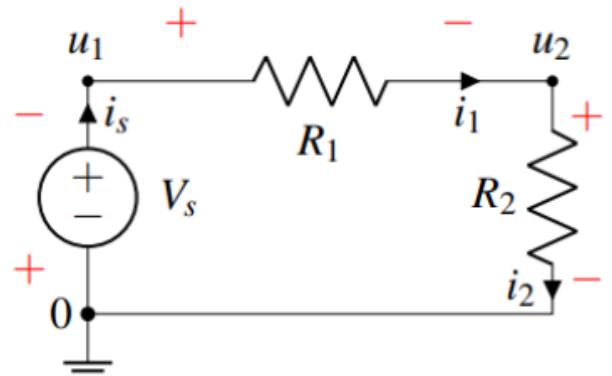
Write equations based on KCL and Ohm's law

Step 5: Identifying Unknowns and Reducing Them

At this stage in the circuit analysis algorithm, we find that there are several **unknowns** labeled on our circuit. These are the branch currents i_s , i_1 , and i_2 , and the node voltages u_1 and u_2 .

We can reduce the number of unknown variables by making some plain substitutions.

- Simplify node voltages: We can apply the known voltage of the voltage source to reduce the number of unknown node voltages. For this example,



$$u_1 - 0 = V_s$$

thus the node voltage $u_1 = V_s$ is known.

Simplify element currents in the same branch: If a node connects only two elements, then applying KCL at this node reveals the currents through both elements are equivalent. In this example,

$$i_1 = i_s \quad \text{and} \quad i_2 = i_s$$

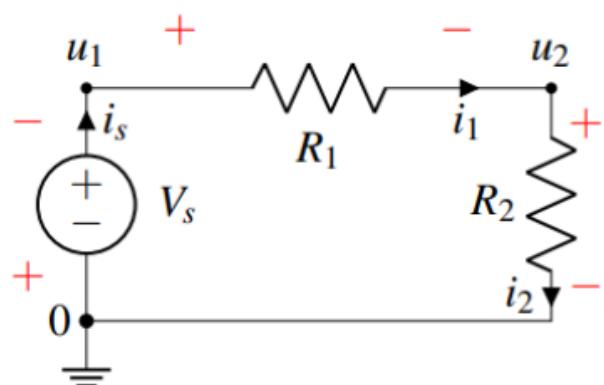
and so the circuit currents can be effectively expressed with a single unknown current i_s . Utilizing the current of a current source can also simplify the unknown element currents.

Write equations based on KCL and Ohm's law

Step 6a: Set up a system of linear equations using KCL and I-V relationships. Write a KCL equation at each node with unknown voltage.

Begin by writing KCL equations for every node in the circuit.

$$i_s - i_1 = 0$$



$$i_1 - i_2 = 0$$

$$i_2 - i_s = 0$$

Write equations based on KCL and Ohm's law

Step 6b: Use the I-V relationships of each element and express the voltage across each circuit element as a difference of node voltages. We know that the difference in potentials across the voltage source must be its voltage, V_s . We also know that the voltage across the resistor is equal to the current times the resistance, from Ohm's Law (i.e., $V = I \cdot R$). Note that the polarity of Ohm's Law depends on correctly applying the **passive sign convention**. Thus, we have the following equations:

$$V_{R1} = i_1 R_1$$

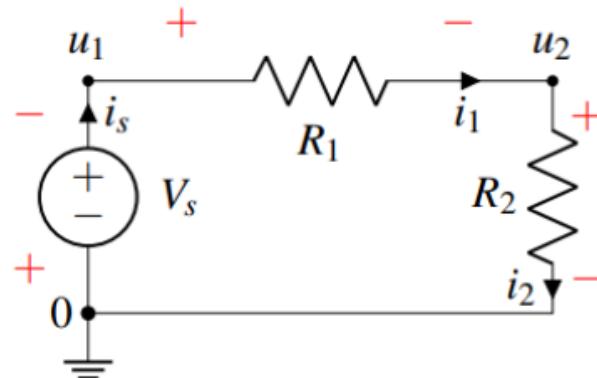
$$V_{R2} = i_2 R_2$$

Next, express the voltage across each circuit element as a difference of node voltages.

$$V_{R1} = u_1 - u_2$$

$$V_{R2} = u_2 - 0$$

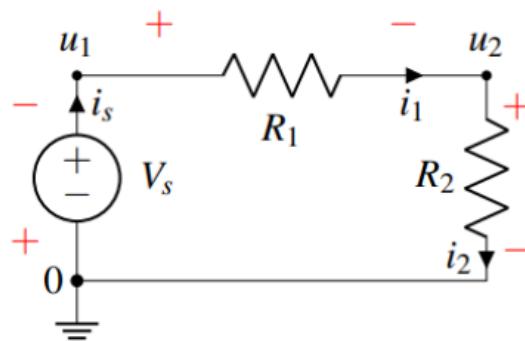
In this way, we directly relate the element I-V characteristics and the node voltages.



Solve for unknowns

Step 7: Simplify your equations and solve. Be sure to incorporate the reduction in unknowns from Step 5 (i.e., $u_1 = V_s$, $i_1 = i_s$, $i_2 = i_s$).

The final equations in terms of the two unknowns u_2 and i_s are:



$$V_s - u_2 = i_s R_1$$

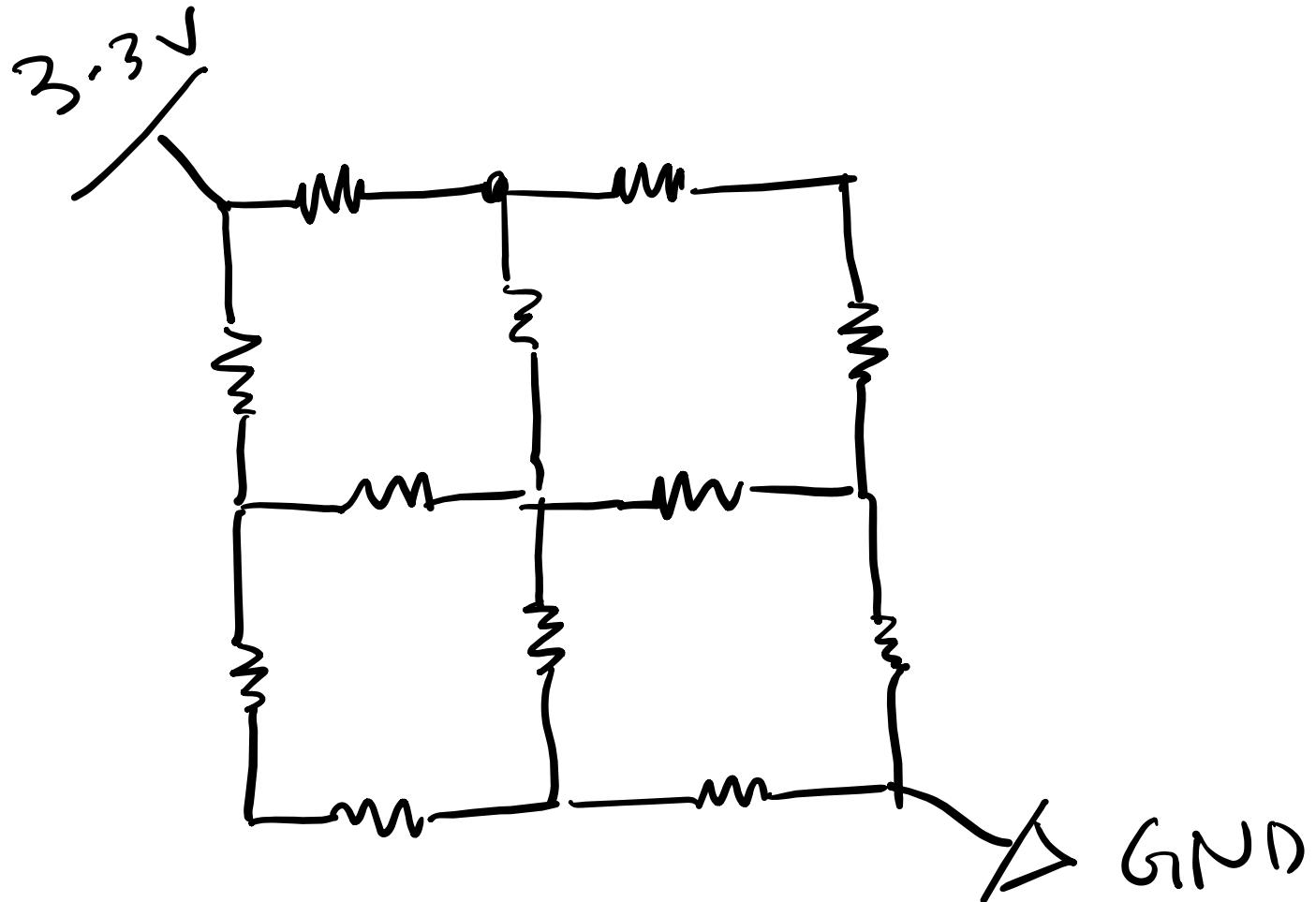
$$u_2 = i_s R_2$$

To solve, one can use substitution to find expressions for the unknowns u_2 and i_s :

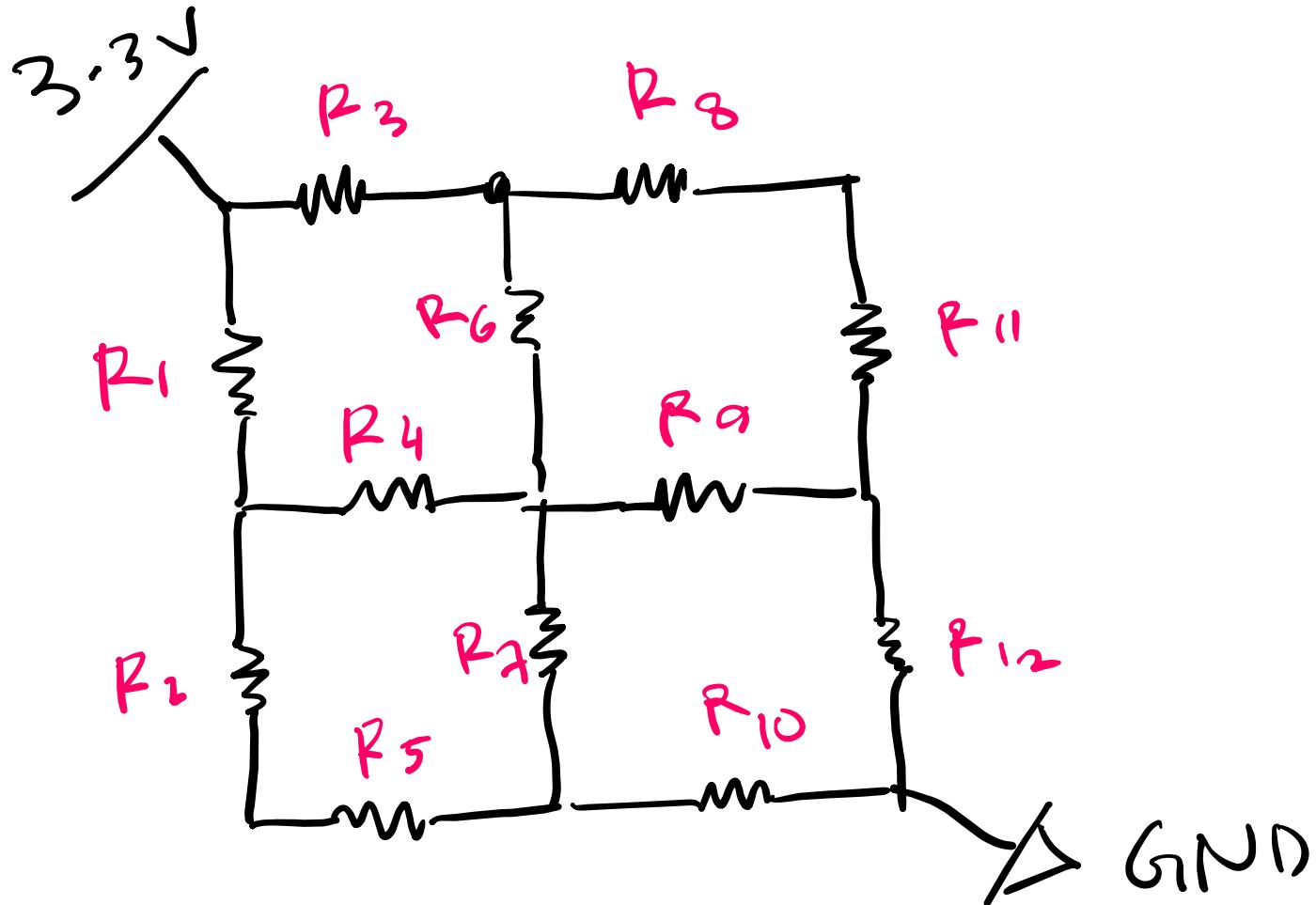
$$u_2 = \frac{R_2}{R_1 + R_2} V_s$$

$$i_s = \frac{1}{R_1 + R_2} V_s$$

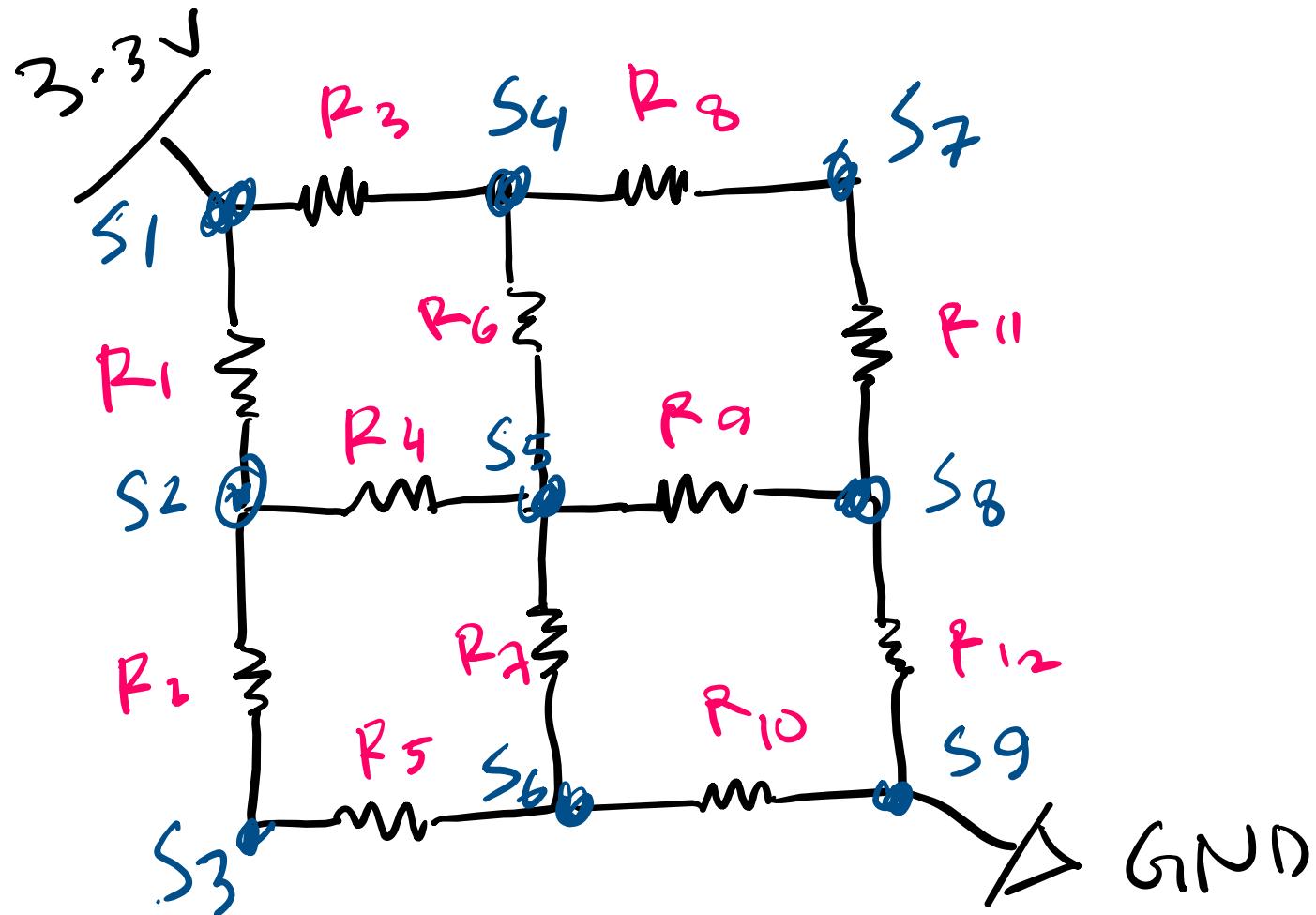
Touchpad – Network 2 in the demo board



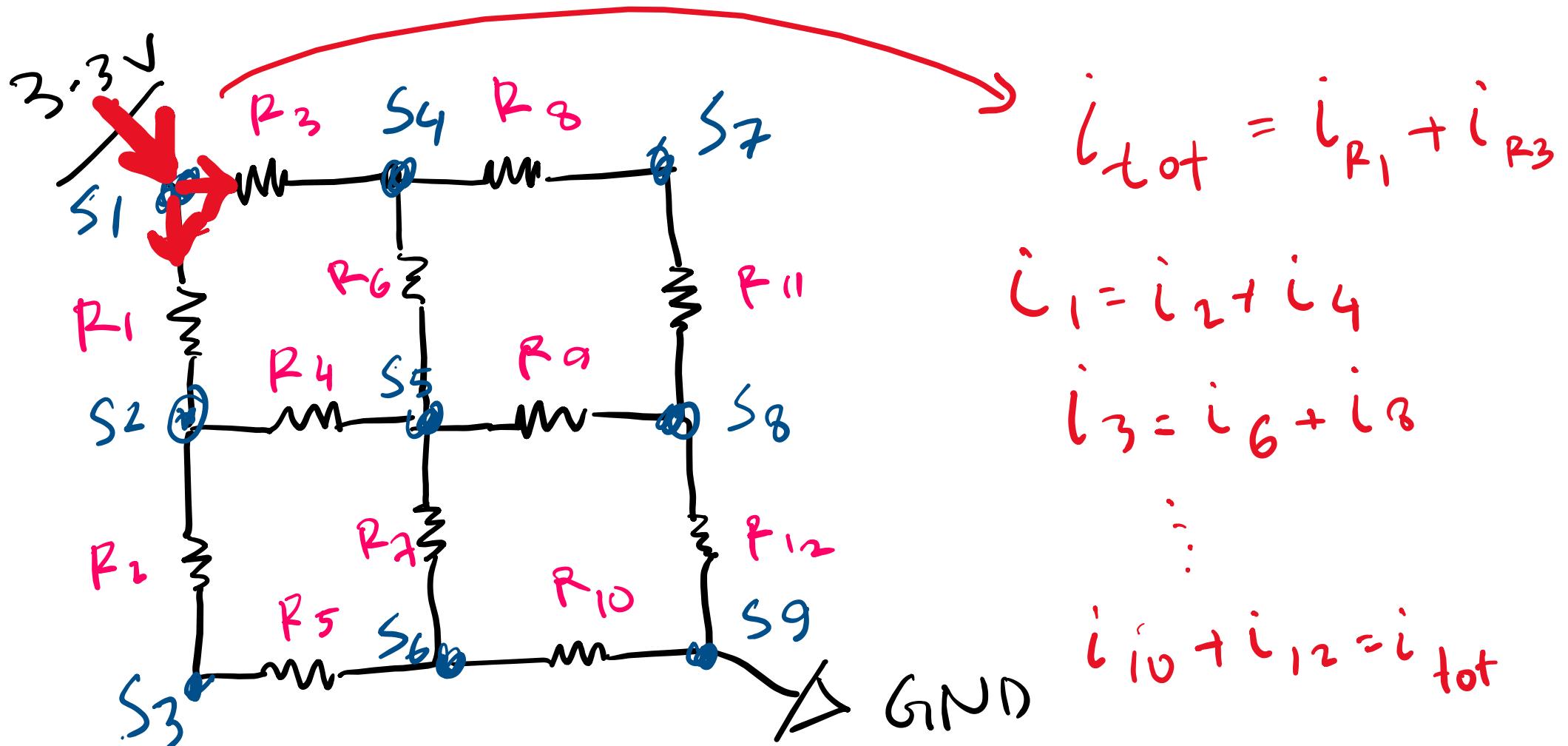
Touchpad – Network 2 in the demo board



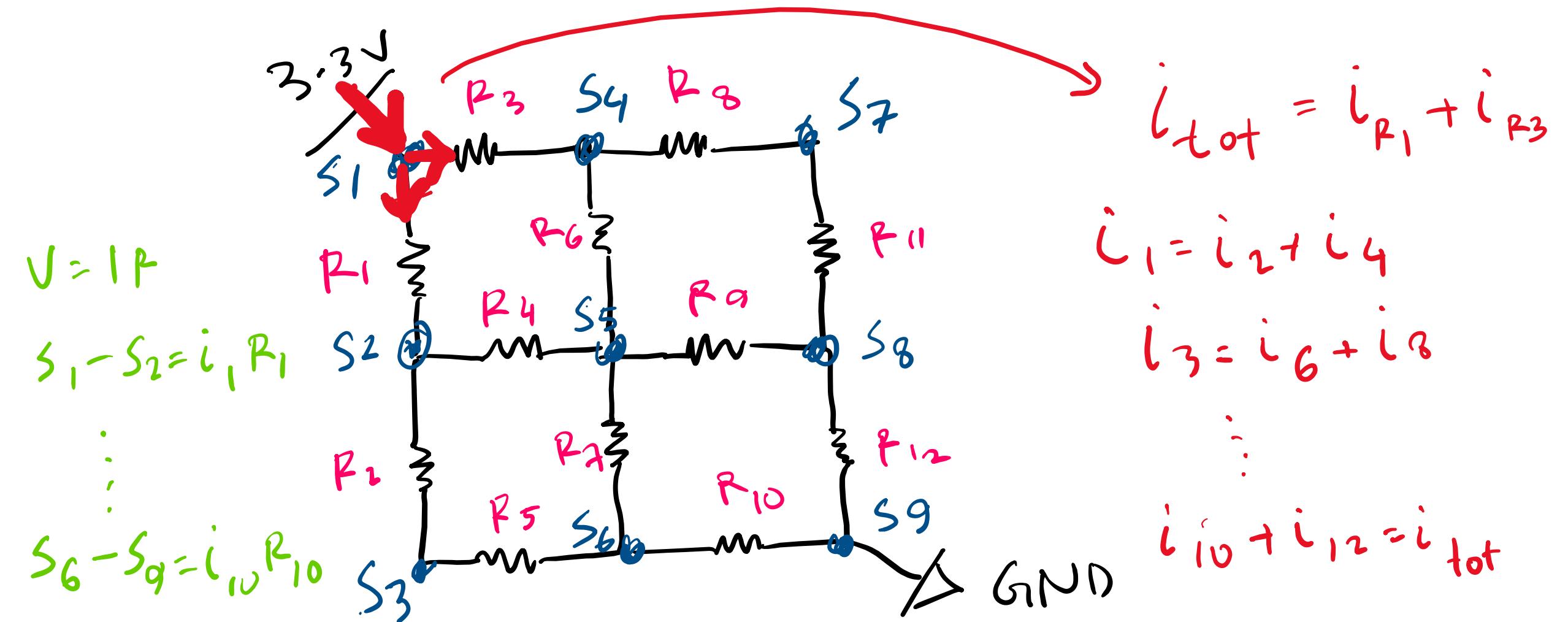
Touchpad – Network 2 in the demo board



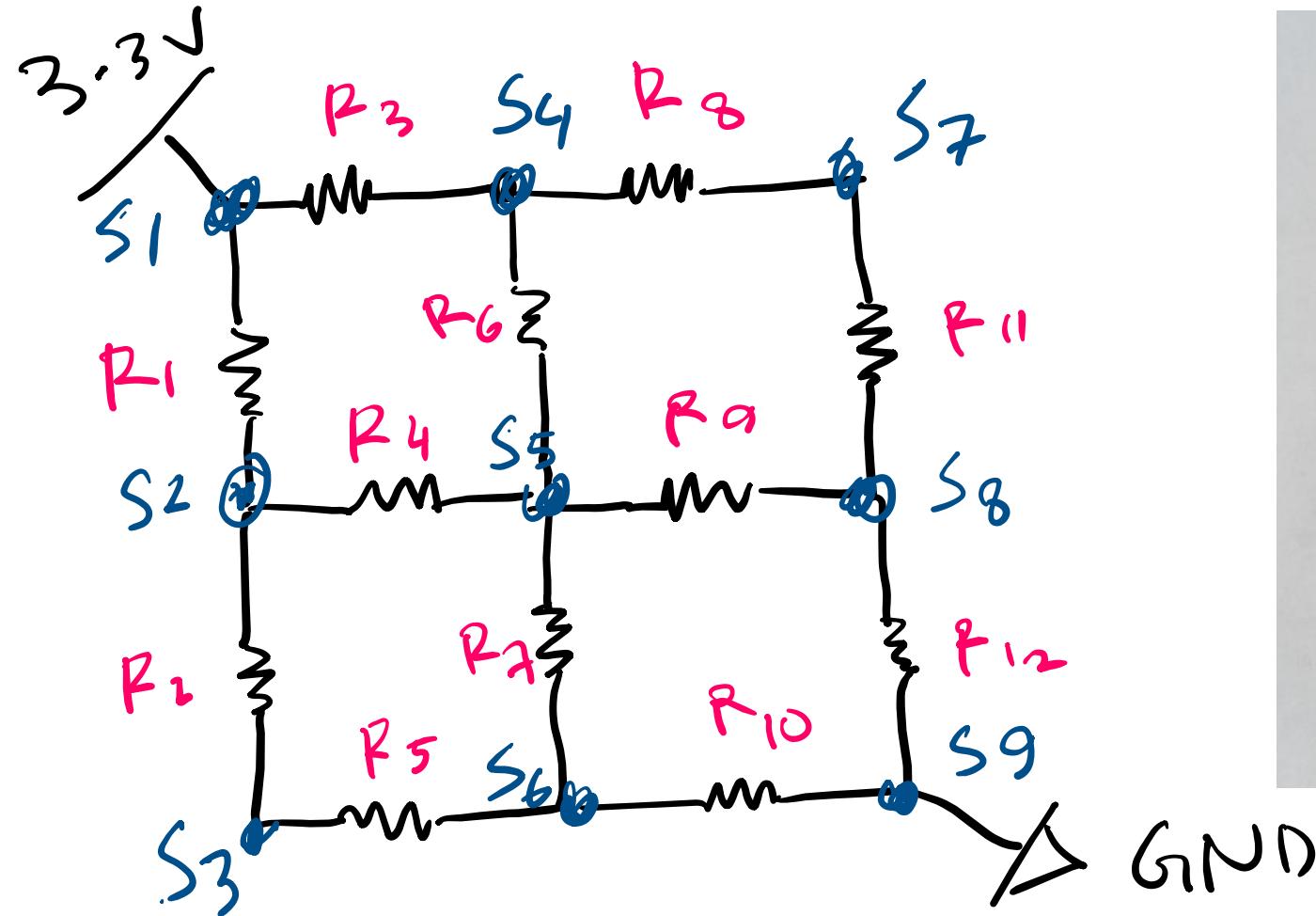
Touchpad – Network 2 in the demo board



Touchpad – Network 2 in the demo board



Touchpad – Network 2 in the demo board



$\frac{1}{R_{eq}}$

$$V_{R1} = S_1 - S_2 = i_1 R_1 = S_1 - S_2$$

$$N_{R2} = i_2 R_2 = S_2 - S_3$$

$$V_{R3} = i_3 R_3 = S_1 - S_4$$

$$V_{R4} = i_4 R_4 = S_2 - S_5$$

$$V_{R5} = i_5 R_5 = S_3 - S_6$$

$$i_{R6} = S_4 - S_5$$

$$i_{R7} = S_5 - S_6$$

$$i_{R8} = S_4 - S_7$$

$$i_{R9} = S_5 - S_8$$

$$i_{R10} = S_6 - S_7$$

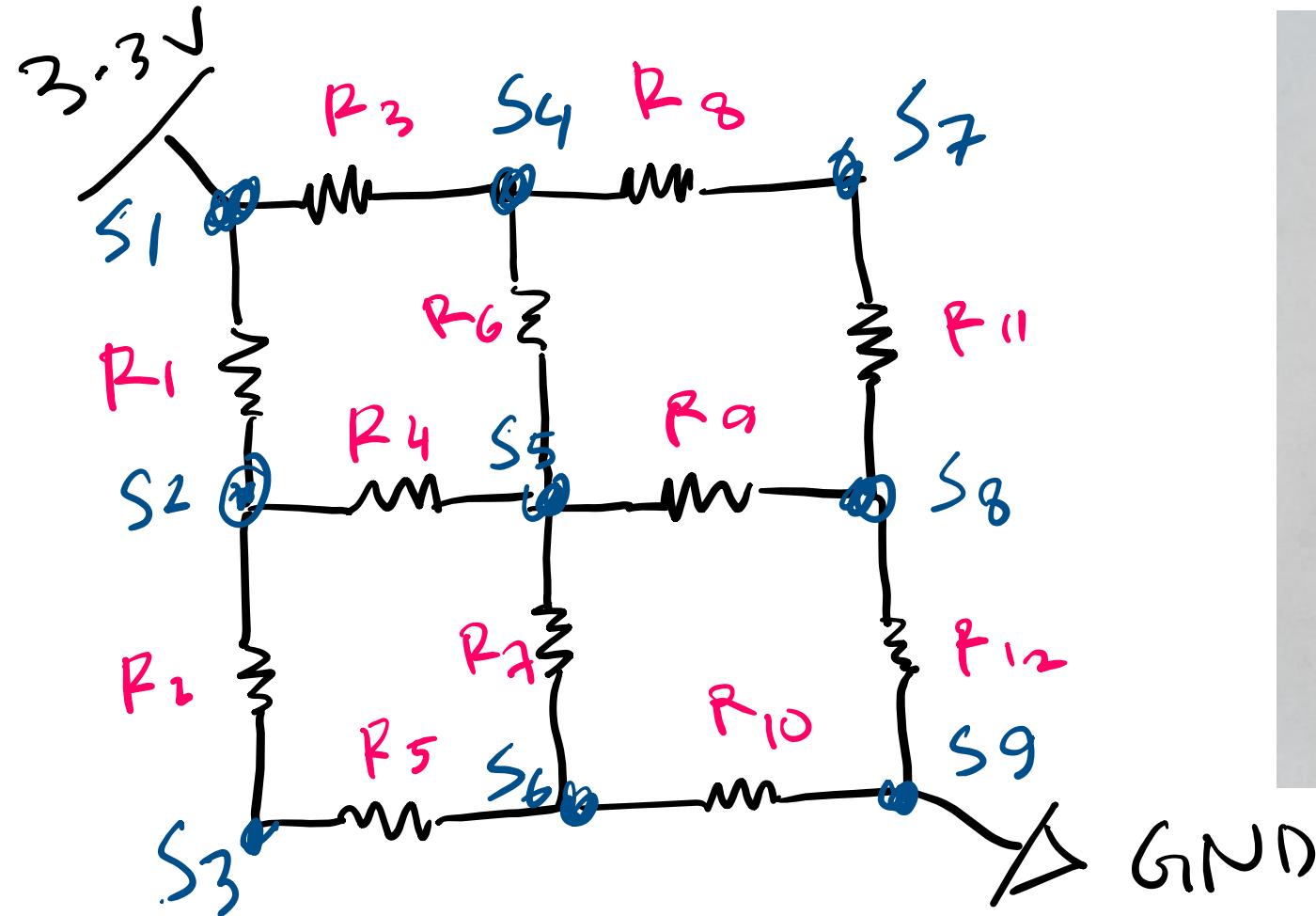
$$i_{R11} = S_7 - S_8$$

$$i_{R12} = S_8 - S_9$$

$$A_{21} = \frac{V}{i}$$

gen.

Touchpad – Network 2 in the demo board



(12 eq)

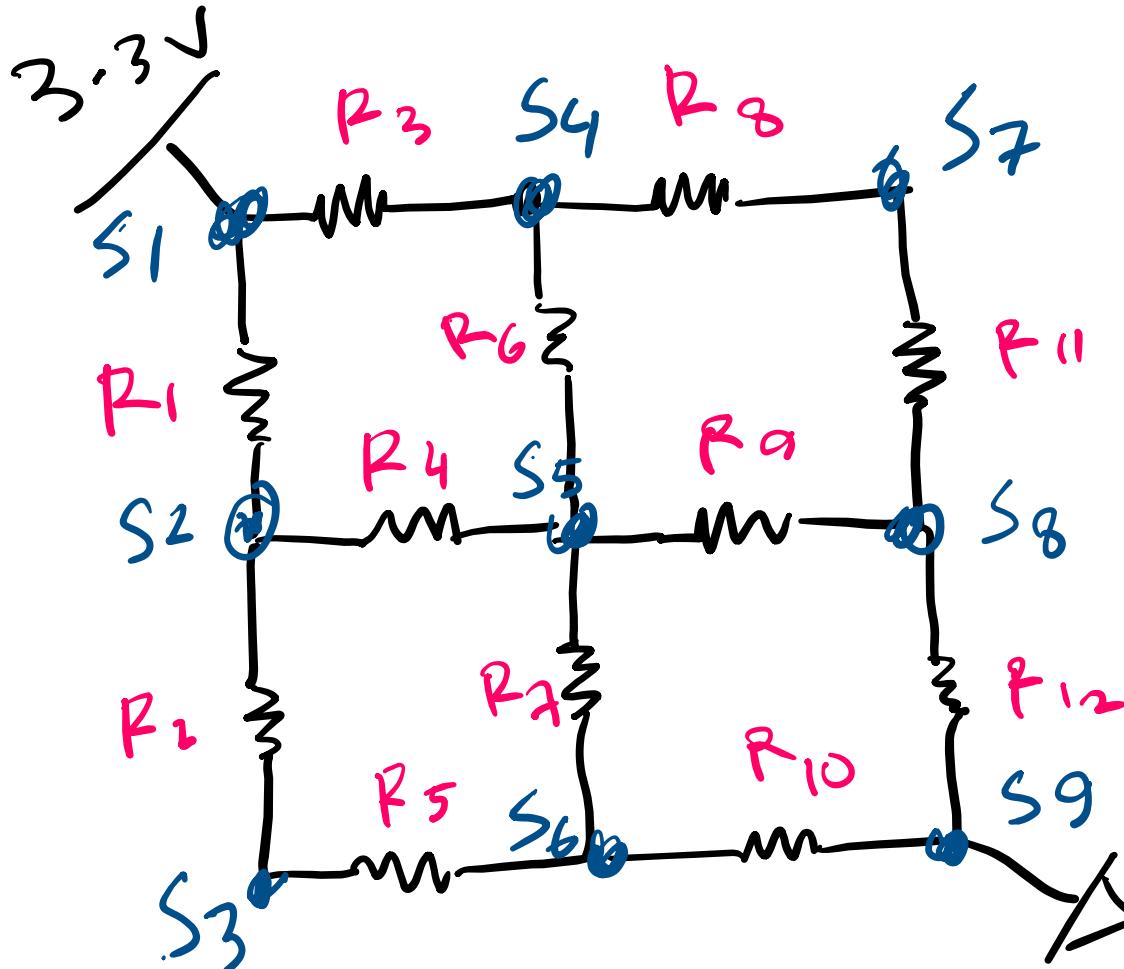
$$\begin{aligned}
 i_t &= i_{R1} + i_{R2} \\
 i_{R1} &= i_{R2} + i_{R4} \\
 i_{R3} &= i_{R6} + i_{R8} \\
 i_{R5} &= i_{R7} \\
 i_{R8} &= i_{R11} \\
 i_{R4} + i_{R6} &= i_{R7} + i_{R9} \\
 i_{R5} + i_{R7} &= i_{R10} \\
 i_{R9} + i_{R11} &= i_{R12} \\
 i_{R10} + i_{R12} &= i_t \\
 \end{aligned}$$

(9 eq)

$$\begin{aligned}
 V_{R1} &= S_1 - S_2 = i_{R1} = S_1 - S_2 \\
 V_{R2} &= i_{R2} = S_2 - S_3 \\
 V_{R3} &= i_{R3} = S_1 - S_4 \\
 V_{R4} &= i_{R4} = S_2 - S_5 \\
 V_{R5} &= i_{R5} = S_3 - S_6 \\
 i_{R6} &= S_4 - S_5 \\
 i_{R7} &= S_5 - S_6 \\
 i_{R8} &= S_4 - S_7 \\
 i_{R9} &= S_5 - S_8 \\
 i_{R10} &= S_6 - S_7 \\
 i_{R11} &= S_7 - S_8 \\
 i_{R12} &= S_8 - S_9
 \end{aligned}$$

$Ax = b$

Touchpad – Network 2 in the demo board

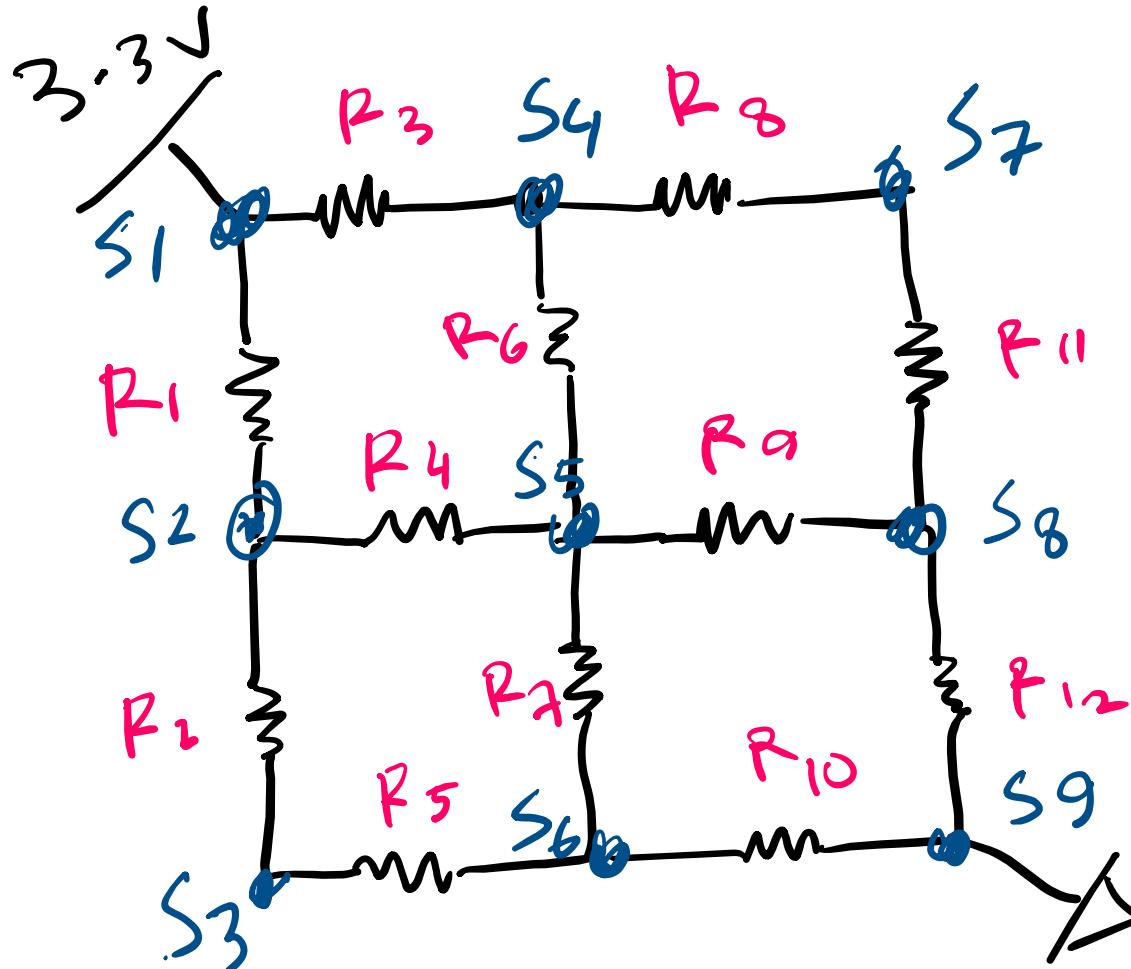


1. $i_t = i_1 + i_3$
2. $i_1 = i_2 + i_4$
3. $i_3 = i_6 + i_8$
4. $i_2 = i_5$
5. $i_8 = i_{11}$
6. $i_4 + i_6 = i_7 + i_9$
7. $i_5 + i_7 = i_{10}$
8. $i_9 + i_{11} = i_{12}$
9. $i_{10} + i_{12} = i_t$
10. $i_1 \cdot r_1 = s_1 - s_2$
11. $i_2 \cdot r_2 = s_2 - s_3$
12. $i_3 \cdot r_3 = s_1 - s_4$
13. $i_4 \cdot r_4 = s_2 - s_5$
14. $i_5 \cdot r_5 = s_3 - s_6$
15. $i_6 \cdot r_6 = s_4 - s_5$
16. $i_7 \cdot r_7 = s_5 - s_6$
17. $i_8 \cdot r_8 = s_4 - s_7$
18. $i_9 \cdot r_9 = s_5 - s_8$
19. $i_{10} \cdot r_{10} = s_6 - s_9$
20. $i_{11} \cdot r_{11} = s_7 - s_8$
21. $i_{12} \cdot r_{12} = s_8 - s_9$

With the conditions:

- $r_1 = r_2 = r_3 = r_4 = r_5 = r_6 = r_7 = r_8 = r_9 = r_{10} = r_{11} = r_{12} = 100$
- $s_1 = 3.3$
- $s_9 = 0$

Touchpad – Network 2 in the demo board



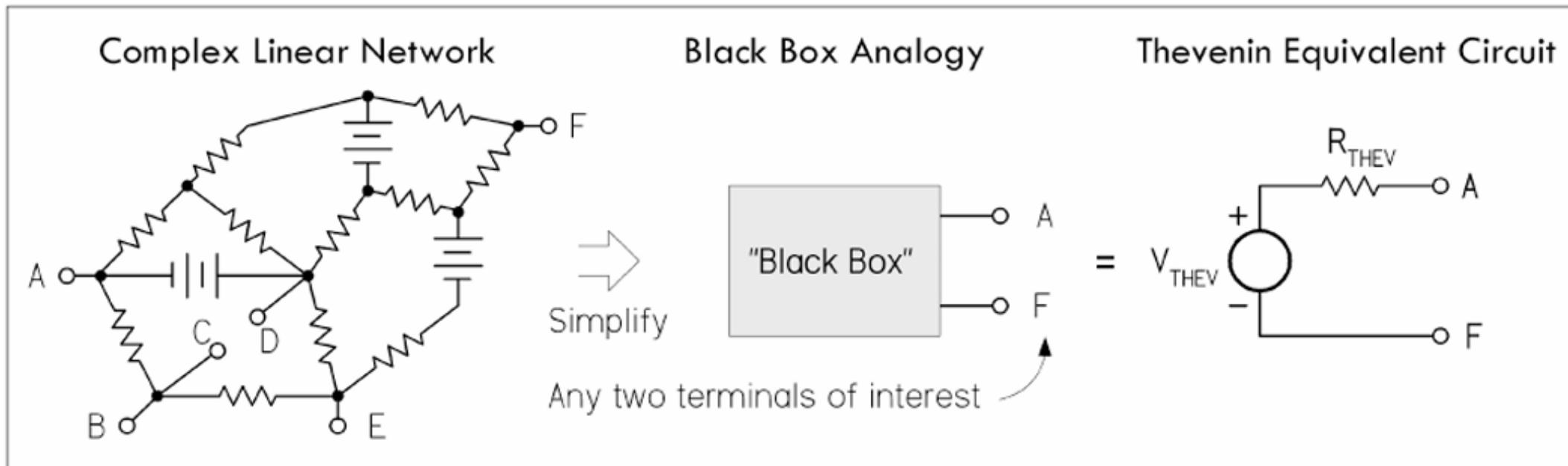
The solution to the system of equations is as follows:

- $i_1 = 0.011$
- $i_2 = 0.0055$
- $i_3 = 0.011$
- $i_4 = 0.0055$
- $i_5 = 0.0055$
- $i_6 = 0.0055$
- $i_7 = 0.0055$
- $i_8 = 0.0055$
- $i_9 = 0.0055$
- $i_{10} = 0.011$
- $i_{11} = 0.0055$
- $i_{12} = 0.011$
- $i_t = 0.022$

The intermediate variables are:

- $s_2 = 2.2$
- $s_3 = 1.65$
- $s_4 = 2.2$
- $s_5 = 1.65$
- $s_6 = 1.1$
- $s_7 = 1.65$
- $s_8 = 1.1$

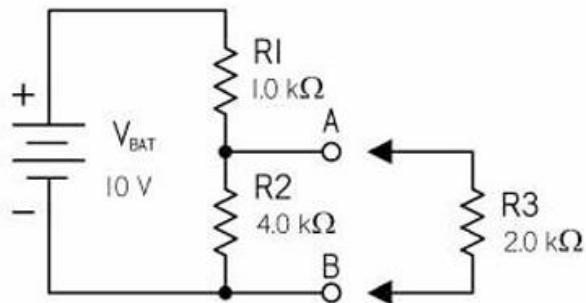
Equivalent circuits



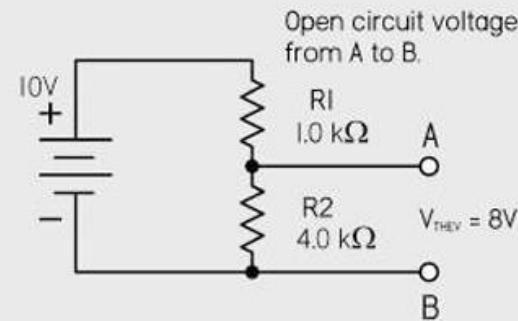
Thevenin's equivalent circuit

Thevenin's Theorem

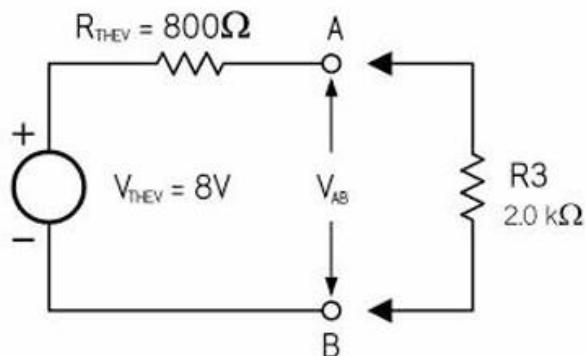
Real Circuit



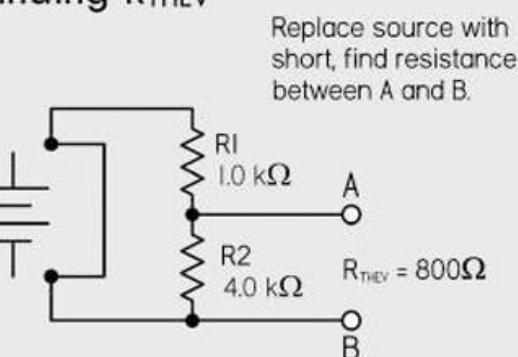
Finding V_{THEV}



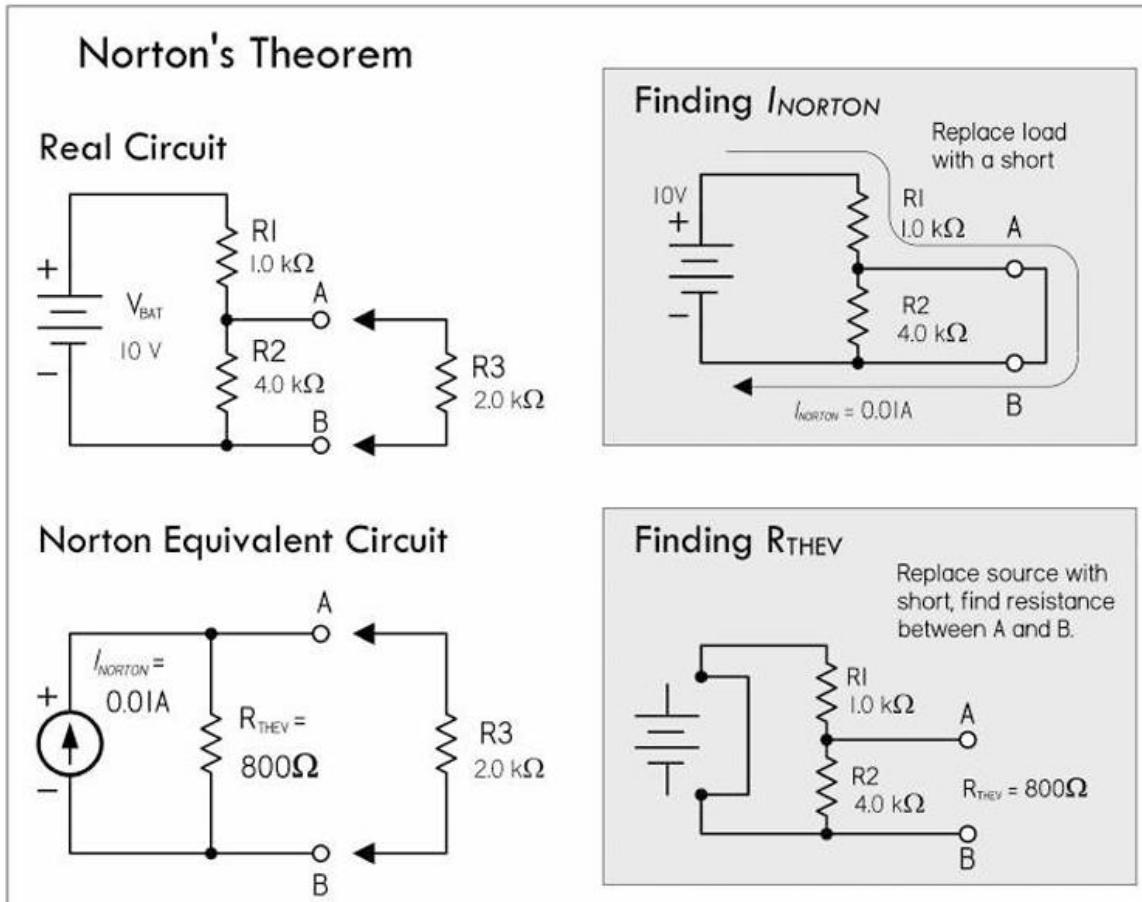
Thevenin Equivalent Circuit



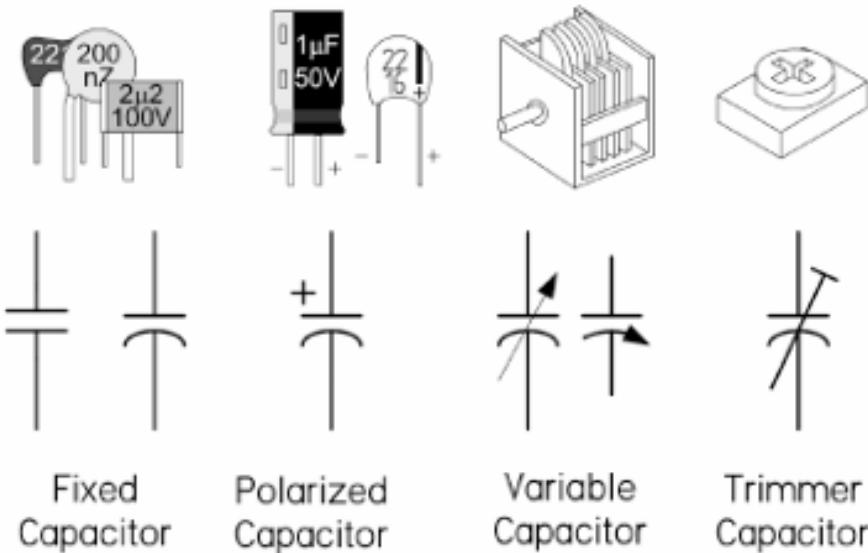
Finding R_{THEV}



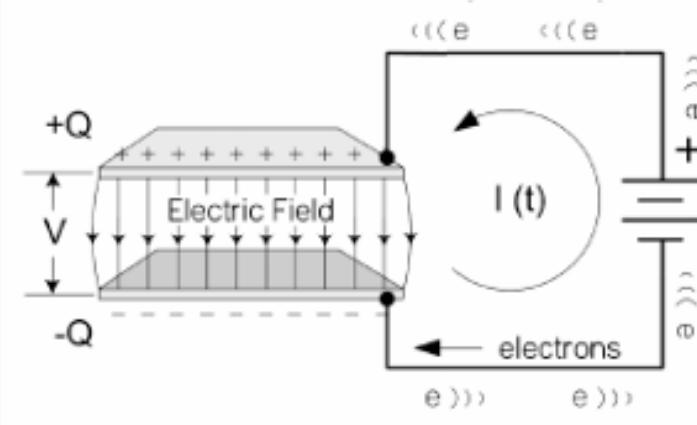
Norton's equivalent circuit



Capacitor Symbols

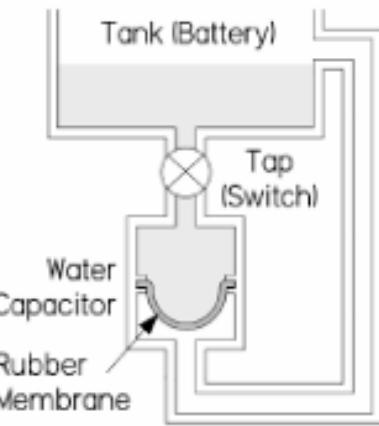


Parallel Plate Capacitor



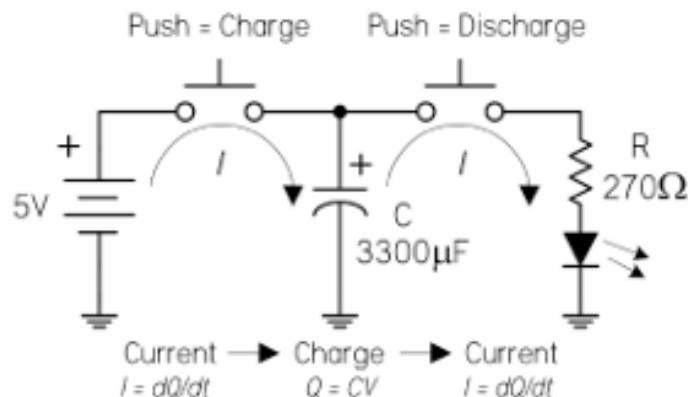
$$\text{Capacitance} = \frac{\text{Charge}}{\text{Voltage}} \quad C = \frac{Q}{V}$$

Water Analogy

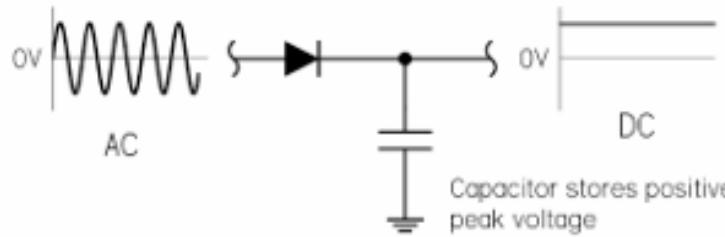


Capacitors

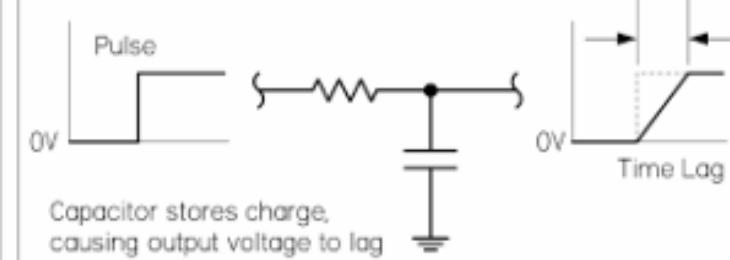
Basic Charge Storage and Discharge



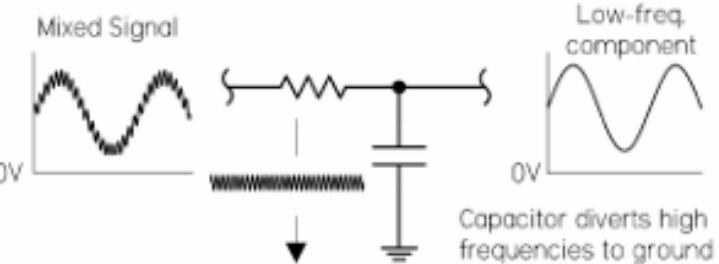
Smoothing Circuit



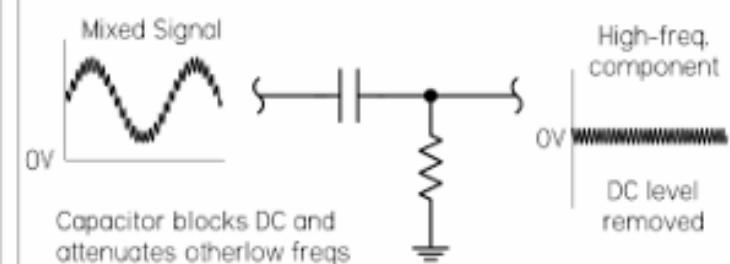
Timing & Waveform Shaping



Bypassing and Low-Pass Filtering

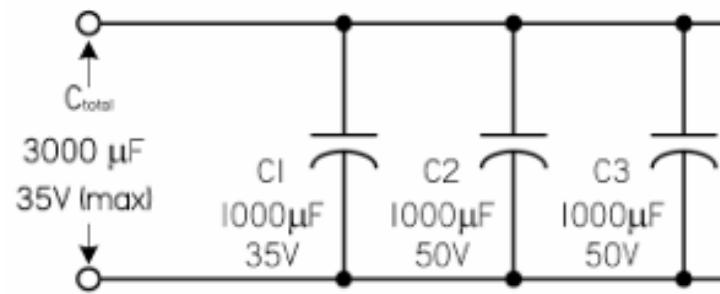


Coupling and High-Pass Filtering



Capacitors In Parallel

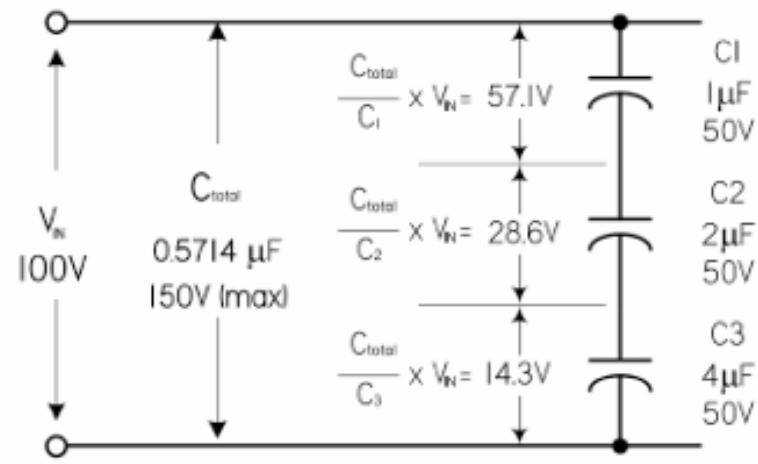
Increases the total capacitance, but limits max. voltage rating to that of smallest rated capacitor.



$$C_{\text{total}} = C_1 + C_2 + C_3 + \dots + C_n$$

Capacitors In Series

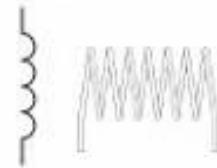
Increases max voltage rating, but decreases capacitance.



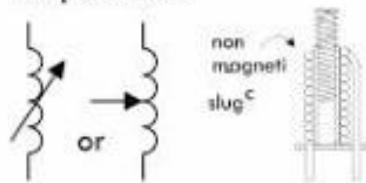
$$C_{\text{total}} = \frac{1}{\frac{1}{C_1} + \frac{1}{C_2} + \frac{1}{C_3} + \dots + \frac{1}{C_n}}$$

Inductor Symbols

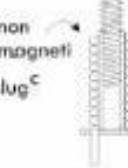
Air Core



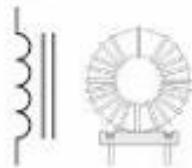
Adjustable



or



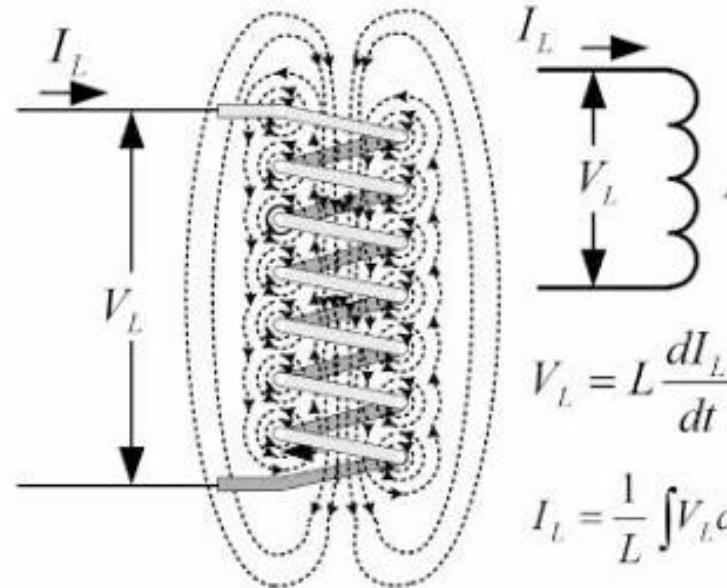
Magnetic or Iron Core



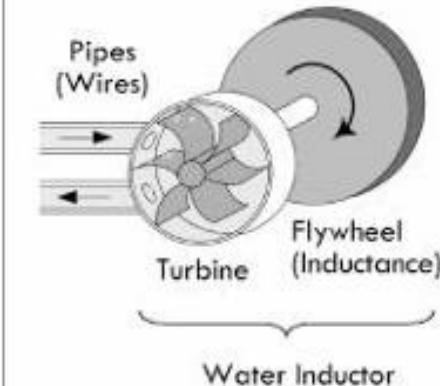
Ferrite Bead



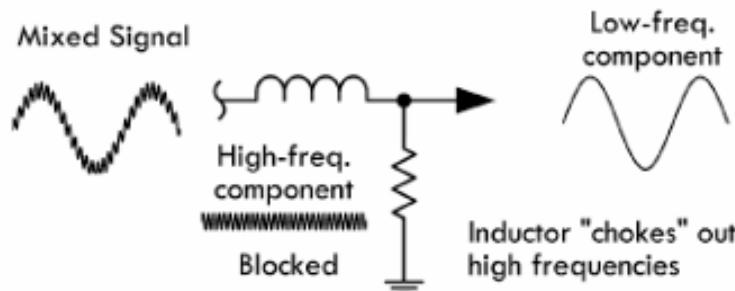
Air-Core Inductor



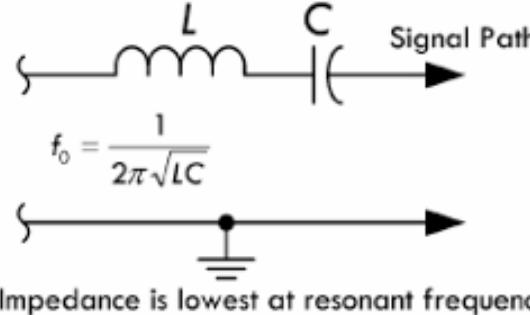
Water Analogy



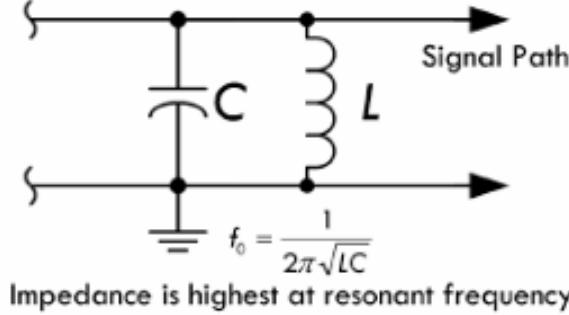
A. Low-Pass Filter - Coupling



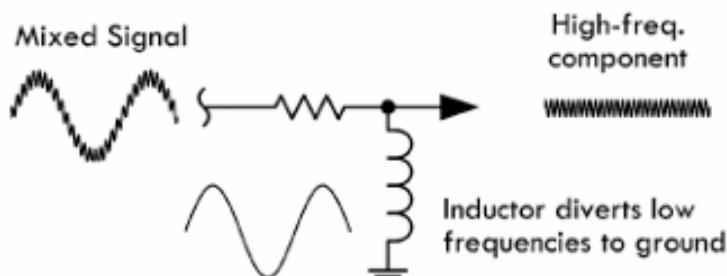
C. Series Resonant Circuit



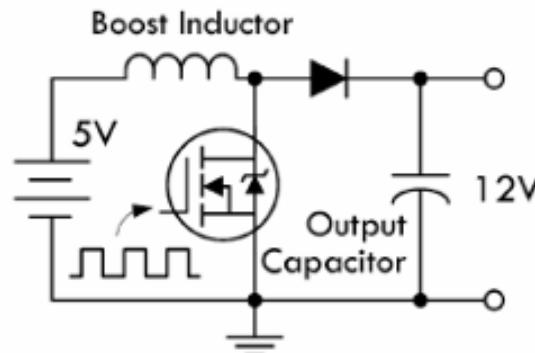
D. Parallel Resonant Circuit



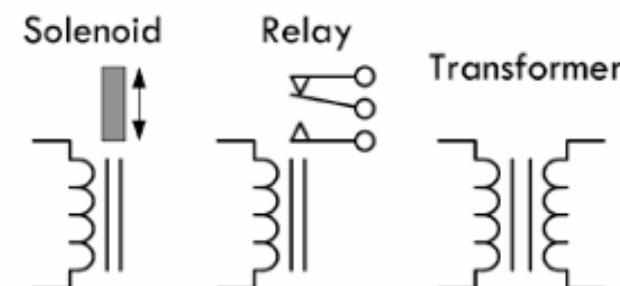
B. High-Pass Filter - Bypassing



E. Boost Converter



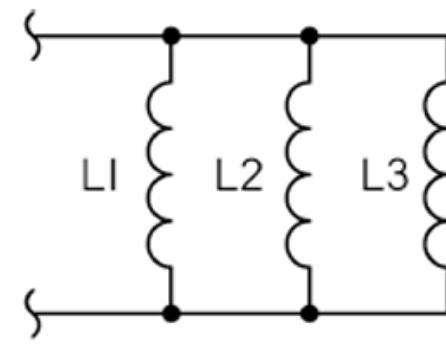
F. Miscellaneous



Inductors



Inductors in Series



Inductors in parallel