

November 2, 4:00 PM: Discussed our general project objectives and divided work among us.
November 2, 5:00 PM: Assembled existing modules from the lab.
November 2, 6:00 PM: Converted data memory and instruction memory to be byte addressable.

November 3, 12:30 PM: Datapath drawing Completed
November 3, 3:35 PM: Created branch control unit module
November 3, 5:30 PM: Completed branch control unit
November 3, 7:00 PM: Created control unit module
November 3, 9:45 PM: Completed control unit

November 4, 4:00PM: Completed top module connections (datapath)
November 4, 6:00PM: Created all test cases
November 4, 10:00PM: Tested all instructions apart from I-Type and R-Type

November 5, 4:30PM: Tested remaining instructions
November 5, 9:30PM: Created the test cases directory, and added all assembly and machine code