



Rajeev Kumar <rajeevkumar.rke@gmail.com>

Lab Hosting | ANALOG AND DIGITAL ELECTRONICS LAB-I

2 messages

Prateek Sharma <manager.iitdelhi@gmail.com>

Thu, Jun 13, 2024 at 3:54 PM

To: "Prof. RS Anand" <anandfee@iitr.ac.in>

Cc: Prof Ranjan Bose <rbose.iitd@gmail.com>, "Prof. Suresh Bhalla" <sbhalla@civil.iitd.ac.in>, Seshan Srirangarajan <seshan@ee.iitd.ac.in>, Rajeev IITR <rajeevkumar.rke@gmail.com>

Respected Sir,

Greetings for the Day! I hope my email finds you well!

The Virtual Labs team confirms that the Analog and Digital Electronics Lab-I developed under your supervision has been evaluated by the domain experts and is approved for hosting. The following experiments are ready for the hosting:

- | |
|--|
| 1. Familiarisation with general bread board |
| 2. Familiarization of ICs |
| 3. To verify De-Morgan's theorems |
| 4. 4 to 16 decoder using 3 to 8 decoder IC (74138) |
| 5. Full Subtractor using 4:1 MUX. |
| 6. To design a BCD to Decimal decoder |
| 7. Even/Odd Parity Generator. |
| 8. Generation of Clock Pulse using NAND gate |
| 9. Application of basic logic gates in fire and burglar alarms |

Request you to kindly initiate the hosting request in the GitHub and share the link.

Please let me know in case of any queries.

Thanks & Regards,

Prateek Sharma

Sr. Project Assistant | वरिष्ठ परियोजना सहायक

Virtual Labs (वर्चुअल लैब्स) | An Ministry of Education, Govt. of India initiative

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Prateek Sharma <manager.iitdelhi@gmail.com>

Thu, Jul 11, 2024 at 4:03 PM

To: "Prof. RS Anand" <anandfee@iitr.ac.in>

Cc: Prof Ranjan Bose <rbose.iitd@gmail.com>, "Prof. Suresh Bhalla" <sbhalla@civil.iitd.ac.in>, Seshan Srirangarajan <seshan@ee.iitd.ac.in>, Rajeev IITR <rajeevkumar.rke@gmail.com>

Respected Sir,

Greetings for the Day!

Please do the needful by Monday, 15 July 2024 so that the lab can be hosted.

Please let me know in case of any queries.

Thanks & Regards,

Prateek Sharma