

Comparison Rule: IED: Individual Special

Compare a specific IED (requires user modification)

Comparing IED[name="OHG_112_P1"] elements from OHG_2025_10_31_Issued_stage1 - Pre SEL iid import.scd to IED[name="OHG_112_P1"] elements from OHG_2025_10_31_Issued_stage1 - Post SEL iid import.scd

- Excluding elements: DAI[name="setSrcRef"], LN[lnClass="LGOS"], LN[lnClass="LSVS"], Private
- Excluding attributes: desc, LN0.lnType, LN.lnType, LNodeType.id, DO.type, SDO.type, DA.type, BDA.type, DAType.id, EnumType.id, FCDA.prefix, RptEnabled.max
- Excluding no namespaces

OHG_112_P1 IED: SEL-411L-2 Line Protection - TP version 1.0.0 (3)

E1 AccessPoint: Engineering Access (1)

S1 Server (1)

CFG LDevice: Data Sets, Control Blocks, and Supervision Logical Nodes (1)

CFG LN0 (3)

Ctl DataSet: Output Signals (2)

#19 FCDA: Annunciation > User ITrip Send1 (RWB: PSV29)

#20 FCDA: Annunciation > User ITrip Send1 (RWB: PSV29)

CFG Inputs (8)

http://www.selinc.com/2006/61850 svTimeRef OHG_112_MU1

VAY[0] ExtRef: VAY (Local Bus Or Line VT)

VAZ[0] ExtRef: VAZ (Alt Bus VT or Bus VT Phase A)

VB020|q=0x1807[0] ExtRef: Transformer Dif CB Fail Initiate (TX Feeder Or Tee Off Applications)

VB033[0] ExtRef: Ctl - Tx Dif1 GOOSE Quality (Tx Feeder Or Tee Off Application)

VBY[0] ExtRef: VBY (Local Bus Or Line VT)

VBZ[0] ExtRef: VBZ (Alt Bus VT or Opposing Cct Line VT Phase B)

VCY[0] ExtRef: VCY (Local Bus Or Line VT)

VCZ[0] ExtRef: VCZ (Alt Bus VT or Spare)

Ctl GSEControl: Trip, Close, CBF and ARecl signals (4)

#LN0_Root_Cmb -> #LN0_Root_Cmb@OHG_T1_P1#1@OHG_112_P1#1 LNodeType: Control of IEC 61850 Mode/Behavior - Enabled

LN LGOS 1 -> LGOS 2, LGOS 6, LGOS 7, LGOS 8, LGOS 10, LGOS 11, LGOS 12, LGOS 13, LGOS 14, LGOS 15, LGOS 16 LN: Ind - Line CB

LN LSVS 1 -> LSVS 3, LSVS 4, LSVS 5, LSVS 6, LSVS 7 LN: MSVCB11 - Line side CT/VT

P1 AccessPoint: Process Bus (1)

S1 AccessPoint: Station Bus (1)

