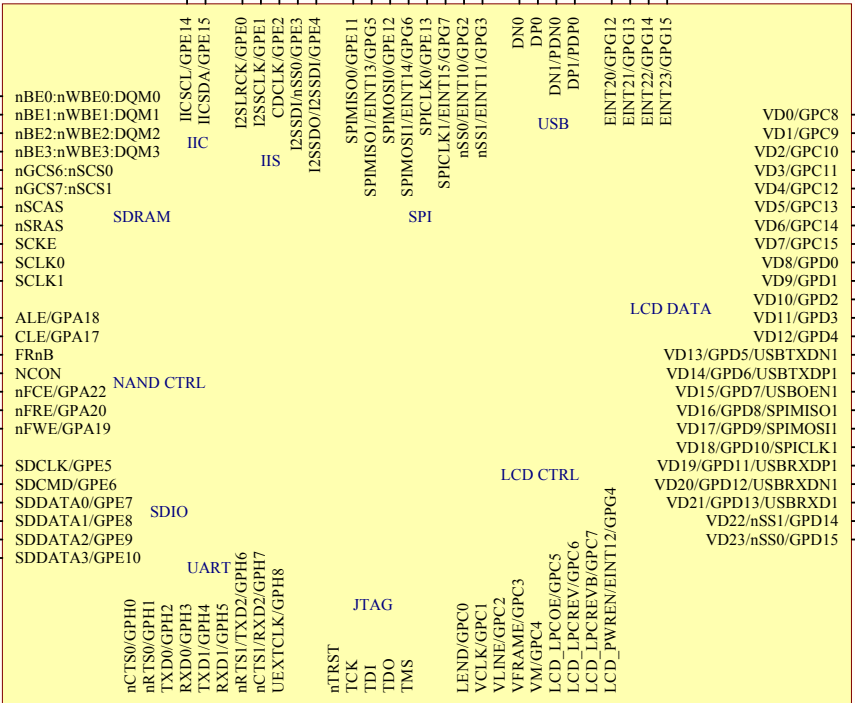
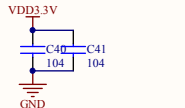
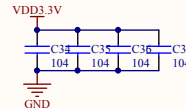
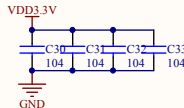


- LnWBE0 D4
- LnWBE1 B5
- LnWBE2 D5
- LnWBE3 E5
- nGCS6 D2
- nSCAS E3
- nSRAS C6
- SCKE A2
- SCLK0 B4
- SCLK1 B3
- ALE D1
- CLE F5
- FRnB G6
- NCON R12
- nFCE F4
- nFRE E1
- nFWE F3
- SDCLK N8
- SDCMD K8
- SDDATA0 R8
- SDDATA1 M8
- SDDATA2 P8
- SDDATA3 J9



U1B
S3C2440X

LCD PWREN



1

2

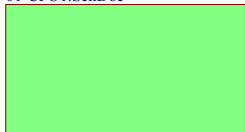
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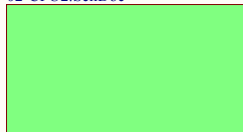
A

A

CPU1
01-CPU1.SchDoc



CPU2
02-CPU2.SchDoc



B

B

CPU3
03-CPU3.SchDoc



MEM
04-MEM.SchDoc



C

C

INTERFACE
05-INTERFACE.SchDoc



S3C2440A-
Creat : 2010/07/06

D

D

UP-TECH R&D @TM

1

2

3

4