

```
1 #include "led fun.h"
                                                 46
                                                             GPIOB->ODR |=0xlu1<<13;
 2 #include "main.h"
                                                 47
                                                            HAL Delay(300);
 3 #include <stdio.h>
                                                 48
                                                            GPIOB->ODR &=~(0xlul<<13);
                                                 49
                                                            GPIOC->ODR |=0xlul;
                                                 50
 6  void led change (int mode) {
                                                 51
                                                            HAL Delay(300);
 7
      switch (mode)
                                                            GPIOC->ODR &=~(0xlul);
                                                 52
 8 🗀
     {
                                                 53
 9
       case All led on:
                                                 54
                                                            GPIOC->ODR |=0xlul<<1;
         GPIOA->ODR |=0x603ul;
10
                                                 55
                                                            HAL Delay(300);
11
                                                 56
                                                            \overline{GPIOC} \rightarrow ODR &= \sim (0xlul << 1);
12
         GPIOB->ODR |=0x3u1<<12;
                                                 57
13
         GPIOC->ODR |=0x3ul;
                                                 58
14
          break;
                                                 59
       case All led off:
15
                                                 60
                                                            GPIOC->ODR |=0xlul<<1;
         GPIOA->ODR &=~0xfffful;
16
                                                            HAL Delay(300);
                                                 61
          GPIOB->ODR &=~0xfffful;
17
                                                 62
                                                            \overline{GPIOC} -> ODR &= \sim (0 \times lul << 1);
18
          GPIOC->ODR &=~0xfful;
                                                 63
19
         break;
                                                            GPIOC->ODR |=0xlul;
                                                 64
20
                                                 65
                                                            HAL Delay(300);
21
       case Bit_led_loop:
                                                            GPIOC->ODR &=~(0xlul);
                                                 66
          GPIOA->ODR &=~0xfful;
22
                                                 67
          GPIOB->ODR &=~0xfful;
23
                                                            GPIOB->ODR |=0xlu1<<13;
                                                 68
         GPIOC->ODR &=~0xfful;
24
                                                 69
                                                            HAL Delay(300);
25
                                                 70
                                                            GPIOB->ODR &=~(0x1u1<<13);
26
          GPIOA->ODR |=0xlul;
                                                 71
27
          HAL Delay(300);
                                                 72
                                                            GPIOB->ODR |=0xlu1<<12;
         GPIOA->ODR &=~(0xlul);
28
                                                 73
                                                            HAL Delay(300);
29
                                                 74
                                                            GPIOB->ODR &=~(0xlul<<12);
          GPIOA->ODR |=0xlu1<<1;
30
                                                 75
31
          HAL Delay(300);
                                                 76
                                                            GPIOA->ODR |=0xlul<<10;
32
          GPIOA \rightarrow ODR \&=\sim (0xlul <<1);
                                                 77
                                                            HAL_Delay(300);
33
                                                 78
                                                            GPIOA->ODR &=~(0xlu1<<10);
34
          GPIOA->ODR |=0xlu1<<9;
                                                 79
35
          HAL Delay(300);
                                                 80
                                                            GPIOA->ODR |=0xlu1<<9;
36
          GPIOA \rightarrow ODR \&=\sim (0xlul << 9);
                                                 81
                                                            HAL Delay(300);
37
                                                 82
                                                            GPIOA->ODR &=~ (0xlul << 9);
38
          GPIOA->ODR |=0xlu1<<10;
                                                 83
39
          HAL Delay(300);
                                                            GPIOA->ODR |=0xlul<<1;
                                                 84
40
          GPIOA->ODR &=~(0xlul << 10);
                                                 85
                                                            HAL Delay(300);
41
                                                 86
                                                            GPIOA->ODR &=~ (0xlul << 1);
42
          GPIOB->ODR |=0x1u1<<12;
                                                 87
          HAL Delay(300);
43
44
          GPIOB->ODR &=~(0xlul << 12);
                                                 88
                                                            GPIOA->ODR |=0xlul;
45
                                                 89
                                                            HAL Delay(300);
                                                 90
                                                              GPIOA->ODR &=~(0xlul);
                                                 91
                                                 92
                                                 93
                                                              break:
                                                 94
                                                         }
                                                 95 L}
```