XIAMEN OCULAR OPTICS CO.,LTD

SPECIFICATIONS OF LCD MODULE

PART NUMBER DATE

GDM1602B SERIES JULY 28, 1998

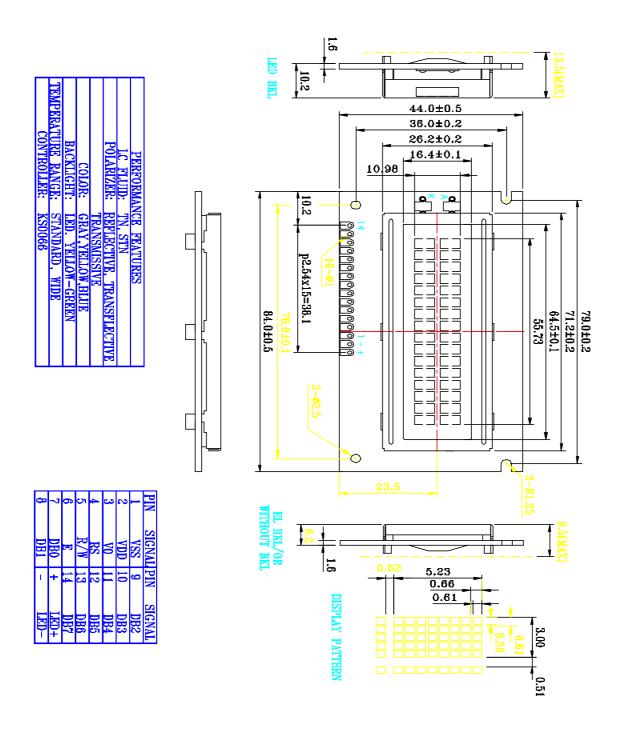
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CONTENTS

Mechanical Diagram	2
Absolute Maximum Ratings	3
Description of Terminals	3
Optical Characteristics	4
Electrical Characteristics DC Characteristics AC Characteristics	4 5
Write Cycle	5
Timing Characteristics	6
Block Diagram	7
Display Commands	8
Reliability and Lift Time	9
Standard CharacterPattern	10

Mechanical Diagram



Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit
Power Voltage	V _{DD} -V _{SS}	0	7.0	V
Input Voltage	Vin	Vss	V_{DD}	
Operating Temperature Range	T _{OP}	0	+50	
Storage Temperature Range	T _{ST}	-20	+60	

^{*}Wide Temperature range is available

(operating/storage temperature as wide as -20 +70/-30 +80.).

Description Of Terminals

Pin	Pin	Input/	External	Function
No.	Name	Output	Connection	
1	VSS	_	Power	VSS:GND
2	VDD	_	Supply	VDD: +5V
3	VO	_		V _{LCD} adjustment
				Register select signal
4	RS	INPUT	MPU	"0":Instruction register (when writing)
				Busy flag & address counter (When
				reading)
				"1":Data register (when writing & reading)
5	R/W	Input	MPU	Read/write select signal
				"0" for writing , "1" for reading
6	E	Input	MPU	Operation (data read/write) enable signal
				Low-order lines of data bus with 3-state,
7				bi-directional function for use in data
/	DB0-DB3	Input	MPU	transaction with the MPU. These lines
10				are
				not used when interfacing with a 4-bit
				microprocessor.
				High-order lines of data bus with 3-state,
11				bi-directional function for use in data
/	DB4-DB7	Input	MPU	transactions with the MPU. DB7 may also
14				be used to check the busy flag.
15	LED+		LED BACKLIGHT	LED + VOLTAGE TYPE:4.2V
/		Input	POWER SUPPLY	MAX : 4.5V
16	LED			LED: GND

Optical Characteristics

for TN Type Display Module (T_a=25., V_{DD}=5.0Vñ0.25V)

Item	Symbo	Conditio	Min.	Тур.	Max.	Unit
Viewing angle		C _r .4	-25			deg
	,		-30		30	
Contrast ratio	C _r		•	2		
Response time(rise)	T _r			120	150	ms
Response time(fall)	Tr		•	120	150	ms

for STN Type Display Module (T_a=25., V_{DD}=5.0Vn0.25V)

Item	Symbo I	Conditio n	Min.	Тур.	Max.	Unit
Viewing angle	,	C _r .2	-60		35	deg
			-40		40	
Contrast ratio	C _r			6		
Response time(rise)	T,			150	250	ms
Response time(fall)	T _r		٠	150	250	ms

Electrical Characteristics

DC Characteristics

Parameter	Symbol	Conditions	Min.	Type	Max.	Unit
Supply voltage for LCD	V_{DD} . V_{O}	T _A =25.		4.6		V
Input voltage	V_{DD}		4.7		5.5	٧
Supply current	I _{DD}	$V_{DD} = 5.0V; T_A = 25.$		1.5	2.5	mA
Input leakage current	I _{LKG}		_		1.0	. A
"H" level input voltage	V _{IH}		2.2		V _{DD}	V
"L" level input voltage	V _{IL}	Twice initial value	0	_	0.6	V
		or less				
"H" level output voltage	V _{oh}	LOH= -0.25MA	2.4			V
"L" level output voltage	V _{oL}	LOL=1.6MA	_	_	0.4	V
Backlight supply power	V _F			4.2	4.5	V

AC Characteristics

Read Cycle $(V_{DD}=5.0V+10\%, V_{SS}=OV, T_a=25.)$

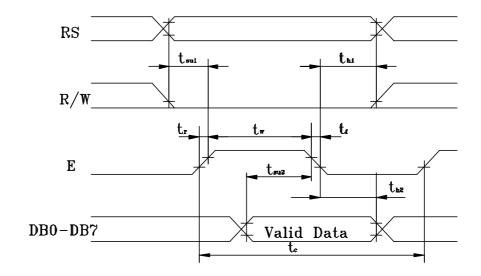
Parameter	Symbol	Test pin	Min.	Type	Max.	Unit
Enable cycle time	t _c	E	500			
Enable pulse width	t _w	E	300			
Enable rise/fall time	t _r , t _f	E			25	
RS,R/W setup time	t _{su}	RS; R/W	100			ns
RS.R/W address hold time	t _h	RS; R/W	10			
Read data output delay	t _D	DB0-DB7	60		190	
Read data hold time	t _{DH}	DB0-DB7	20	•	•	

Write Cycle

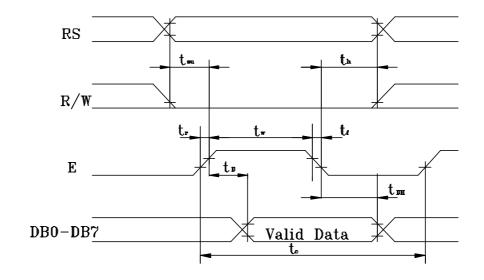
Parameter	Symbol	Test pin	Min.	Туре	Max.	Unit
Enable cycle time	t _c	E	500			
Enable pulse width	t _w	E	300			
Enable rise/fall time	t _r ,t _f	E			25	
RS,R/W setup time	t _{su1}	RS; R/W	100			ns
RS,R/W address hold time	t _{h1}	RS; R/W	10		•	
Data setup time	t su2	DB0-DB7	60			
Data hold time	t _{h2}	DB0-DB7	10			

Timing Characteristics

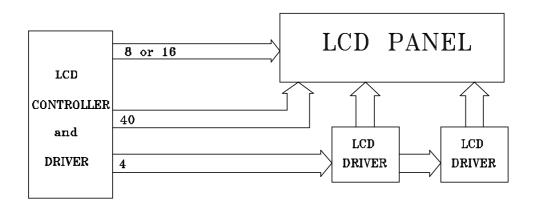
Write Timing

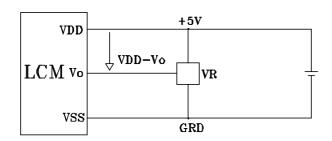


Read Timing



Block Diagram





VDD-Vo: LCD DRIVING VOLTAGE

VR: 10K-20K.

Display command

Parameter	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Note	Executing time fosc=250k hz
Clear Display	0	0	0	0	0	0	0	0	0	1		1.64ms
Cursor home	0	0	0	0	0	0	0	0	1	*		1.64ms
Entry Mode Set	0	0	0	0	0	0	0	1	1/D	S	DB1=1:Increment DB1=0:Decrement DB0=1:The display is shifted DB0=0:The display is not shifted	40.s
Display on/off	0	0	0	0	0	0	1	D	O	В	DB2=1:Display on DB2=0: Display off DB1=1:Cursor on DB1=0: Cursor off DB0=1:Brinking on DB0=0:Brinking off	40.s
Cursor / Display Shift	0	0	0	0	0	1	S/C	R/L	*	*	DB3=1:Shifts display one character DB2=1:Right shift DB2=0:Left shift	40.s
System Set	0	0	0	0	1	DL	N	F	*	*	DB4=1:8 bits DB4=0:4 bits DB3=1:2 lines display (1/16 duty) DB3=0:1 line display DB2=1:5.10 dots, 1/11 duty DB2=1:5.7 dots, 1/8 duty	40.s
Set CG RAM Address	0	0	0	1	corre	RAM espond or add		SS			The address length that can be set is 64 address	40.s
Set DD RAM Address	0	0	1	DD R	AM ad	dress					The address length that can be set is 80 address	40.s
Read Busy Flag/Address Counter	0	1	BF								DB7=1:Busy (instruction not accepted) DB7=0:Ready(for instruction)	40.s
Write Data	1	0	Write	e data								46.s
Read Data	1	1	Read	d data								46.s

DD RAM Address:

Address for line 1 Address for line 2

2	3 4	4 5	0	/	8	9	10	1 1	12	13	14 1	၁ I	0		
00	01	02	03	04	05	06	07	08	09	1A	1B	1C	1D	1E	1F
40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F

Reliability and Life Time

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8

1.Reliability Test

_			Evaluations	and Assessment*	
Storage Condition	Content	Current consumption	Oozing	Contrast	Other appearances
Operation at high temperature and humidity	40.,90% RH,240hrs	Twice initial value or less	none	More than 80% of initial value	No abnormality
High temperature storage	60., 240hrs	Twice initial value or less	none	More than 80% of initial value	No abnormality
Low temperature storage	-20., 240hrs	Twice initial value or less		More than 80% of initial value	No abnormality

^{*}Evaluations and assessment to be made two hours after returning to room temperature (25.ñ5.).

2. Liquid crystal panel service life

50,000 hours minimum at 25 nd 10.,45 nd 20% RH.

^{*}The LCDs subjected to the test must not have dew condensation.

Standard Character Pattern

Lower Bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)			Ø	a	P	*	F					9	Ę	œ	þ
xxxx0001	(2)			1	A	Q	ā	9				7	チ	4	Ų	D
xxxx0010	(3)			2	B	R	b	r			F	4	ij	х,	űL.	8
xxxx0011	(4)		#	3	C	5	C	S			J	ウ	Ţ	Ħ	Ŵ	20
xxxx0100	(5)		\$	4	D	T	d	t.			1	I	ŀ	t	1	Ω
xxxx0101	(6)		7.	(J)	E	Ш	e	u				才	+	1	Ю	ü
xxxx0110	(7)		&	6	F	Ų	f	Ų			7	力	_	3	2	Σ
xxxx0111	(8)		7	7	G	Ш	9	Ш			7	†	Z	う	ū	Д
xxxx1000	(1)		(8	H	X	h	X			4	7	末	IJ	" Γ	X
xxxx1001	(2))	9	I	Y	i	닐			÷	丁	Ļ	լե	-1	닠
xxxx1010	(3)		*		J	Z	j	Z			I		n	L	j	Ŧ
xxxx1011	(4)		+	7	K	Е	k	{			才	ţ	L	П	×	万
xxxx1100	(5)		7	<	L	¥	1				t	=)	7	7	4	F
xxxx1101	(6)				М]	M)			ュ	Z	ጎ		ŧ	-
xxxx1110	(7)			>	Н	^	n	→			3	t	市	4,74	ħ	
xxxx1111	(8)		<i>_</i>	?	0		0	÷			111	'n	₹		:O	

Note: The user can specify any pattern for character-generator RAM.