MaAL

Mov Instruction:

decluss-1 RJ. Sep Theory.

. Mor instruction to traffer data

· Belucer registers

· Between register and a memory location.

· Move a number directly to a register or a memory location

Syntax: Mor destination, source

Example: Mor Ax, WORD1

+ This reads "Move WORD1 to Ax"

The content of register Ax are replaced by the contents of memory location WORD1

I The U content of WORD are unchanged.

XCHG Instruction:

to exchange the > XCHG instruction contents of

· Two registees

Register and a memory Symlax: XCHq destination; Example: XCHq AH, a memory location destination, source

Add & SUB Instruction: + ADD instruction to add contents of · Two registers · A register and a memory Docation · A number to a register · A number to a memory Docation. Syntax: ADD destination, source Example: ADD WORDI, AX. instruction to subtract the of registers. + SUB · Two registers. · A register and a memory Doration. · A number from a memory Jocation. · A number from a memory Jocation. · Two Symbox: SUB destinations source Example: SUB AX, DX

INC & DEC & NEG:

INC (increment) instruction is used to add 1 to the contents of a register or memory location.

Syntax: INC dostination
Example: INC WORDS.
, DEC (decrement) instruction is used to
Subtract 1 from the contents of
Subtract 1 from the contents of a register or memory location
Syntax: DEC destination
Emple: DEC BYTE 1.
. Destination can be 8-bit or
16-bits wide.
The second secon
+ NEG instruction is used to
NEG instruction is used to negate the contents of
destination.
Replace the contents by its
2's complement
Syntax: NEG destination
Syntax: NEG destination Example: NEG BX.