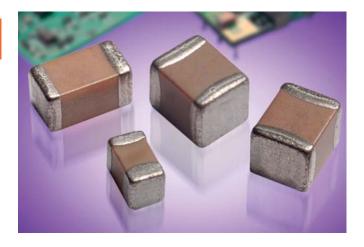
COG (NP0) Dielectric

General Specifications

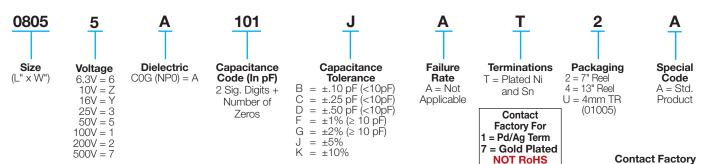


COG (NPO) is the most popular formulation of the "temperature-compensating," EIA Class I ceramic materials. Modern COG (NPO) formulations contain neodymium, samarium and other rare earth oxides.

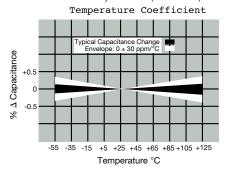
COG (NP0) ceramics offer one of the most stable capacitor dielectrics available. Capacitance change with temperature is 0 $\pm 30 \mathrm{ppm/^\circ C}$ which is less than $\pm 0.3\%$ $\Delta\mathrm{C}$ from -55°C to +125°C. Capacitance drift or hysteresis for COG (NP0) ceramics is negligible at less than $\pm 0.05\%$ versus up to $\pm 2\%$ for films. Typical capacitance change with life is less than $\pm 0.1\%$ for COG (NP0), one-fifth that shown by most other dielectrics. COG (NP0) formulations show no aging characteristics.

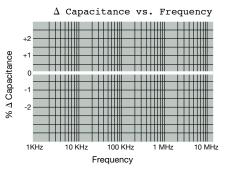
COMPLIANT

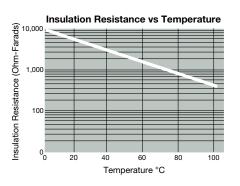
PART NUMBER (see page 2 for complete part number explanation)



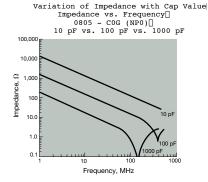
NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.

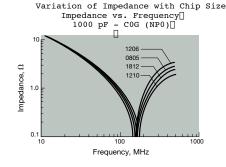


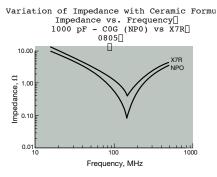




For Multiples







C0G (NP0) Dielectric



Specifications and Test Methods

	ter/Test	NP0 Specification Limits	Measuring Conditions		
Operating Temp		-55°C to +125°C	Temperature Cycle Chamber		
Capac	itance	Within specified tolerance	Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF		
Q		<30 pF: Q≥ 400+20 x Cap Value	1.0 kHz ± 10% for cap > 1000 pF		
<u> </u>		≥30 pF: Q≥ 1000	Voltage: 1.0Vrms ± .2V		
Insulation Resistance		100,000MΩ or 1000MΩ - μF,	Charge device with rated voltage for		
		whichever is less	60 ± 5 secs @ room temp/humidity Charge device with 300% of rated voltage for		
Dielectric Strength		No breakdown or visual defects	1-5 seconds, w/charge and discharge current limited to 50 mA (max)		
			Note: Charge device with 150% of rated voltage for 500V devices.		
Resistance to Flexure Stresses	Appearance	No defects	Deflection: 2mm		
	Capacitance Variation	±5% or ±.5 pF, whichever is greater	Test Time: 30 seconds 7 1mm/sec		
	Q	Meets Initial Values (As Above)	V V		
	Insulation Resistance	≥ Initial Value x 0.3	90 mm —		
Solder	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solder at 230 \pm 5°C for 5.0 \pm 0.5 seconds		
Resistance to Solder Heat	Appearance	No defects, <25% leaching of either end terminal			
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Dip device in eutectic solder at 260°C for 60		
	Q	Meets Initial Values (As Above)			
	Insulation Resistance	Meets Initial Values (As Above)			
	Dielectric Strength	Meets Initial Values (As Above)			
Thermal Shock	Appearance	No visual defects	Step 1: -55°C ± 2° 30 ± 3 minutes		
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp ≤ 3 minutes		
	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2° 30 ± 3 minutes		
Onook	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp ≤ 3 minutes		
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles and measure after 24 hours at room temperature		
Load Life	Appearance	No visual defects			
	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater	Charge device with twice rated voltage in		
	Q (C=Nominal Cap)	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	test chamber set at 125°C ± 2°C for 1000 hours (+48, -0). Remove from test chamber and stabilize at room temperature for 24 hours before measuring. Store in a test chamber set at 85°C ± 2°C/		
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)			
	Dielectric Strength	Meets Initial Values (As Above)			
Load Humidity	Appearance	No visual defects			
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater			
	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	$85\% \pm 5\%$ relative humidity for 1000 hours (+48, -0) with rated voltage applied.		
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber and stabilize at room temperature for 24 ± 2 hours		
	Dielectric Strength	Meets Initial Values (As Above)	before measuring.		

C0G (NP0) Dielectric

Capacitance Range

*EIA 01005

PREFERRED SIZES ARE SHADED

					m			
SIZE	0101*	0201	0402	0	0603	0805	1206	
Soldering	Reflow Only	Reflow Only	Reflow/Wave	Reflow/Wave Reflow/Wave		Reflow/Wave	Reflow/Wave	
Packaging	All Paper 0.40 ± 0.02	All Paper 0.60 ± 0.09	All Paper All Paper 1.00 ± 0.10 1.60 ± 0.15			Paper/Embossed 2.01 ± 0.20	Paper/Embossed 3,20 ± 0,20	
(L) Length mm (in.)	(0.016 ± 0.0008)	(0.024 ± 0.004)	(0.040 ± 0.004)	(0.06)	3 ± 0.006)	(0.079 ± 0.008)	(0.126 ± 0.008)	
(W) Width mm (in.)	0.20 ± 0.02 (0.008 ± 0.0008)	0.30 ± 0.09 (0.011 ± 0.004)	0.50 ± 0.10 (0.020 ± 0.004)	0.50 ± 0.10 0.81 ± 0.15 (0.020 ± 0.004) (0.032 ± 0.006)		1.25 ± 0.20 (0.049 ± 0.008)	1.60 ± 0.20 (0.063 ± 0.008)	
(t) Terminal mm	0.10 ± 0.04	0.15 ± 0.05	0.25 ± 0.15	0.15 0.35 ± 0.15		0.50 ± 0.25	0.50 ± 0.25	
(in.)	(0.004 ± 0.0016) 16	(0.006 ± 0.002) 25 50	(0.010 ± 0.006) 16 25 50		4 ± 0.006) 50 100 200	(0.020 ± 0.010) 16 25 50 100 200	(0.020 ± 0.010) 16 25 50 100 200 500	
Cap 0.5		А	C C C	G G	G G	J J J J J	J J J J J J	
(pF) 1.0 1.2	B B	A A		G G G	G G G			
1.5	В	A A		G G	G G	J J J J	JJJJJJ	
1.8 2.2	B B	A A		G G	G G G	JJJJJJ		
2.7	В	A A	C C C	G G	G G	J J J J J	JJJJJJ	
3.3 3.9	B B	A A		G G G	G G G			
4.7	В	A A	C C C	G G	G G	J J J J	JJJJJJ	
5.6 6.8	B B	A A		G G G	G G G	JJJJJ		
8.2	В	A A	C C C	G G	G G	J J J J J	JJJJJJ	
10 12	B B	A A		G G G	G G G			
15	В	A A	C C C	G G	G G G]]]]]	JJJJJJ	
18 22	B B	A A		G G G	G G G			
27	В	A A	C C C	G G	G G G	J J J J	JJJJJJ	
33 39	B B	A A		G G G	G G G			
47	В	A A	C C C	G G	G G G]]]]]	JJJJJJ	
56 68	B B	A A		G G G	G G G	JJJJJ		
82	В	A A	C C C	G G	G G G	J J J J J	JJJJJJ	
100 120	В	A A		G G G	G G G	J J J J J		
150			C C C	G G	G G G	J J J J	J J J J J	
180 220				G G G	G G G	JJJJJ		
270			<u> </u>	G G	G G	J J J J	J J J J M	
330 390				G G G	G G G		J J J J J M	
<u>470</u> 560			C C C	G G	G G	J J J J J	J J J J J M	
680				G G	G G			
820 1000			C C C	G G G	G G	J J J J J J	J J J J M	
1200				G G	G	J J J J J		
1500 1800				G G	G G	J J J J J J N	J J M Q Q	
2200				G G	G	N N N N	J J M P Q	
2700 3300				G G G	G	N N N N	J J M P Q J J M P Q	
3900				G G	G	P P P N	J J M P	
4700 5600				G G	G	P P P N	J J M P J J M P	
6800			_			PPP	M M M P	
8200 Cap 0.010	 		W			P P P	M M M P P P P P	
(μF) 0.012								
0.015 0.018	 							
0.022 0.027		4	t					
0.033	_							
0.039 0.047								
0.068				1				
0.082 0.1								
WVDC	16	25 50	16 25 50		50 100 200	16 25 50 100 200	16 25 50 100 200 500	
SIZE	0101*	0201	0402		0603	0805	1206	
Letter Max. (A B	C 2 0.56	E G 0.71 0.90	J 0.94	K M 1.02 1.27	N P Q 1.40 1.52 1.78	X Y Z 2.29 2.54 2.79	
	.013) (0.00		(0.028) (0.03		(0.040) (0.050)	(0.055) (0.060) (0.070)	(0.090) (0.100) (0.110)	
	PAPER BANK FANDOCCED WAS IN A COOK							

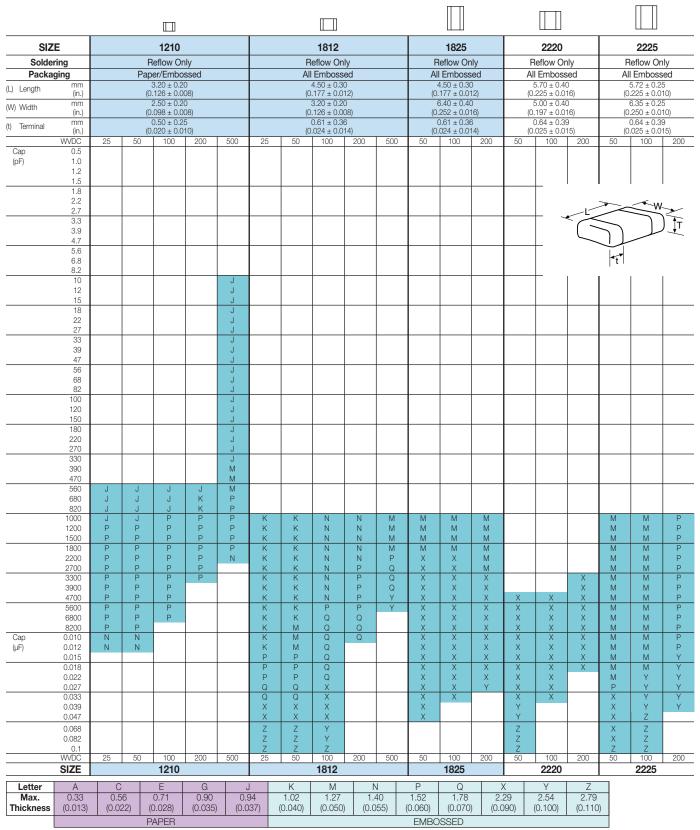
PAPER and EMBOSSED available for 01005

C0G (NP0) Dielectric



Capacitance Range

PREFERRED SIZES ARE SHADED



Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

AVX:

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08055A330KAT4A 08055A330MAT2A 08055A331FAT2A 08055A331FAT4A 08055A331GAT2A 08055A331JAT2A
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