

```
1  --CODE FOR BUTTERFLY
2  library IEEE;                                --IMPORTING LIBRARY
3  use IEEE.STD_LOGIC_1164.ALL;
4  library work;                                --USING FILES FROM WORK DIRECTORY
5  use work.dif_ifft_pkg.ALL;                  --USING PACKAGE DIT_IFFT_PKG FROM
6  WORK DIRECTORY
7  -----
8  entity butterfly is                          --ENTITY DECLARATION
9      port(
10         b1,b2 : in complex;                  --INPUTS OF BUTTERFLY STRUCTURE
11         w :in complex;                      --PHASE FACTOR
12         z1,z2 :out complex);                --OUTPUTS OF LIBRARY
13  end butterfly;
14  -----
15  architecture Behavioral of butterfly is      --ARCHITECTURE DECLARATION
16      signal z2_temp : complex;               --SIGNAL DECLARATION
17  begin
18      z1 <= add(b1,b2);                        --BUTTERFLY EQUATION FOR ADDITION
19      z2_temp <= sub(b1,b2);
20      z2 <= multi(z2_temp,w);                --BUTTERFLY EQUATION FOR
21      SUBTRACTION
22  end Behavioral;
```