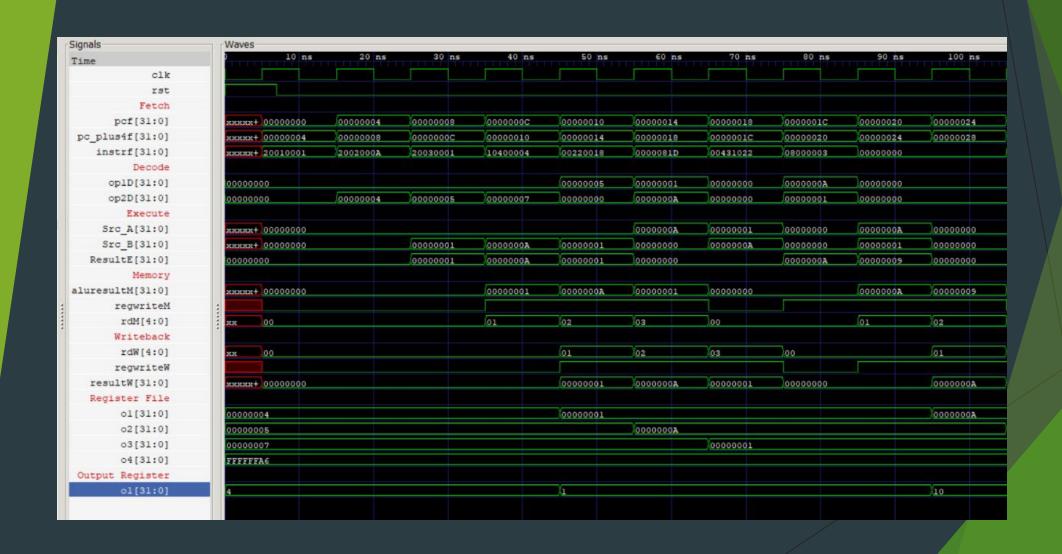
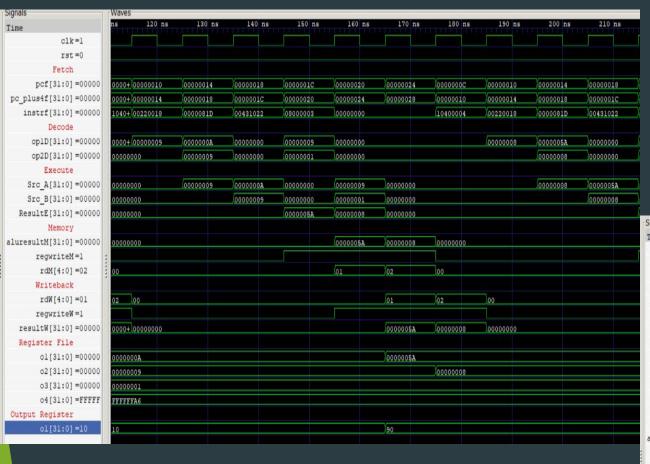
## **Factorial Analysis**

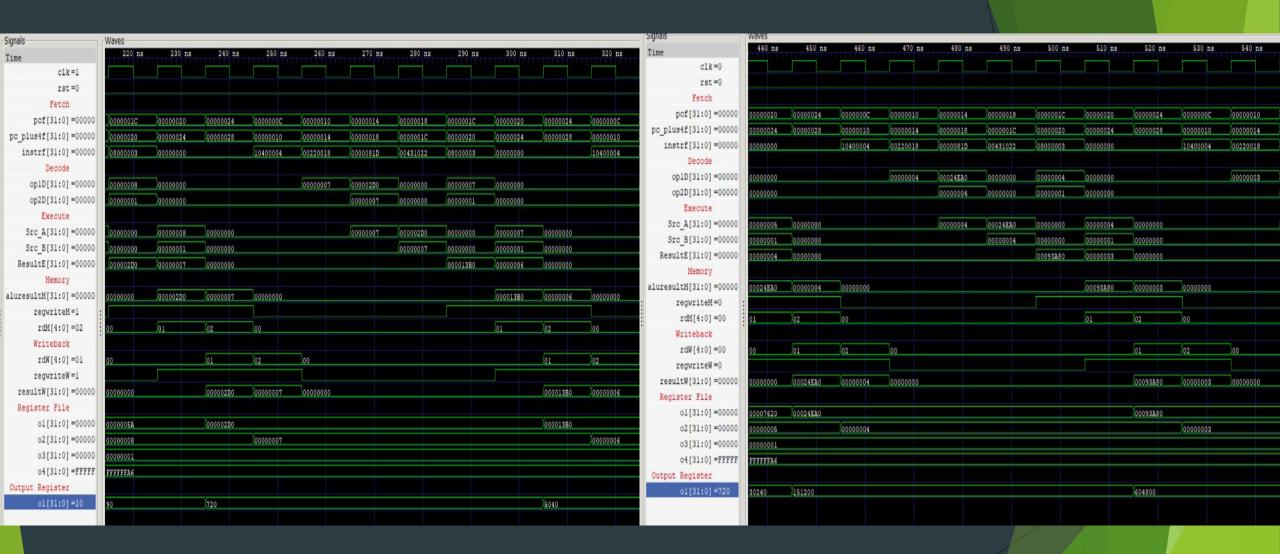




Signals	Waves	77 April 10	reacted to	(25-1411) I	VALUE AND DESCRIPTION OF THE PERSON OF THE P	W. 1940 C. 19	W	Victorian Co.	190411	- AMERICAN III		į
Time	660 ns	670 ns	680 ns	690 ns	700 ns	710 ns	720 ns	730 ns	740 ns	750 ns	760 ns	ı
clk=0												
rst =0												
Fetch												
pcf[31:0] =00000	00000024	00000000	00000010	00000014	00000018	0000001C	00000020	00000024	0000000C	00000010	00000014	ľ
pc_plus4f[31:0] =00000	00000028	00000010	00000014	00000018	0000001C	00000020	00000024	00000028	00000010	00000014	00000018	ľ
instrf[31:0] =00000	00000000	10400004	00220018	0000081D	00431022	08000003	00000000		10400004	00220018	0000081D	ı
Decode												
op1D[31:0] =00000	00000000		00000001	00375F00	00000000	00000001	00000000				00375F00	ı
op2D[31:0] =00000	00000000			00000001	00000000	00000001	00000000					
Execute												
Src_A[31:0] =000000	00000000			00000001	00375F00	00000000	00000001	00000000				
Src_B[31:0] =00000	00000000				00000001	00000000	00000001	00000000				
ResultE[31:0] =00000	00000000					00375F00	00000000				00000001	l
Memory												
aluresultM[31:0] =00000	00000001	00000000					00375F00	00000000				ı
regwriteM=0												
rdM[4:0] =00	02	00					/01	02	00			
Writeback												
rdW[4:0] =00	01	02	00					01	02	00		
regwriteW =0												
resultW[31:0] =00000	00375F00	00000001	00000000					00375F00	00000000			
Register File												
01[31:0] =00000	00375F00											
02[31:0]=00000	00000002	00000001							00000000			
03[31:0] =00000	00000001											
04[31:0] =FFFFF	FFFFFFA6											
Output Register												
o1[31:0] =720	3628800											l
												۲,

ignals	Waves	NAME OF TAXABLE PARTY.	100000000000000000000000000000000000000	TAX PRODUCT	ARAHAMAN	MI SAMUL		A STATE OF THE STA	CANADA P. P.		estatus 11
ime	550 ns	560 ns	570 ns	580 ns	590 ns	600 ns	€10 ns	620 ns	630 ns	640 ns	650 ns
clk=0											
rst=0											
Fetch											
pcf[31:0] =00000	00000014	00000018	0000001C	00000020	00000024	0000000C	00000010	00000014	00000018	0000001C	00000020
pc_plus4f[31:0] =00000	00000018	0000001C	00000020	00000024	00000028	00000010	00000014	00000018	0000001C	00000020	00000024
instrf[31:0] =00000	0000081D	00431022	08000003	00000000		10400004	00220018	0000081D	00431022	08000003	00000000
Decode											
op1D[31:0] =00000	00093A80	00000000	00000003	00000000			00000002	001BAF80	00000000	00000002	00000000
op2D[31:0] =00000	00000003	00000000	00000001	00000000				00000002	00000000	00000001	00000000
Execute											
Src_A[31:0] =00000	00000003	00093A80	00000000	00000003	00000000			00000002	001BAF80	00000000	00000002
Src_B[31:0] =00000	00000000	00000003	00000000	00000001	00000000				00000002	00000000	00000001
ResultE[31:0] =00000	00000000		001BAF80	00000002	00000000					00375F00	00000001
Memory											
luresultM[31:0] =00000	00000000			001BAF80	00000002	00000000					00375F00
regwriteM=0											
rdM[4:0] =00	00			01	02	00					01
Writeback											
rdW[4:0] =00	00				01	02	00				
regwriteW=0											
resultW[31:0] =00000	00000000				001BAF80	00000002	00000000				
Register File											
01[31:0] =00000	00093A80				001BAF80	(0)					
02[31:0]=00000	00000003					00000002					
03[31:0] =00000	00000001										
04[31:0] =FFFFF	FFFFFFA6										
Output Register											
01[31:0] =720	604800				1814400						

Signals	Waves										
Time	330 ns	340 ns	350 ns	360 ns	370 ns	380 ns	390 ns	400 ns	410 ns	420 ns	430 ns
clk=0											
rst=0											
Fetch											
pcf[31:0] =00000	00000010	00000014	00000018	0000001C	00000020	00000024	0000000C	00000010	00000014	00000018	0000001C
pc_plus4f[31:0] =00000	00000014	00000018	0000001C	00000020	00000024	00000028	00000010	00000014	00000018	0000001C	00000020
instrf[31:0] =00000	00220018	0000081D	00431022	08000003	00000000		10400004	00220018	0000081D	00431022	08000003
Decode											
op1D[31:0] =00000	0000000€	000013B0	00000000	0000000€	00000000			00000005	00007€20	00000000	00000005
op2D[31:0] =00000	00000000	0000000€	00000000	00000001	00000000				00000005	00000000	00000001
Execute											
Src_A[31:0] =00000	00000000	00000006	000013B0	00000000	00000006	00000000			00000005	00007620	00000000
Src_B[31:0] =00000	00000000		00000006	00000000	00000001	00000000				00000005	00000000
ResultE[31:0] =00000	00000000			00007620	00000005	00000000					00024EA0
Memory											
aluresultM[31:0] =00000	00000000				00007620	00000005	00000000				
regwriteM=0											
rdM[4:0] =00	00				01	02	00				
Writeback											
rdW[4:0] =00	00					01	02	00			
regwriteW =0											
resultW[31:0] =00000	00000000					00007620	00000005	00000000			
Register File											
01[31:0] =00000	000013B0					00007620					
02[31:0]=00000	00000006						00000005				
03[31:0] =00000	00000001										
04[31:0] =FFFFF	FFFFFFAG										
Output Register											
o1[31:0] =720	5040					30240					



## THANK YOU