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Signature :

Name : M.Y. Supervisor
Date : April 28, 2017

Signature :

Name : My Other Supervisor
Date : April 28, 2017

Signature :

Name : And Another One
Date : April 28, 2017

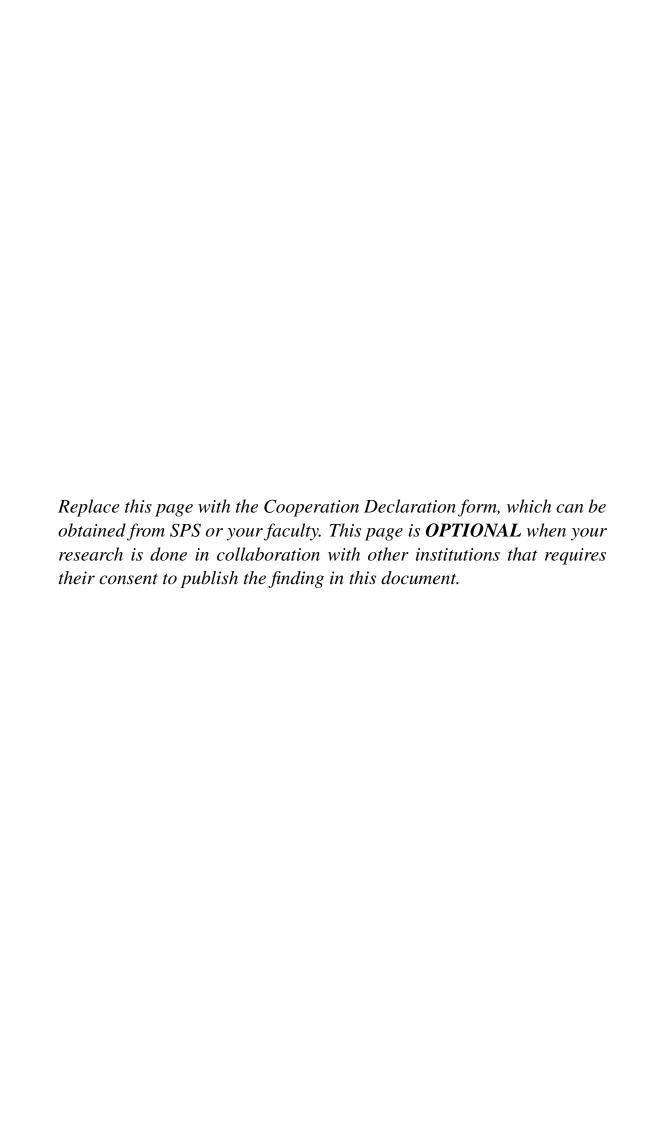
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A thesis submitted in fulfilment of the requirements for the award of the degree of The Degree

Faculty of Electrical Engineering Universiti Teknologi Malaysia

APRIL 2017

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Dedication

ACKNOWLEDGEMENT

Acknowledgement

ABSTRACT

This is the English abstract

ABSTRAK

Ini adalah abstrak Bahasa Melayu

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LIST OF ABBREVIATIONS

XML - Extensible Markup Language

LIST OF SYMBOLS

 γ - Whatever

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Introduction to the thesis [1] to the thesis [2].

- 1.2 State-of-the-Arts
- 1.3 Problem Statement
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- 1.5 Organization

LITERATURE REVIEW

2.1 State-of-the-Arts

2.2 Limitations

- 1. Mentor Graphics 2
 - (a) item 3
- 2. item 4

2.3 Research Gaps

The processing at layer-5¹ is done ...

¹In this thesis, OSI model is used.

RESEARCH METHODOLOGY

| 3.1 Top-level View |
|--------------------|
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- **5.1** Research Outcomes
- **5.2** Contributions to Knowledge
- **5.3** Future Works

REFERENCES

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APPENDIX A

MATHEMATICAL PROOFS

APPENDIX B

PSEUDO-CODES

APPENDIX C

TIME-SERIES RESULTS