

テンソルネットワーク法を用いた 量子計算のシミュレーション

白川知功

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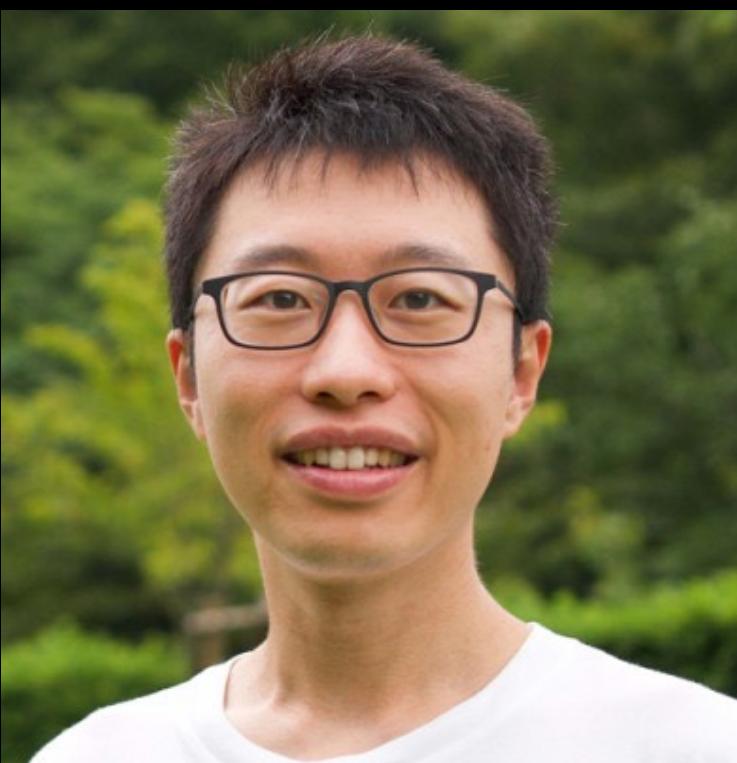
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理化学研究所・数理創造プログラム

理化学研究所・計算科学研究センター・量子HPC連携プラットフォーム部門・量子計算シミュレーション技術開発ユニット

Collaborators



Rong-Yang Sun^{1,2,3}
(2021~)



Hidehiko Kohshiro¹
(2023~, 幸城秀彦)



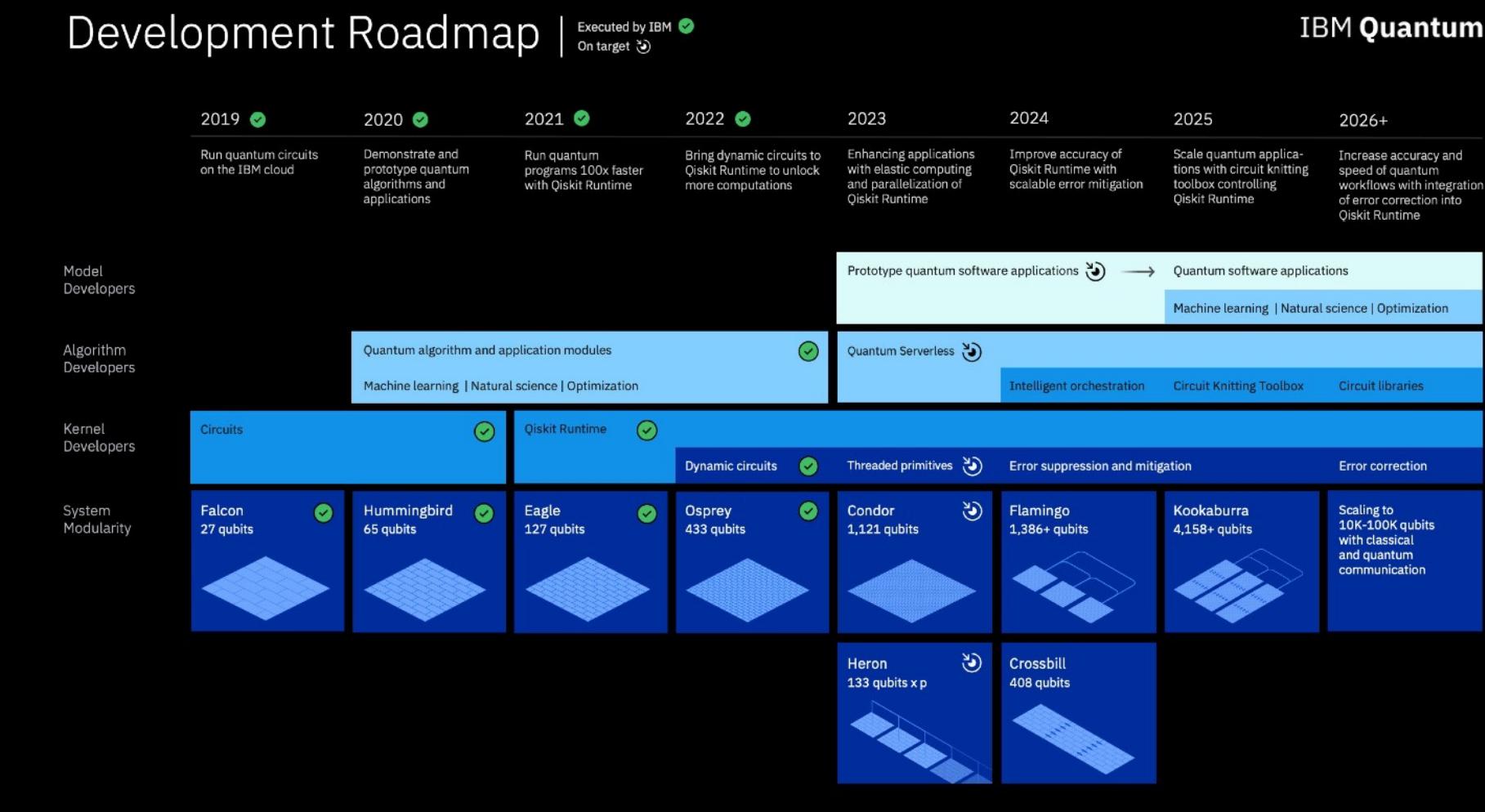
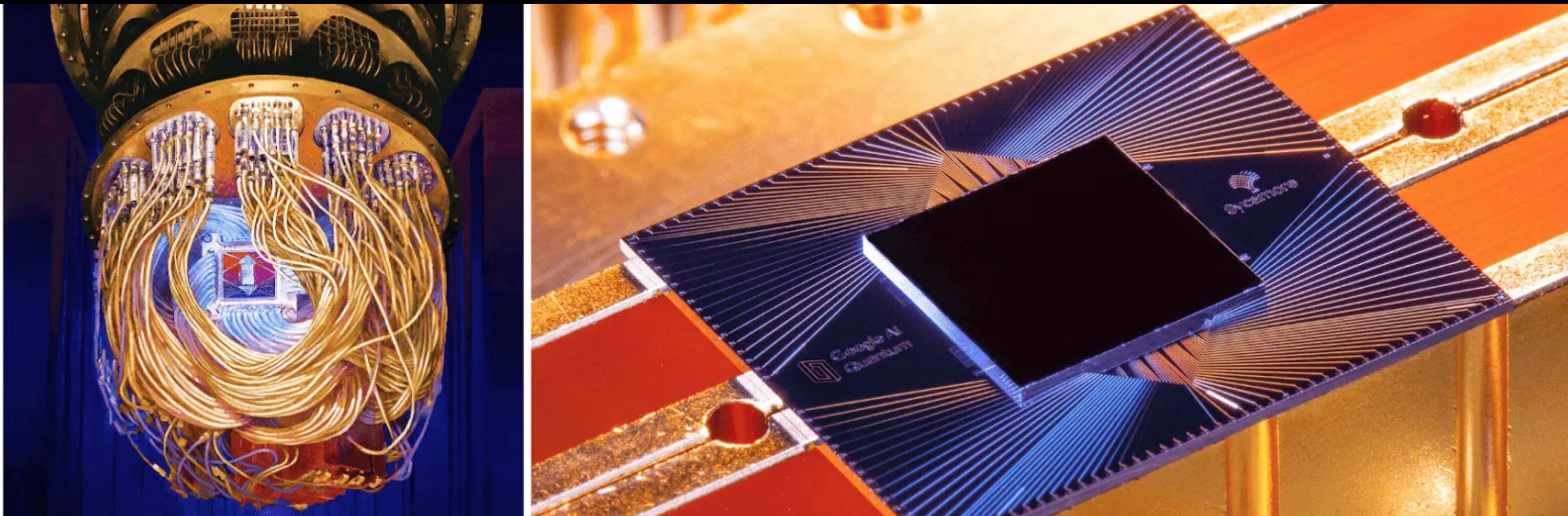
Seiji Yunoki^{1,2,3,4,5}
(柚木清司)

¹Computational Materials Science Research Team, RIKEN R-CCS, Kobe, Hyogo

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[R.-Y. Sun, T. Shirakawa, S. Yunoki, arXiv:2312.02667]

Near-term quantum devices

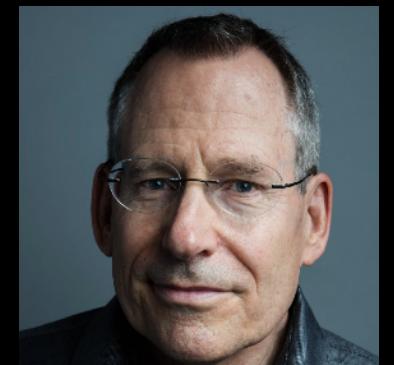


- Noisy intermediate-scale quantum (NISQ) era
 - A few $\mathcal{O}(10^2 \sim 10^3)$ qubits **without** error correction
 - A few $\mathcal{O}(10^1 \sim 10^2)$ depths circuit evolution

Quantum Computing in the NISQ era and beyond

John Preskill

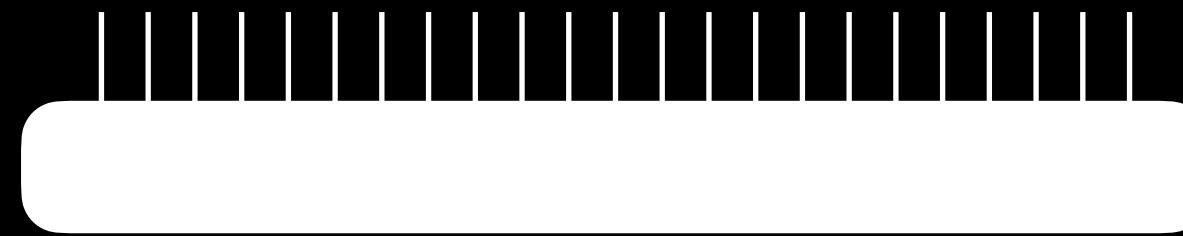
Institute for Quantum Information and Matter and Walter Burke Institute for Theoretical Physics,
California Institute of Technology, Pasadena CA 91125, USA
30 July 2018



Near-term aim: achieve **useful quantum advantage on NISQ devices**

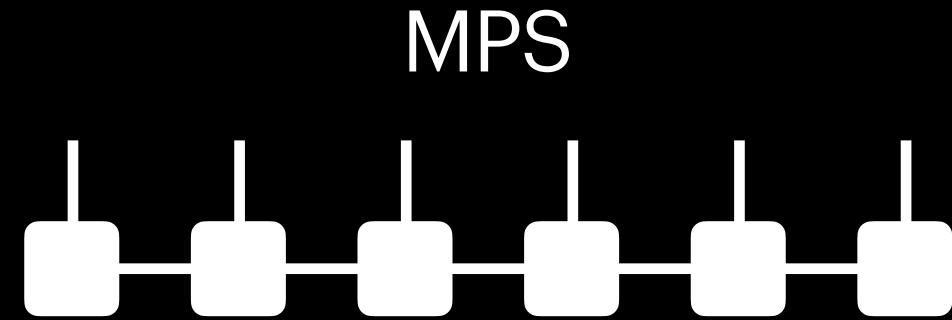
Simulators for quantum computing

State vector simulator

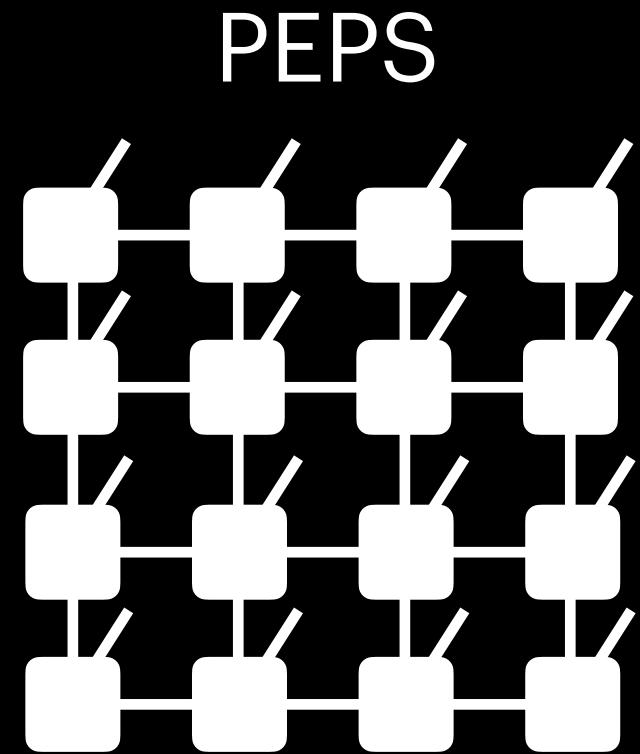


Can compute any quantum circuits
Limitation on number of qubits

Tensor network simulator



Can compute quantum circuits with large qubits
Limitation on entanglements

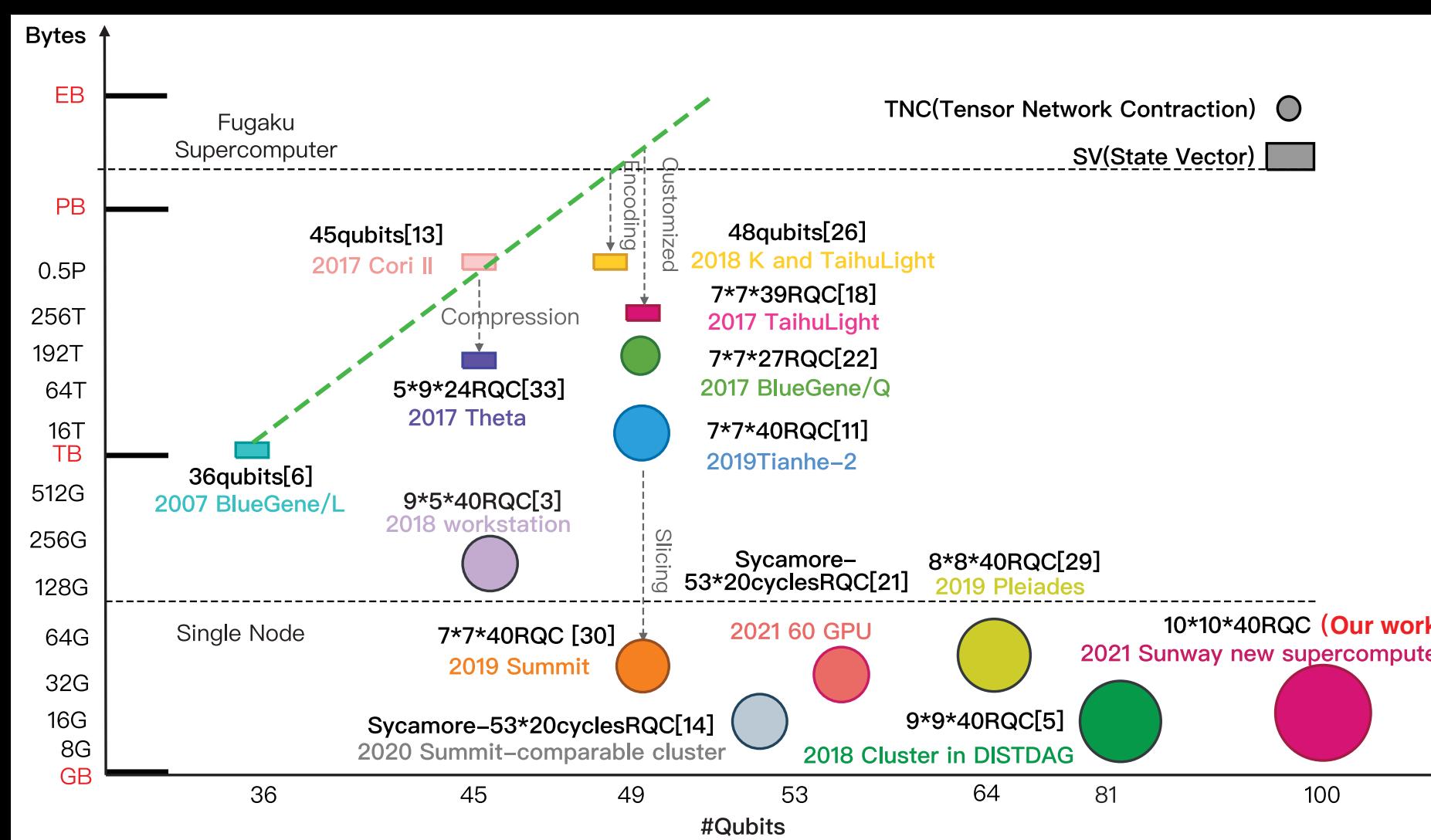


Why we need the simulator of quantum computer?

- (1) To check the validity of the quantum algorithm assuming that the quantum computer has worked correctly.
In order to explore the useful applications of quantum computers, it is necessary to check the results of quantum computers when they work properly.
- (2) To verify that the quantum computer is working properly
Current quantum computing devices are noisy and have no error correction, so they must be evaluated against correct operation.
- (3) To bridge the classical information and quantum information
For realization of scalable state preparation, hybrid tensor network [Xiao Yuan et al., Phys. Rev. Lett. **127**, 040501 (2021)], etc.

Tensor network simulators

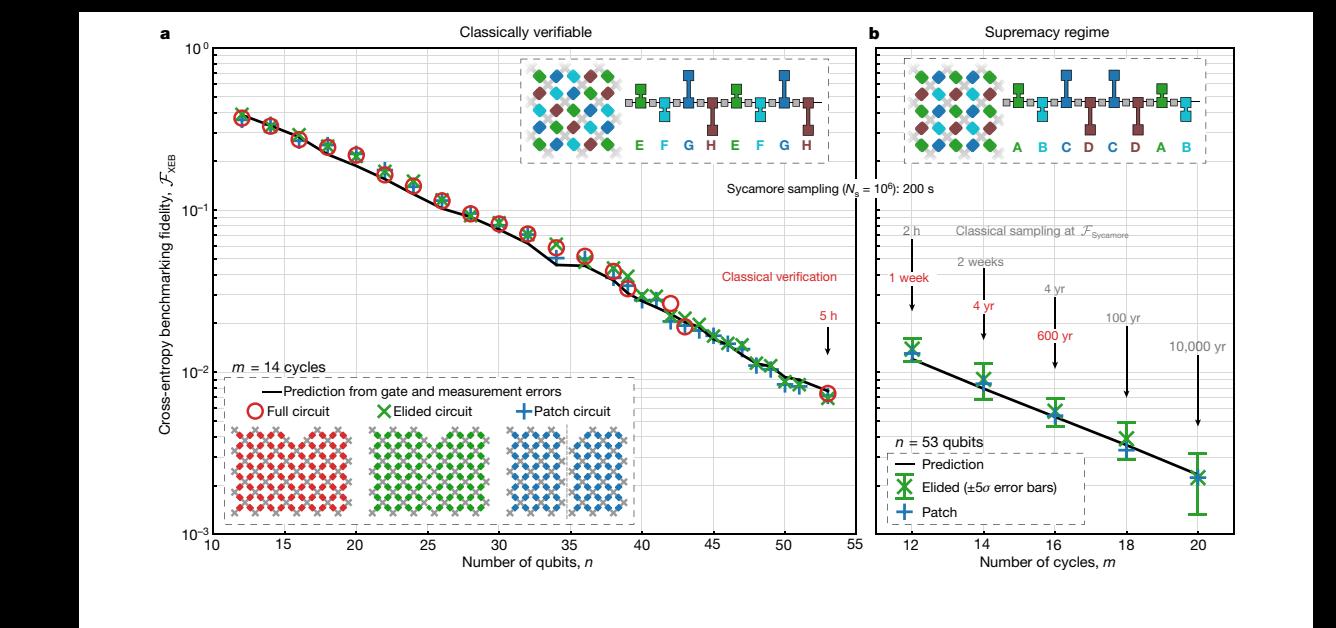
2021 ACM Gordon Bell Prize



- optimal slicing scheme
- three-level parallelization scales to about 42 million cores
- fused permutation and multiplication design for tensor contraction
- mixed-precision scheme

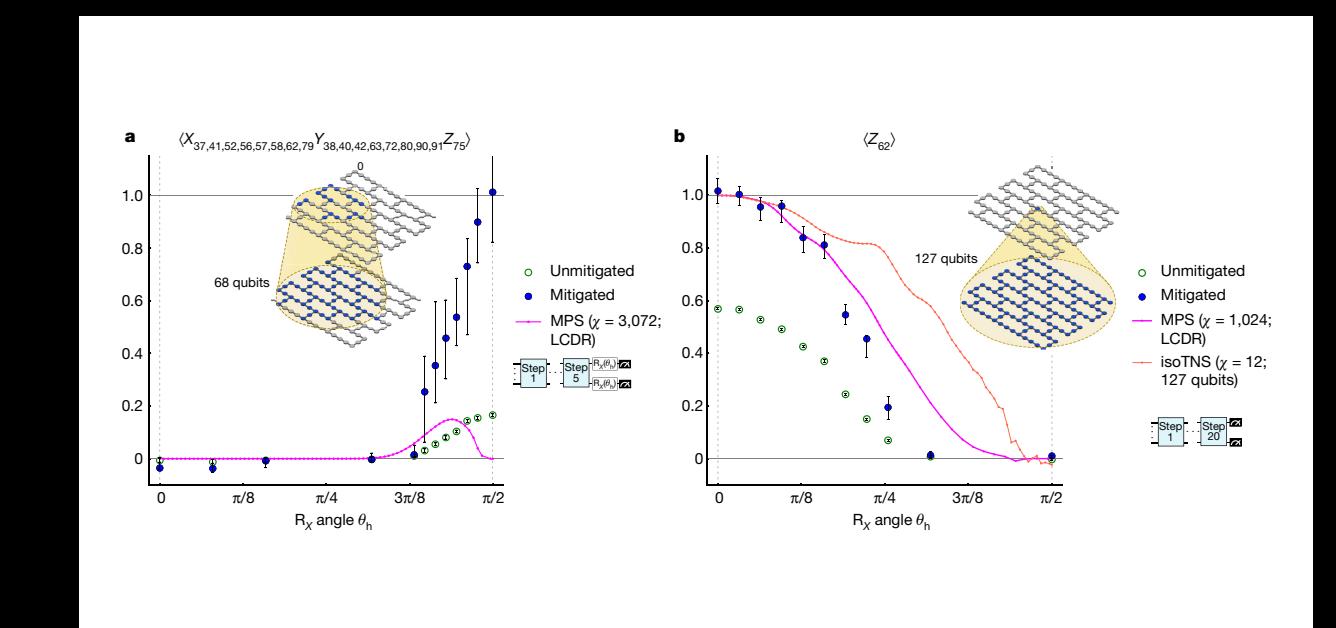
Performance comparison with real quantum devices

[Google, Nature **574**, 505-510 (2019)]



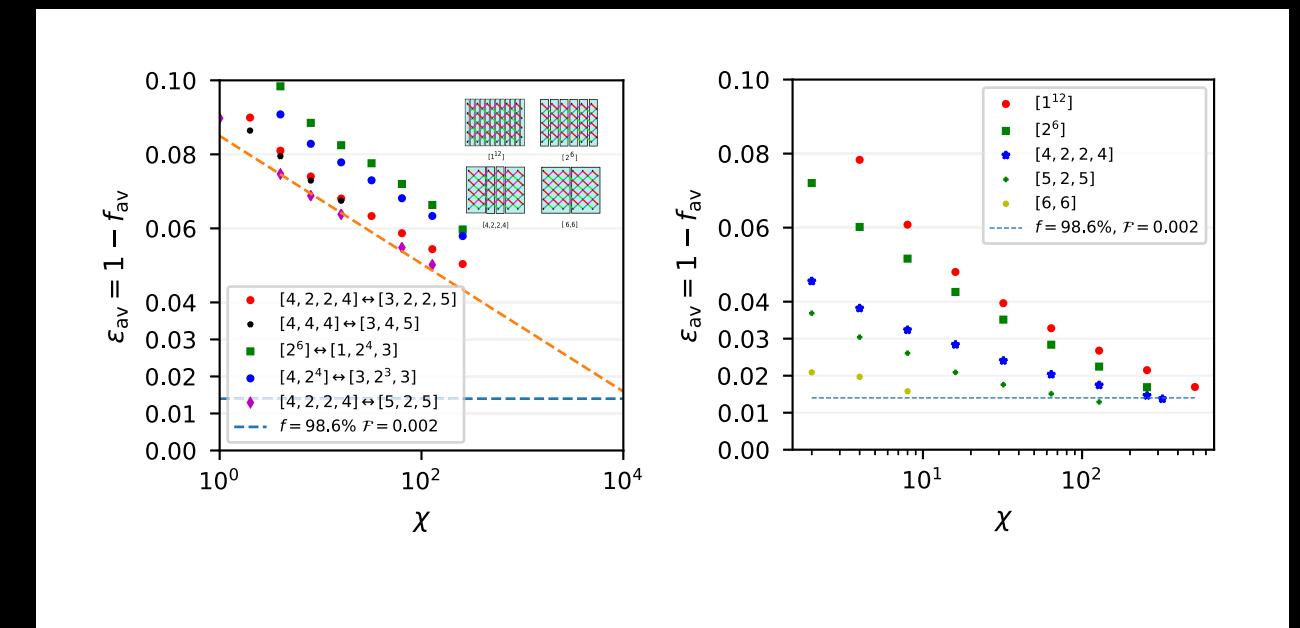
Real-device experiment
for random circuits

[IBM, Nature **618**, 500 (2023)]



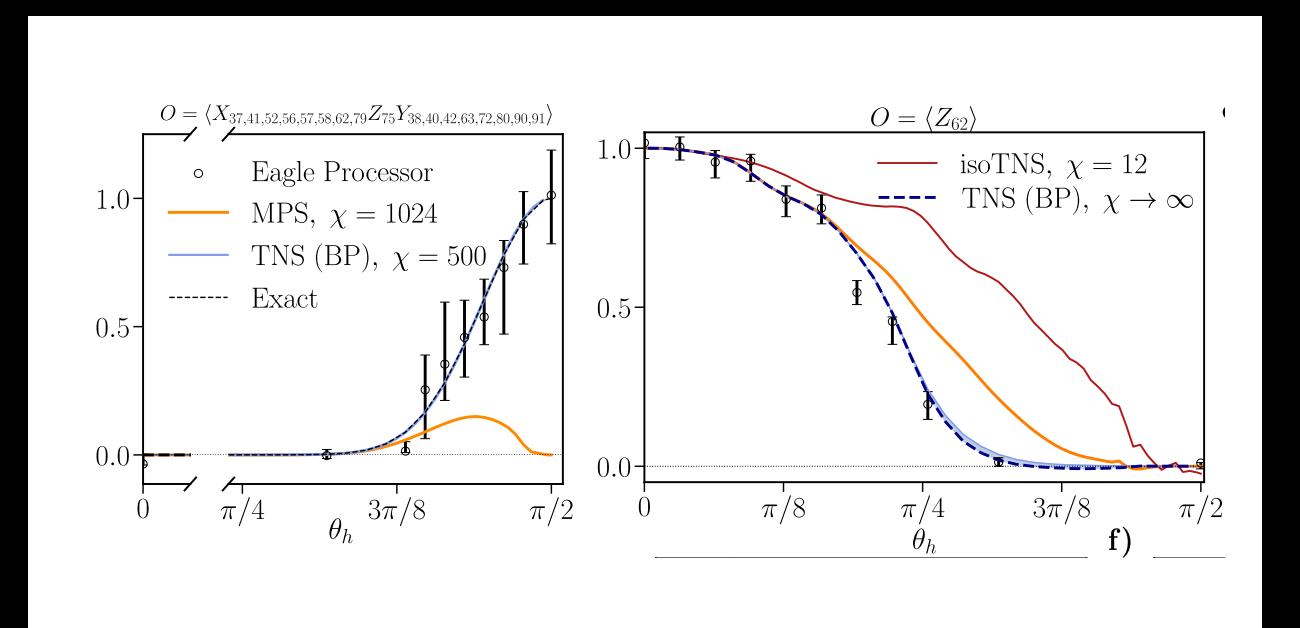
Real-device experiment
for quench dynamics

[Zhou et al, PRX **10**, 041038 (2020)]



Tensor network simulation

[Tindall et al., arXiv:2306.14887]

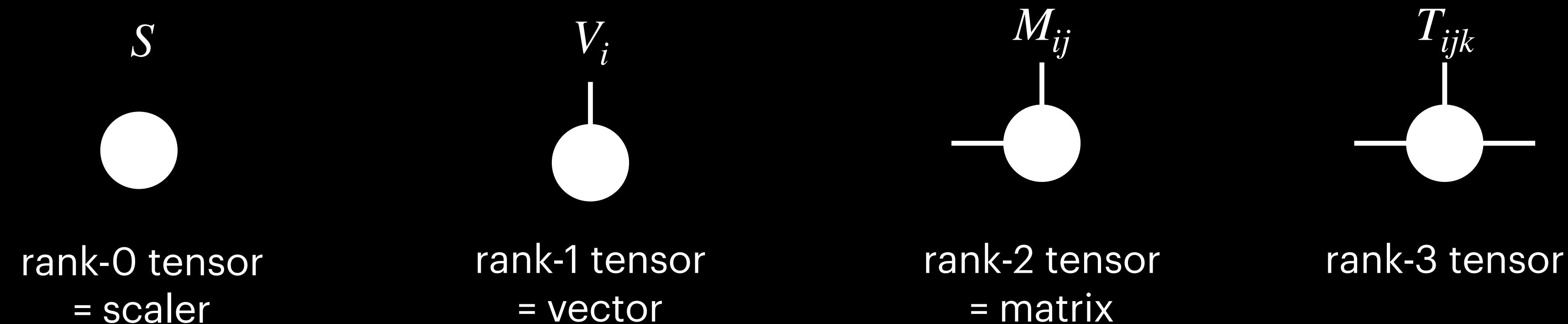


Tensor network simulation
(using Belief propagation technique)

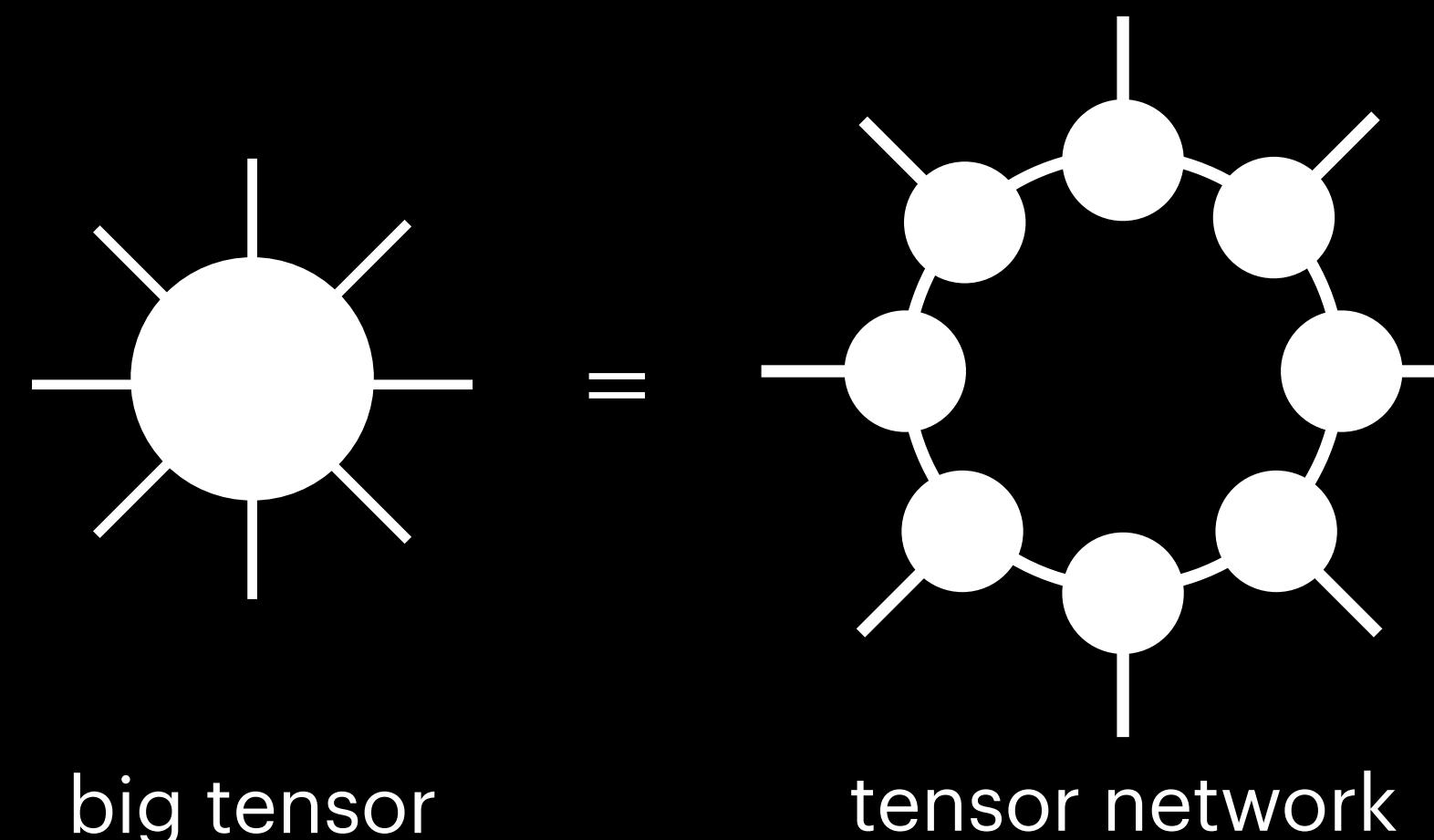
No one knows the limit of performance.

Tensor & Tensor network

rank- n tensor: n -dimensional array



tensor network: a decomposition of a **big** tensor

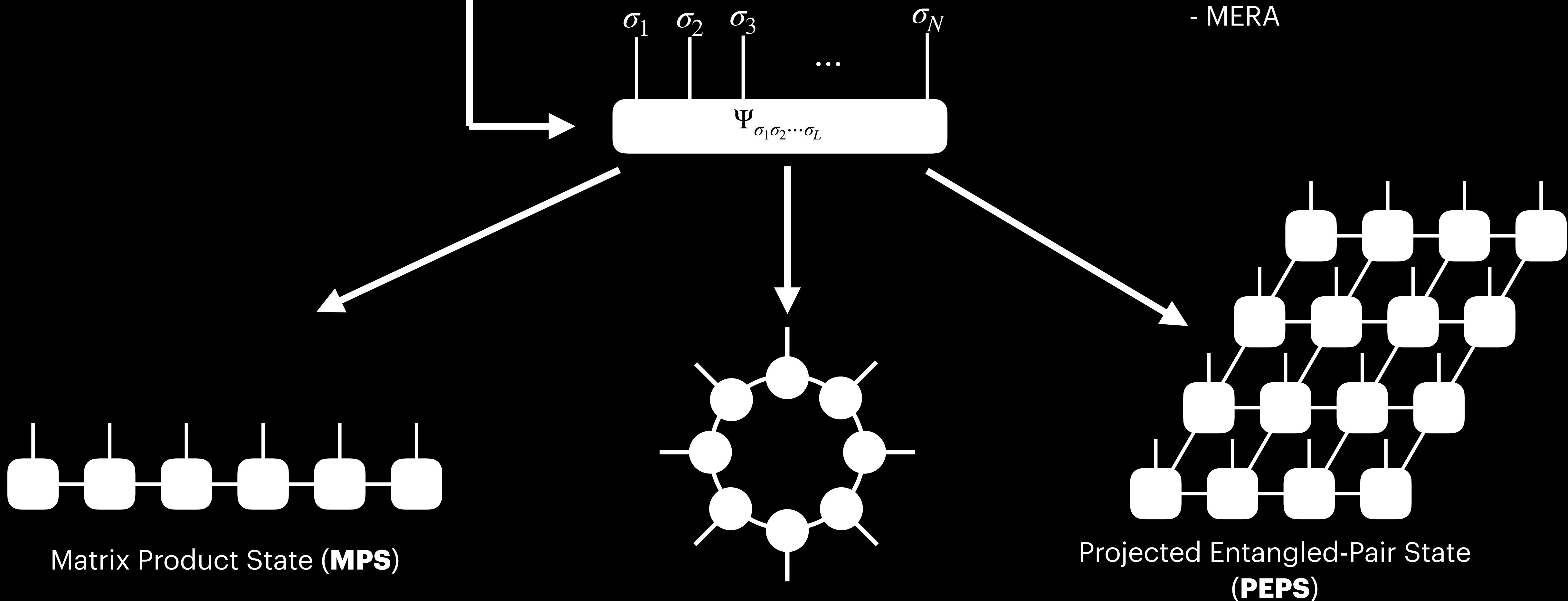


Contraction rule

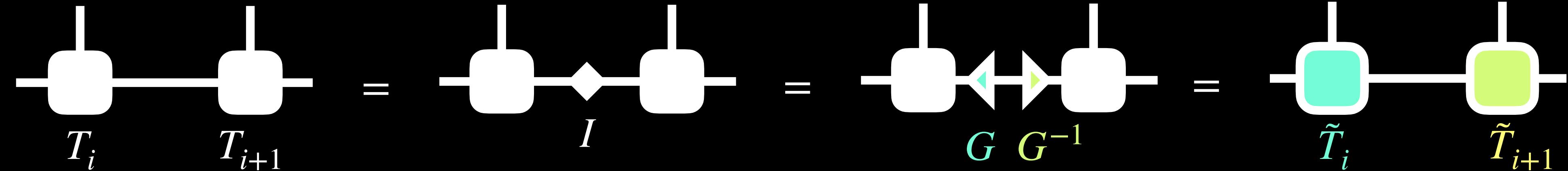
$$\begin{aligned}
 & \text{---} \square \text{---} = v u = \sum_i v_i u_i = \square \\
 & \text{---} \square \text{---} = A u = \sum_i A_{ij} u_j = \text{---} \square \text{---} \\
 & \text{---} \square \text{---} = A B = \sum_i A_{ij} B_{jk} = \text{---} \square \text{---}
 \end{aligned}$$

Tensor network state

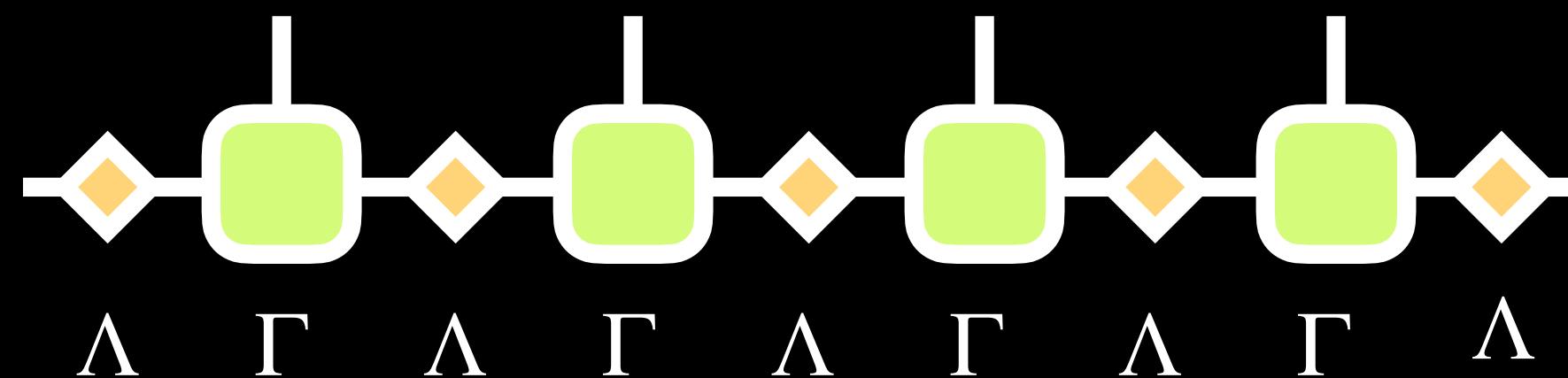
$$|\Psi\rangle = \sum_{\sigma_1=0}^{d-1} \sum_{\sigma_2=0}^{d-1} \cdots \sum_{\sigma_N=0}^{d-1} \underbrace{\psi_{\sigma_1\sigma_2\cdots\sigma_N}}_{\Psi_{\sigma_1\sigma_2\cdots\sigma_L}} |\sigma_1\sigma_2\cdots\sigma_N\rangle$$



Gauge degree of freedom and canonical form



Canonical form (Vidal form) of MPS [Vidal, PRL 2003, Cirac et al., KMP, 2021]



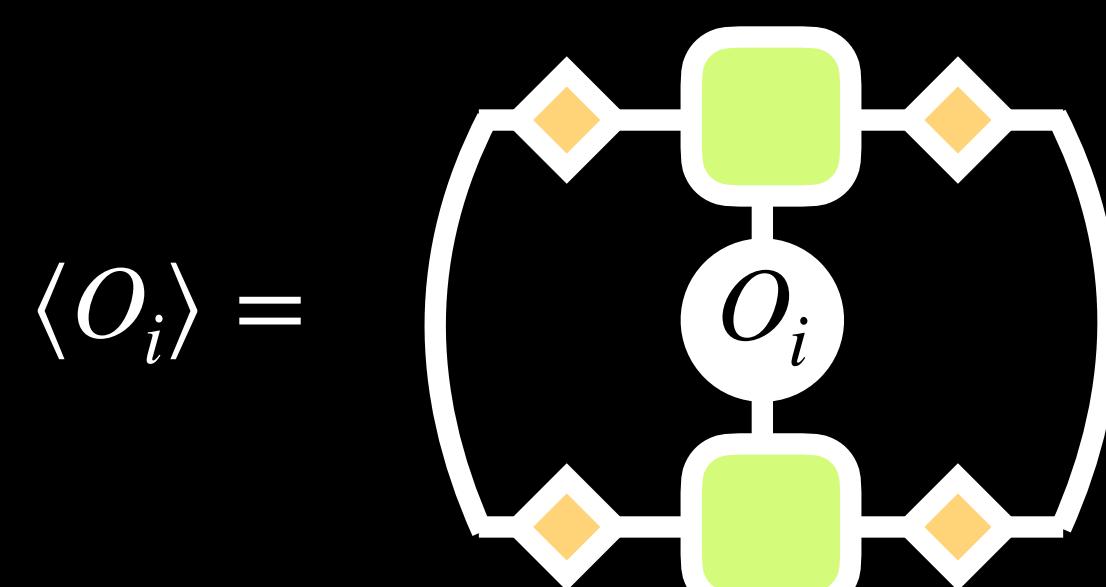
$$A = \Lambda \Gamma \rightarrow A^\dagger A = I$$

$$B = \Gamma \Lambda \rightarrow B B^\dagger = I$$

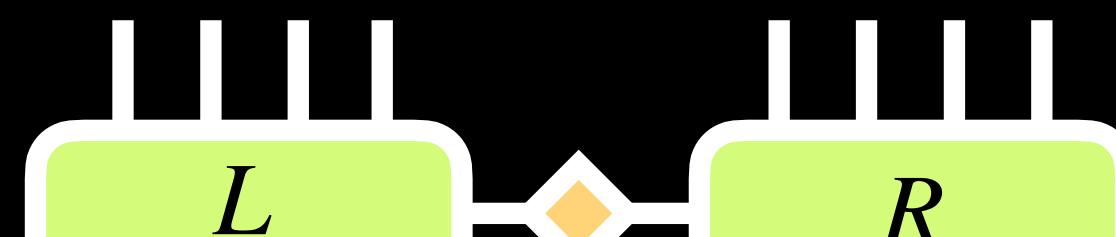
$$\Lambda = \text{diag}(\lambda_1, \lambda_2, \dots, \lambda_\chi)$$

$$\lambda_1 \geq \lambda_2 \geq \dots \geq \lambda_\chi$$

local observable



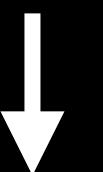
entanglement



$$S = - \sum_{\alpha} \lambda_{\alpha}^2 \ln \lambda_{\alpha}^2$$

best truncation

$$\Lambda = \text{diag}(\lambda_1, \dots, \lambda_{\tilde{\chi}}, \lambda_{\tilde{\chi}+1}, \dots, \lambda_\chi)$$



$$\tilde{\Lambda} = \text{diag}(\lambda_1, \dots, \lambda_{\tilde{\chi}}, \cancel{\lambda_{\tilde{\chi}+1}}, \dots, \cancel{\lambda_\chi})$$

MPS algorithms for quantum many-body physics

Ground state method

Density-matrix renormalization group method (DMRG) [White 1992]

Real-time dynamics

Time evolving block decimation (TEBD) [Vidal 2003]

Time-dependent variational principle (TDVP) [Haegeman et al., 2011]

Finite temperature

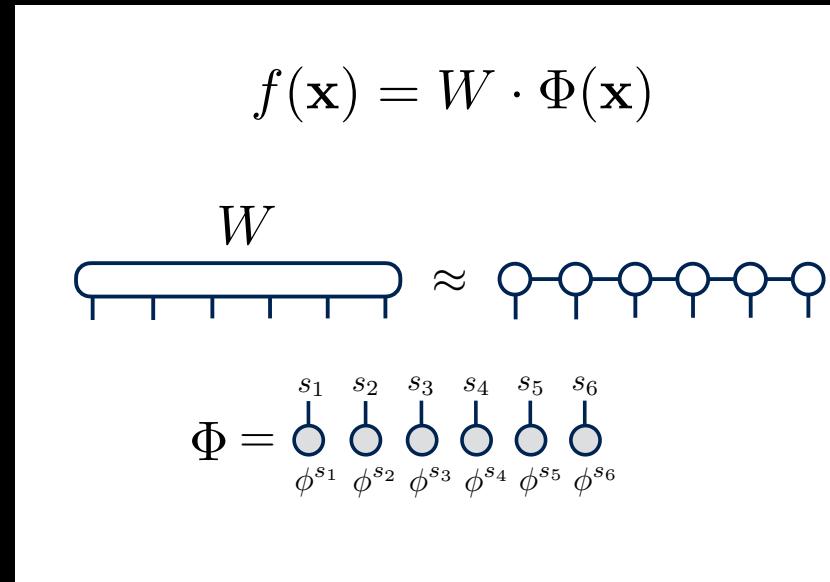
Minimally entangled typically thermal state (METTS) [White 2009]

Thermal pure quantum matrix product state (TPQ-MPS) [Iwaki et. al., 2021]

Application field

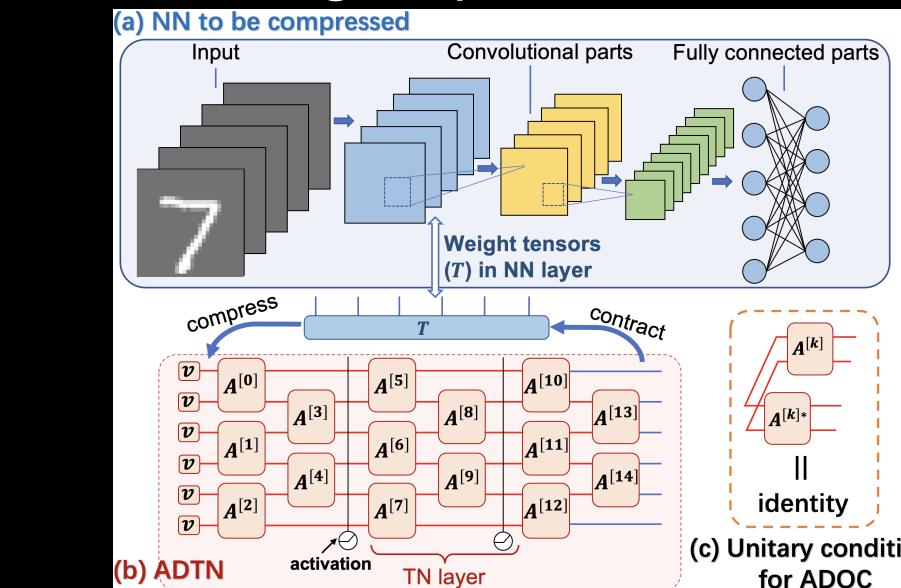
- It has recently attracted attention as an efficient representation of **machine learning** models and as a highly efficient compression method for **big data**.

[Stoudenmire&Schwab, 2017]



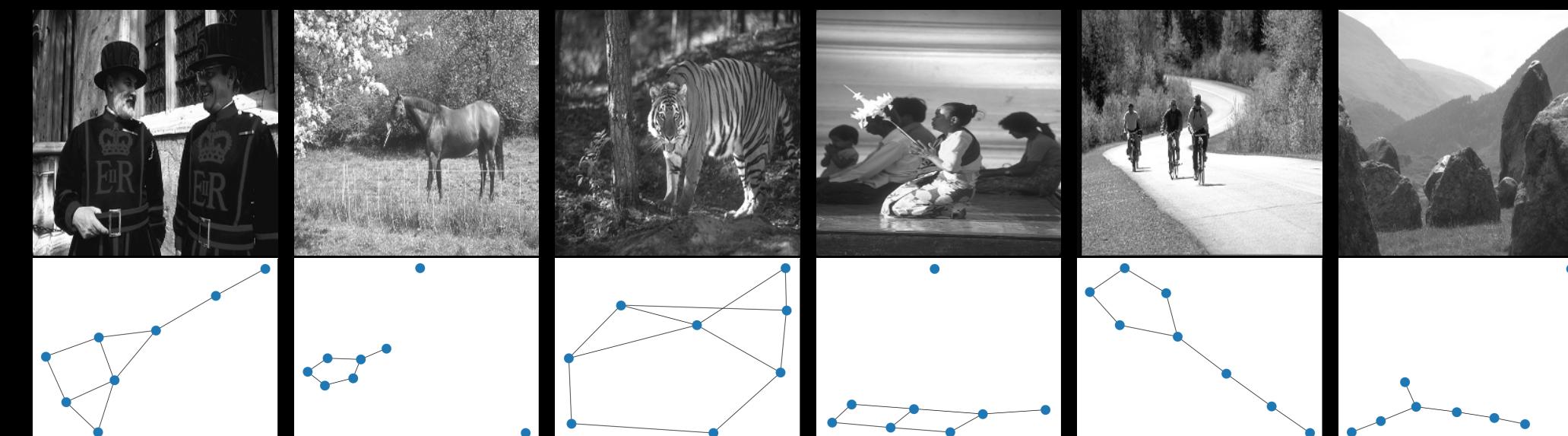
Tensor network as the learning model

[Shi-Ju Ran group, arXiv:2305.06058]



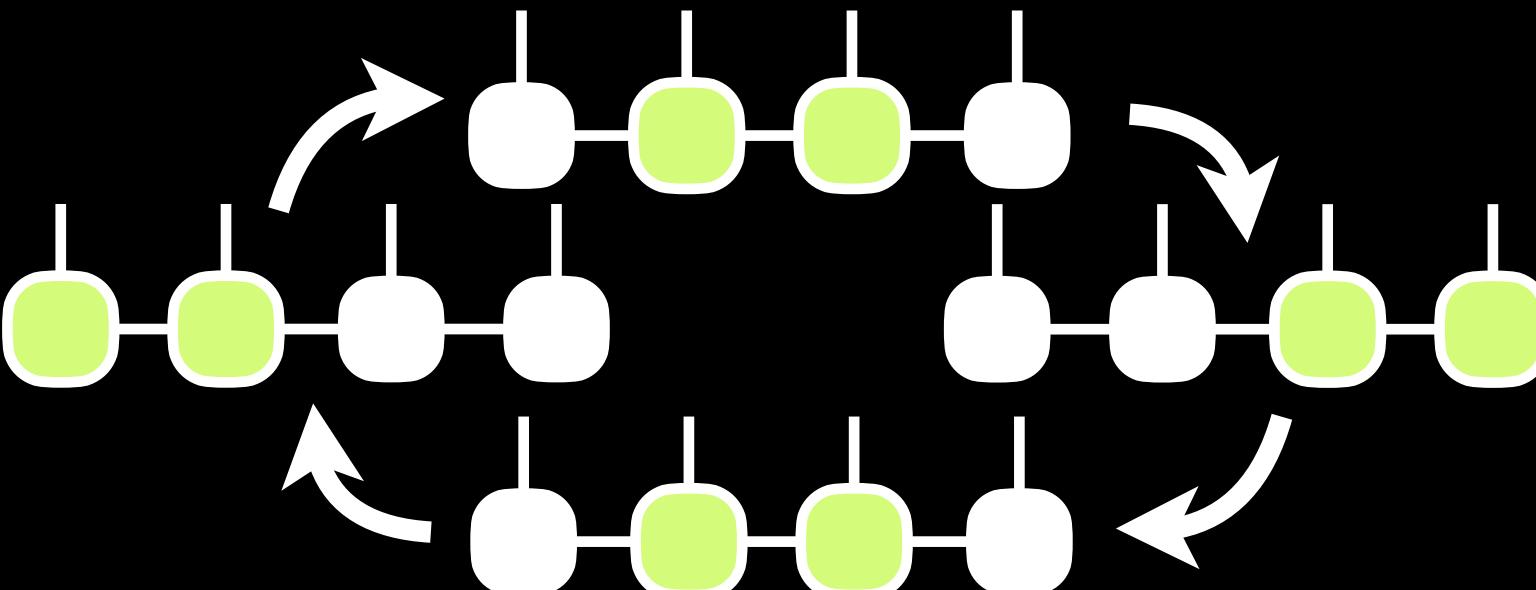
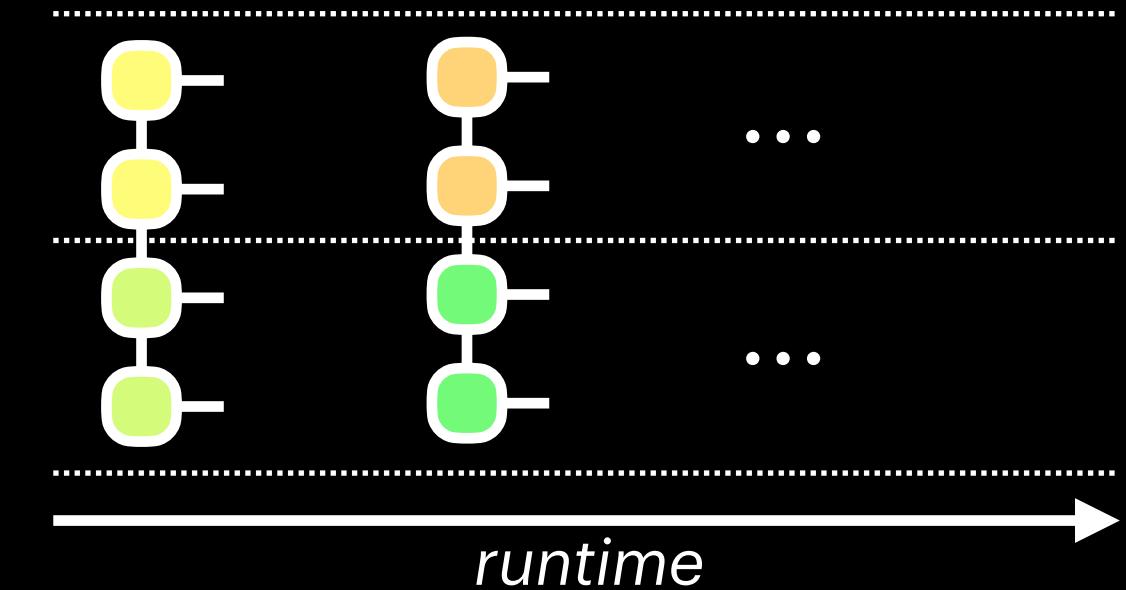
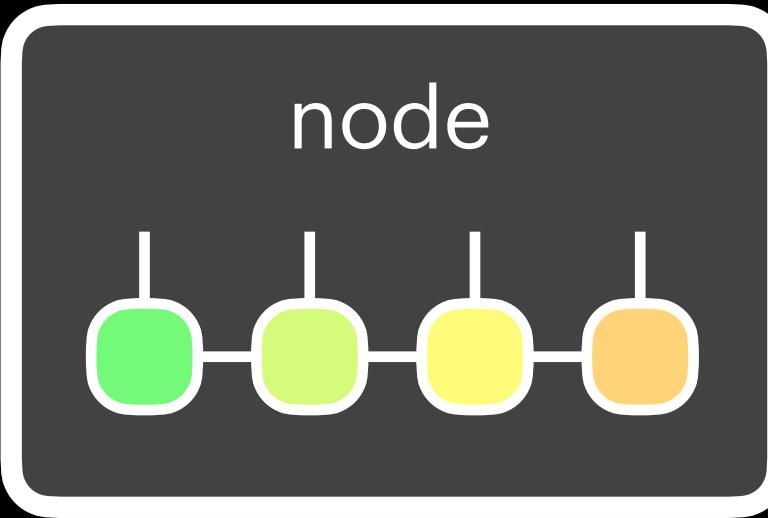
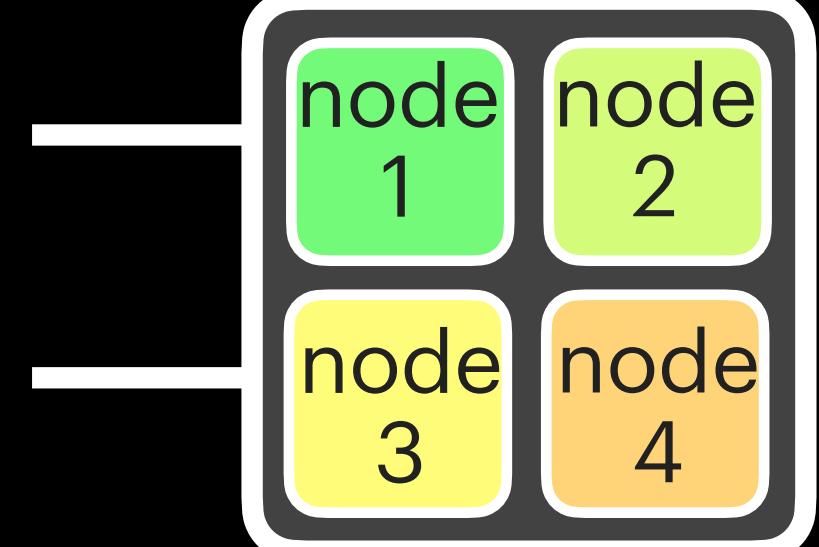
Tensor network as a component of deep neural network

[Chao Li et al (AIP tensor learning team), 2020, 2022, 2023]



Tensor network as a compression of big data / Tensor network structure search problem

Limitations in current TNS development

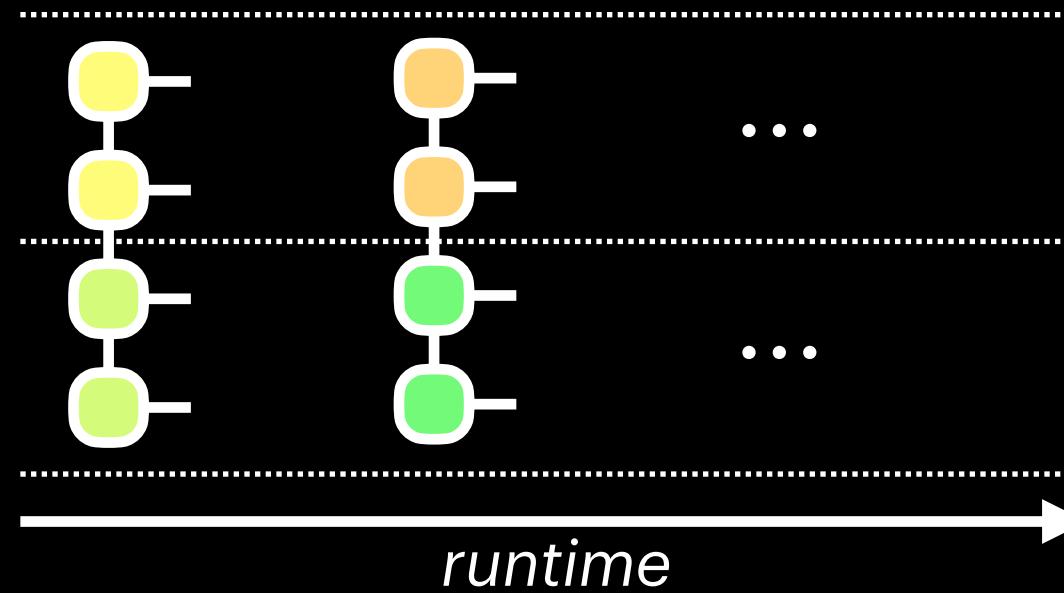
	Limitations	Solution
Algorithms	Sequential nature	Real-space parallelization
		
Data structure	Shared-memory implementation	Distributed-memory implementation
		
		data parallel

Our contributions

Real-space parallelizable MPS algorithm

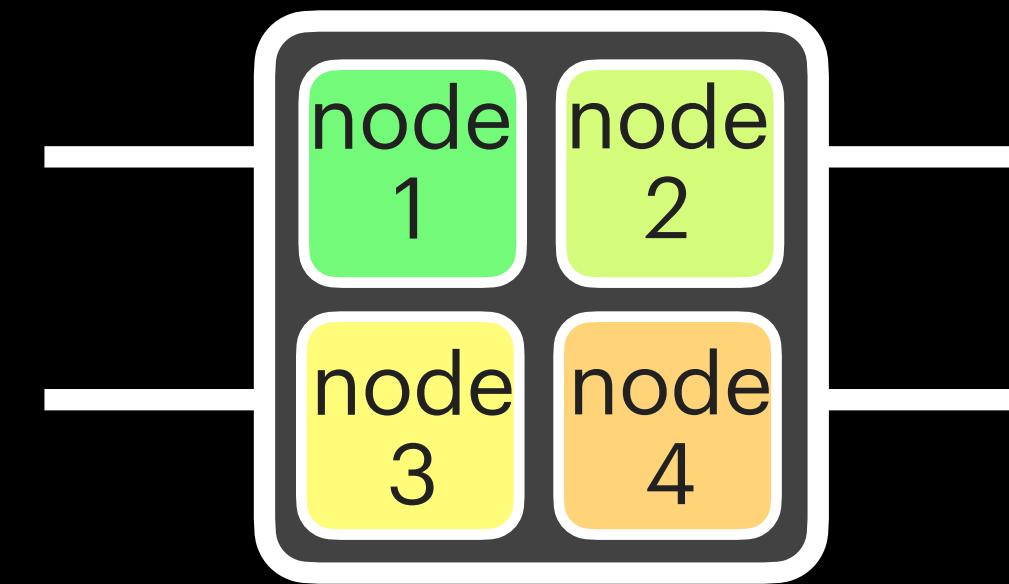
A case study: parallel TEBD (**pTEBD**)

[R.-Y. Sun, T. Shirakawa, S. Yunoki, arXiv:2312.02667 (2023)]

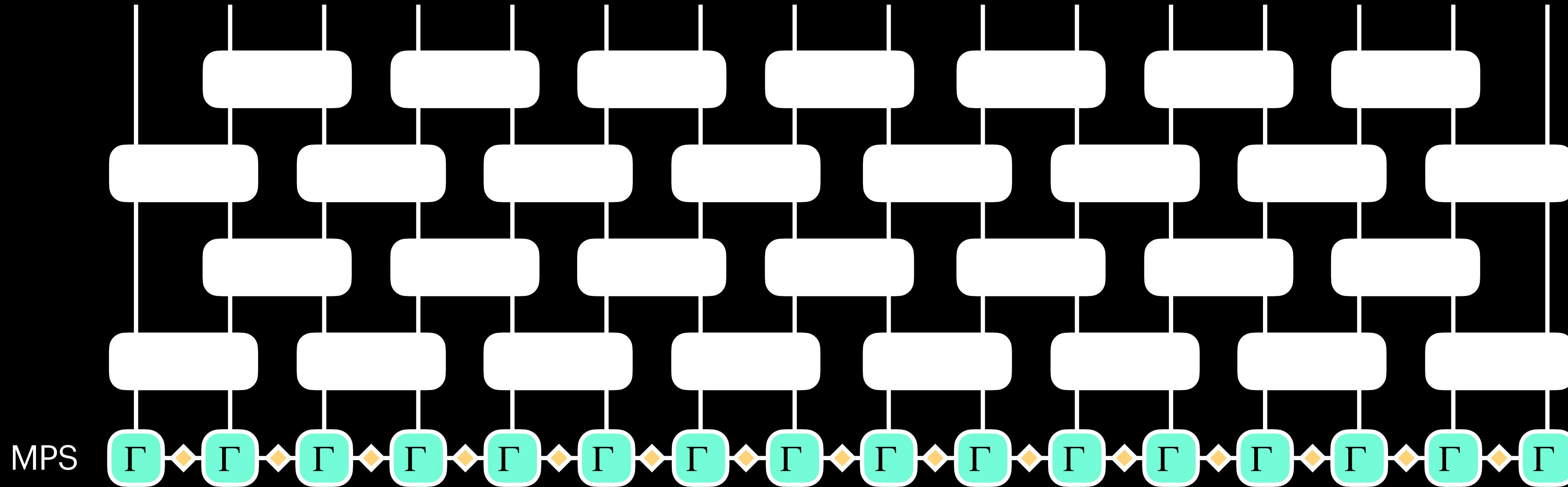


TNS software for Fugaku

<https://github.com/gracequantum>



Time-evolving block decimation (TEBD)

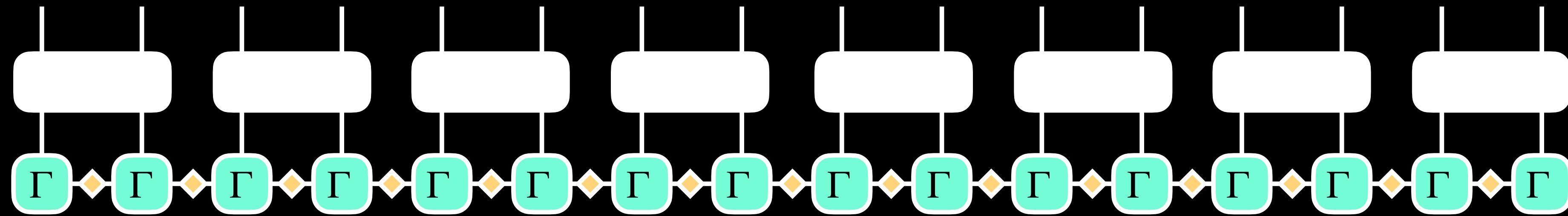


When performing time evolution calculations on MPS, the simplest method is to calculate Trotter slices called **time-evolving block decimation**.

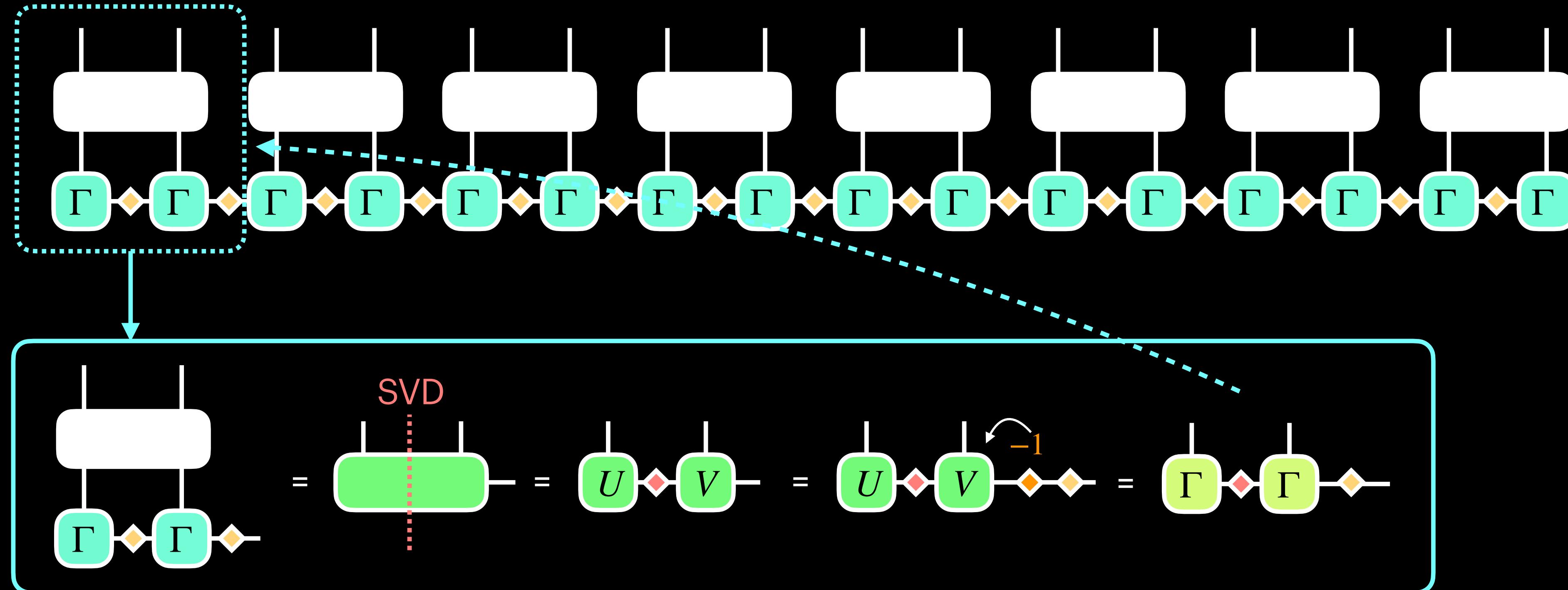
Quantum computing is a time-evolution starting from a trivial initial state (a **direct product state**).

A direct product state is a matrix product state with bond dimension 1.

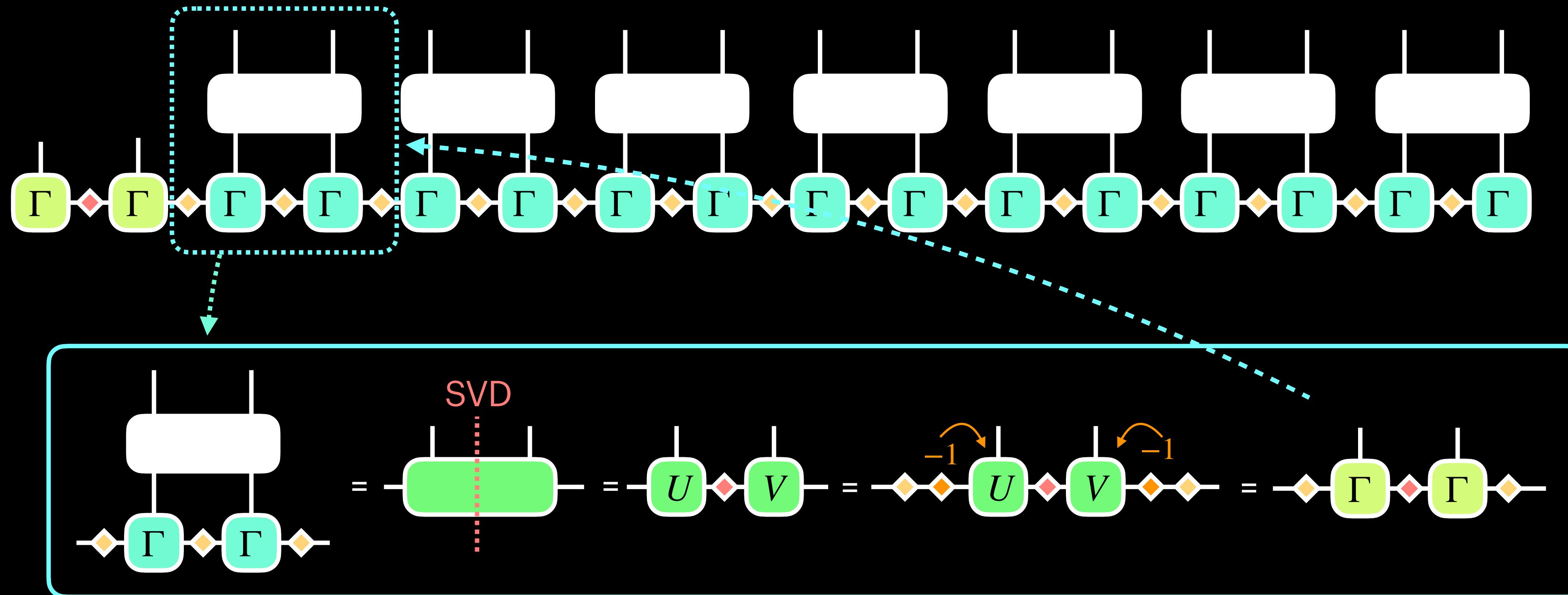
Time-evolving block decimation (TEBD)



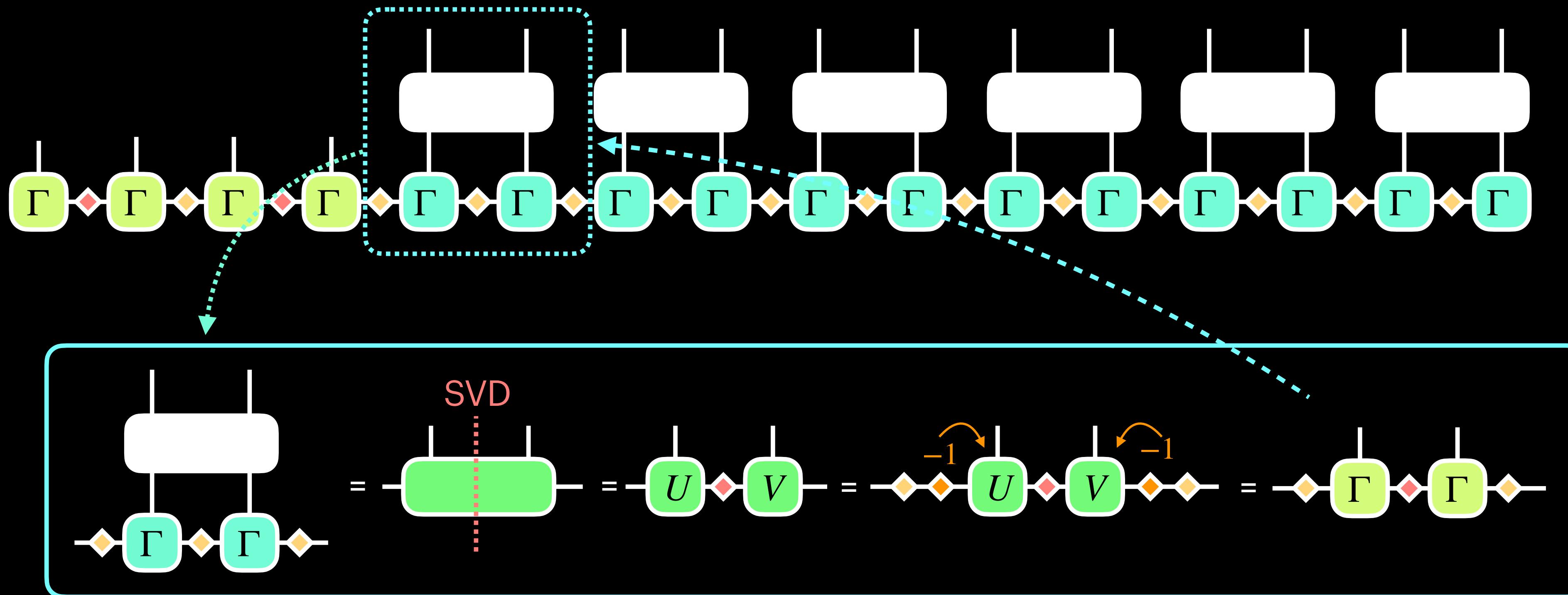
Time-evolving block decimation (TEBD)



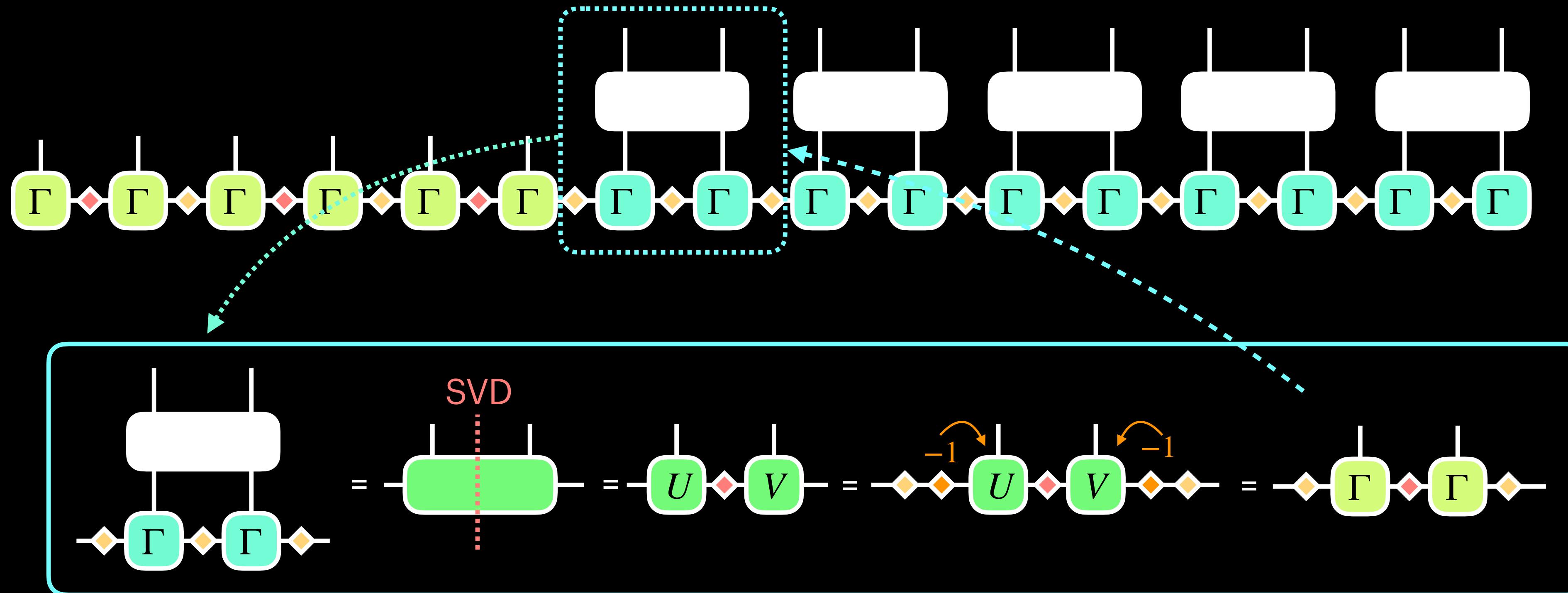
Time-evolving block decimation (TEBD)



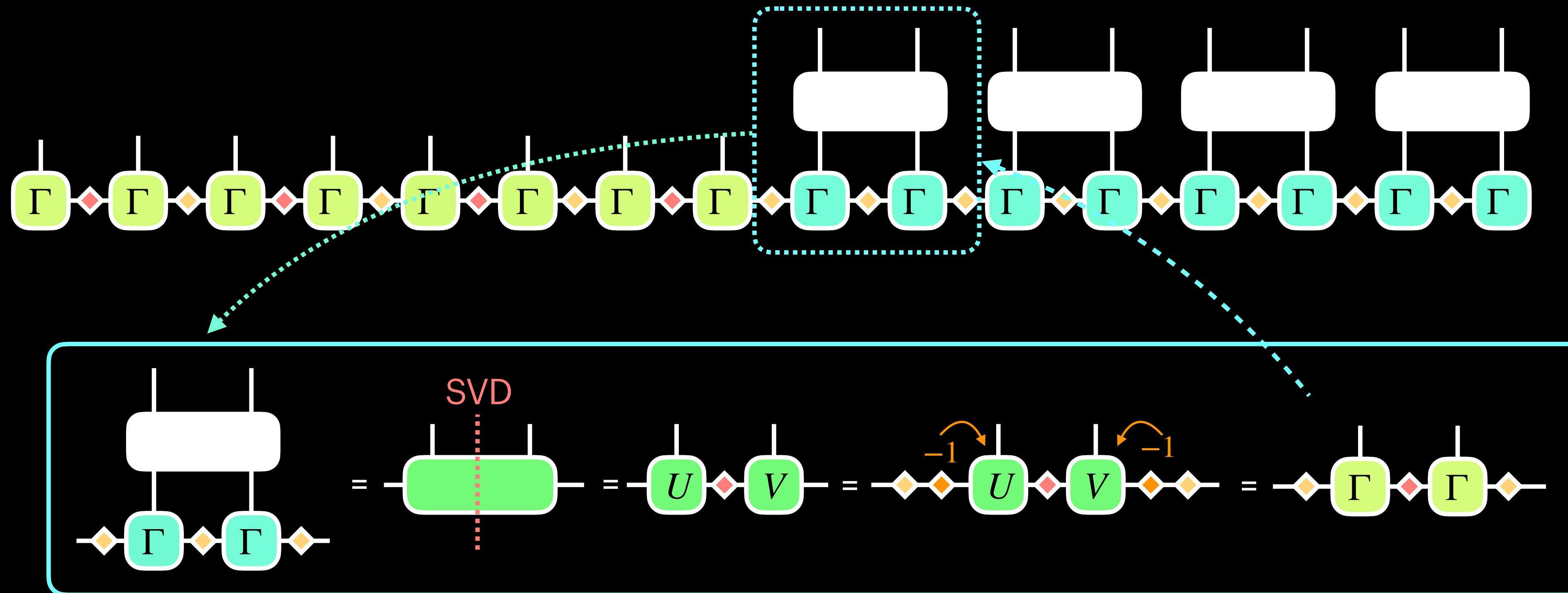
Time-evolving block decimation (TEBD)



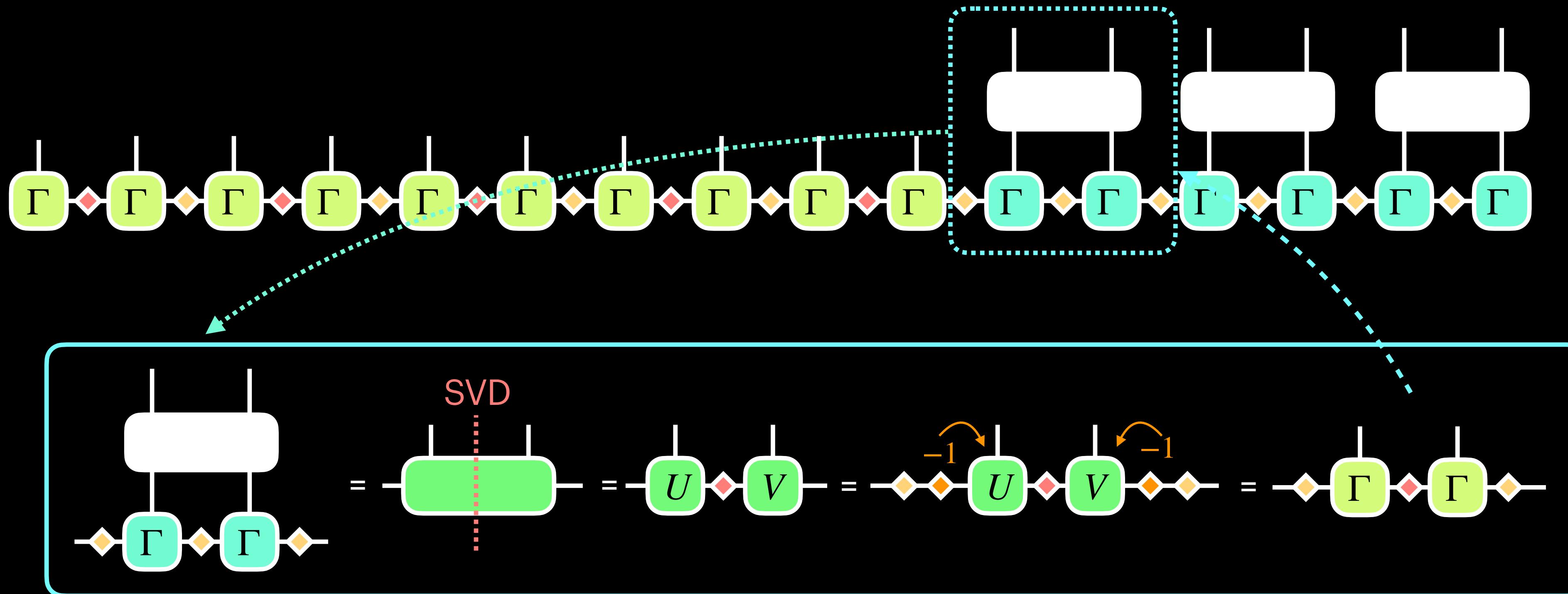
Time-evolving block decimation (TEBD)



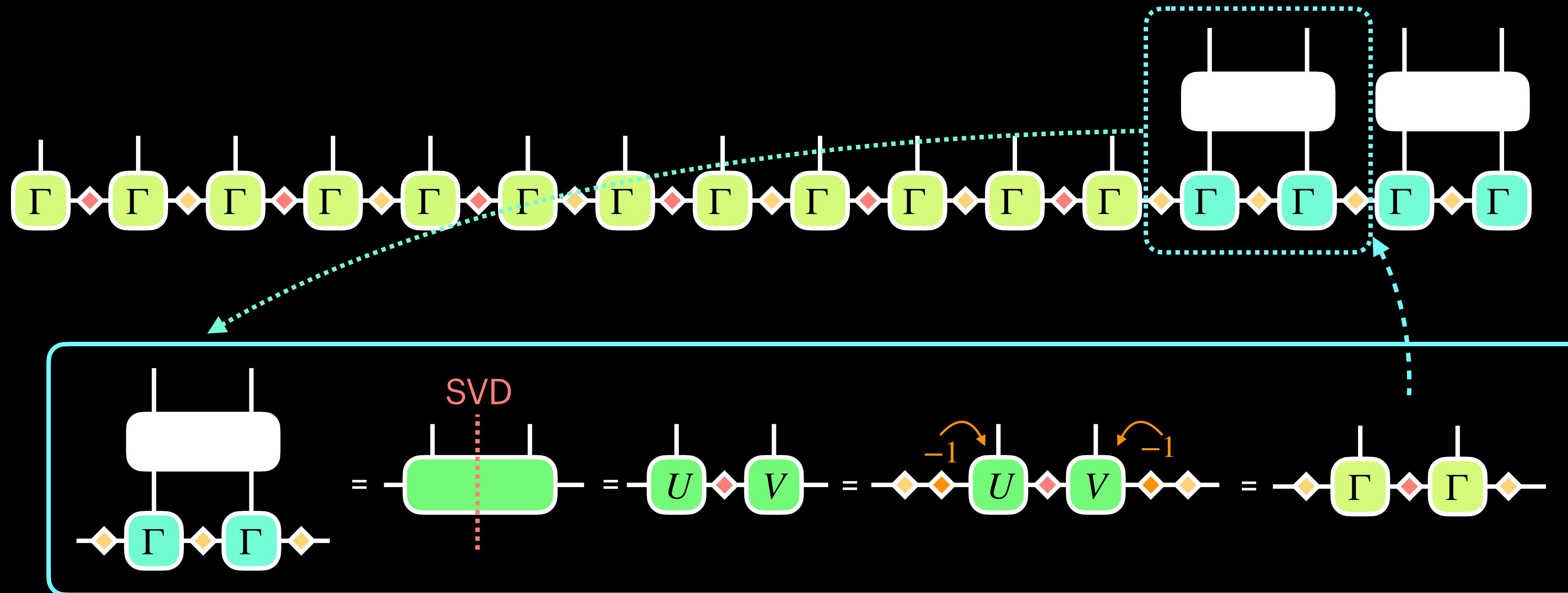
Time-evolving block decimation (TEBD)



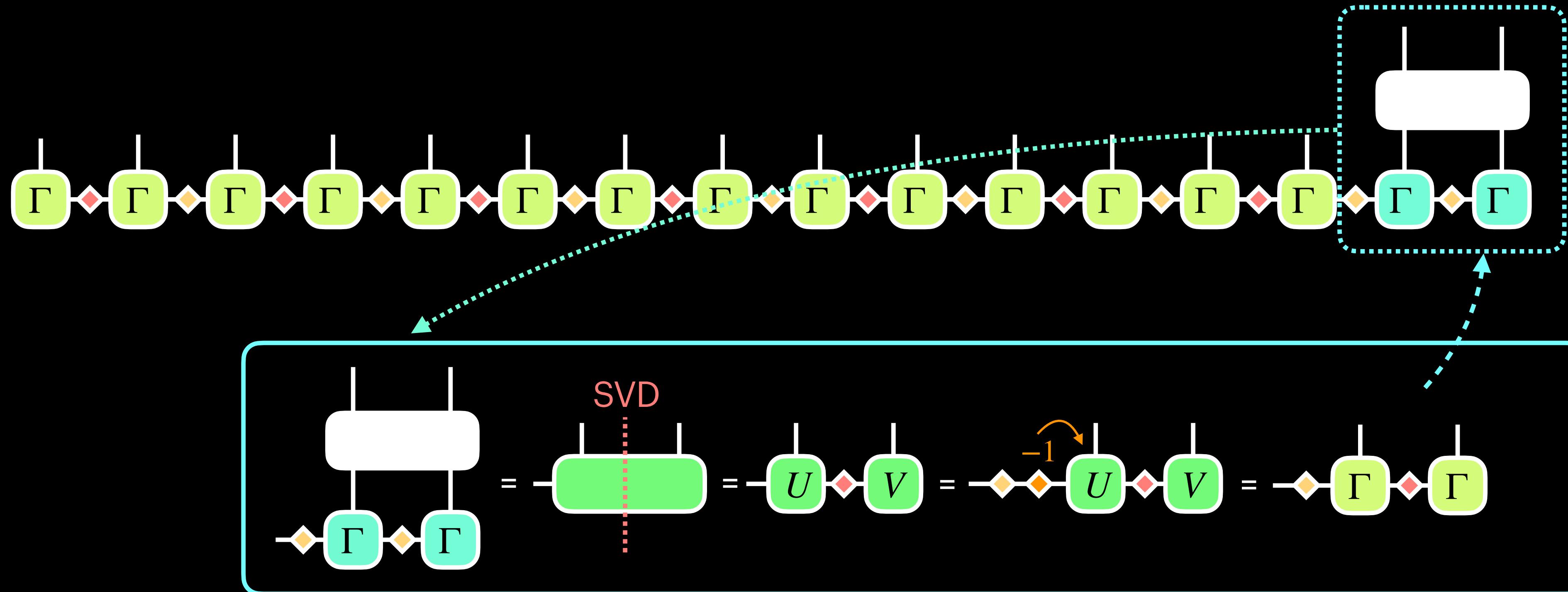
Time-evolving block decimation (TEBD)



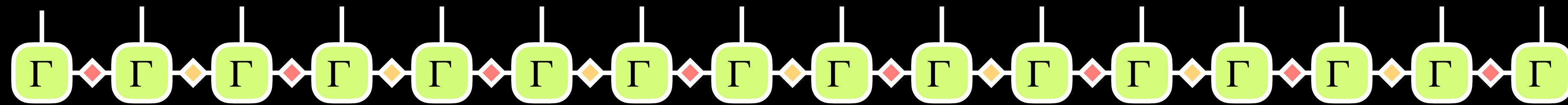
Time-evolving block decimation (TEBD)



Time-evolving block decimation (TEBD)

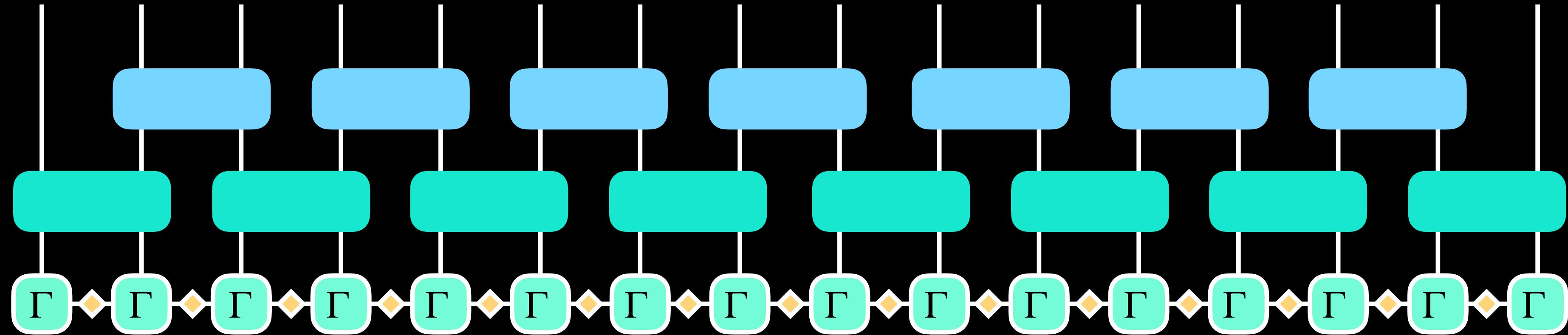


Time-evolving block decimation (TEBD)



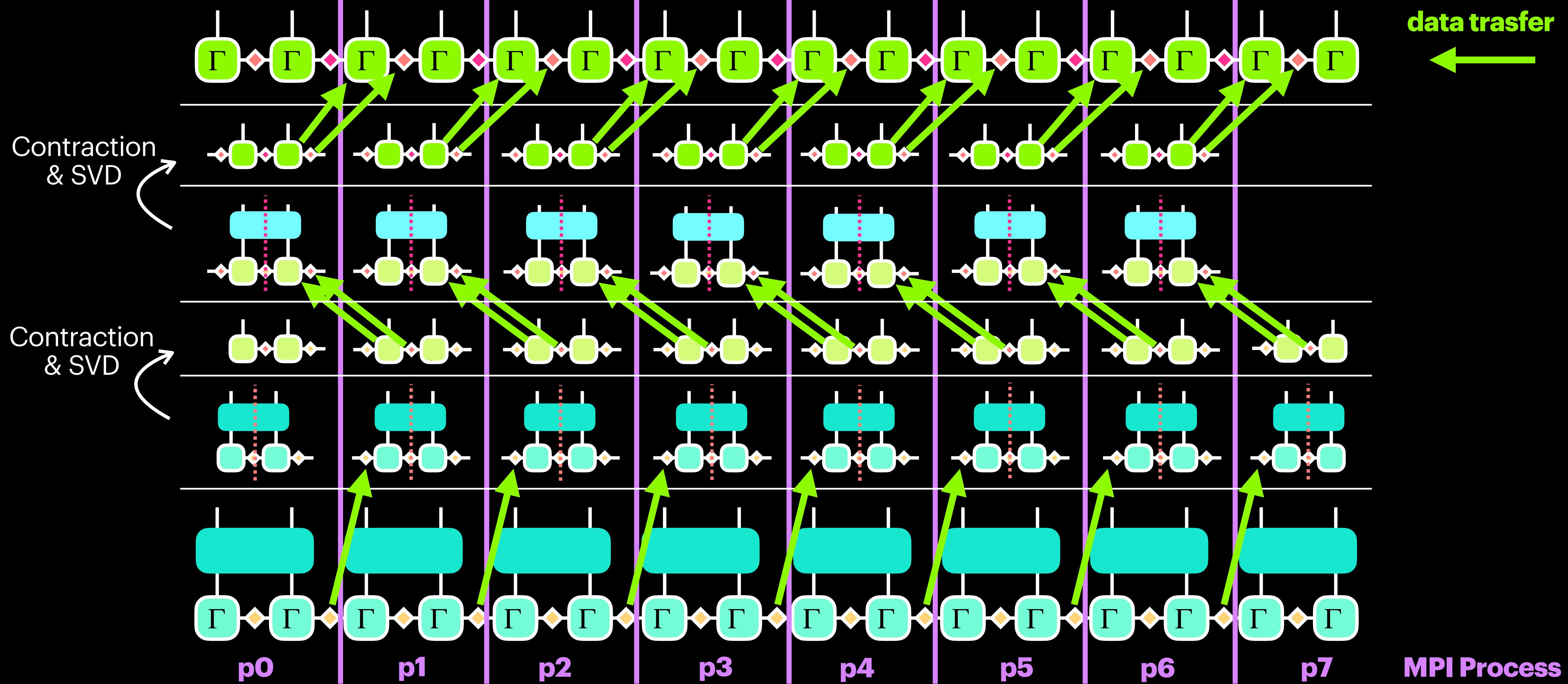
Parallelization of TEBD (pTEBD)

[R.-Y. Sun, T. Shirakawa, S. Yunoki, arXiv:2312.02667 (2023)]

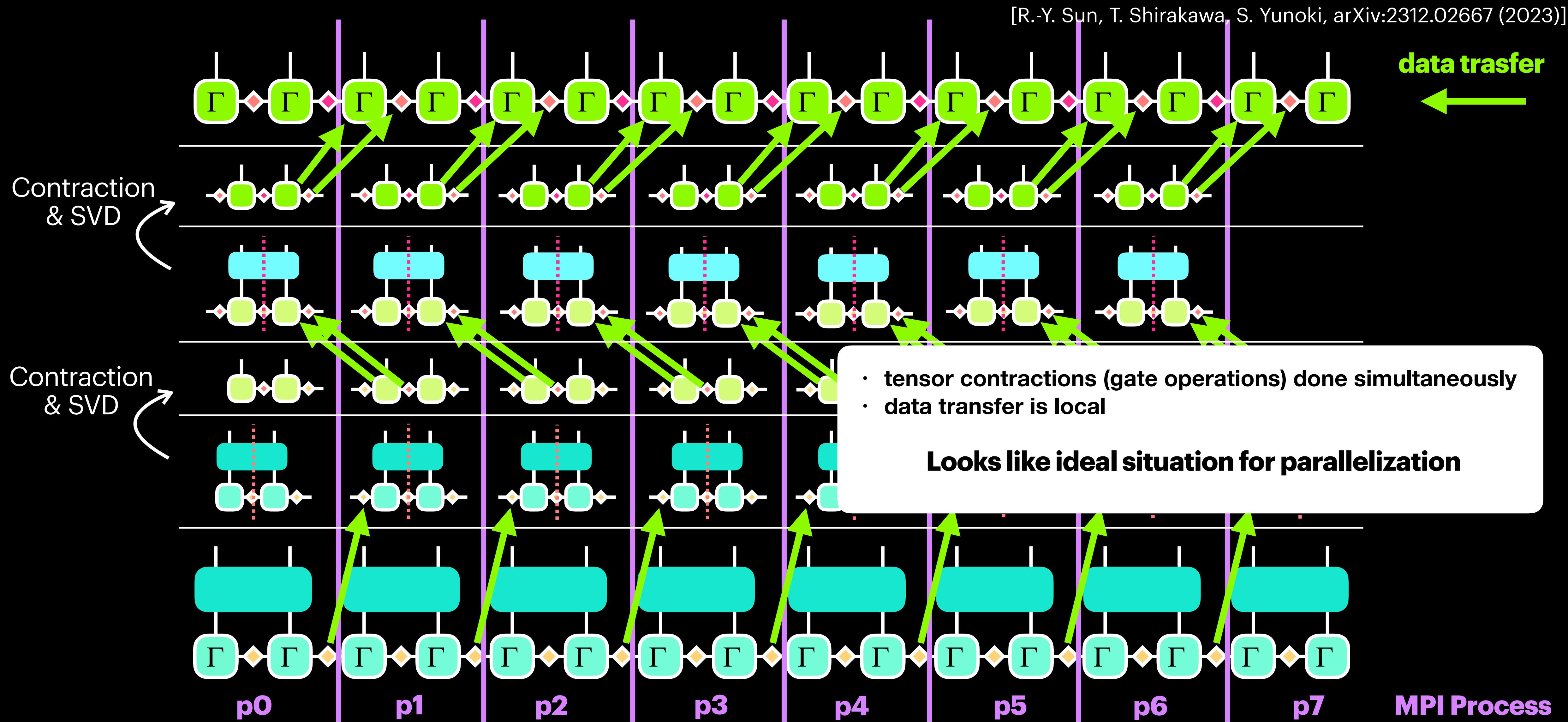


Parallelization of TEBD (pTEBD)

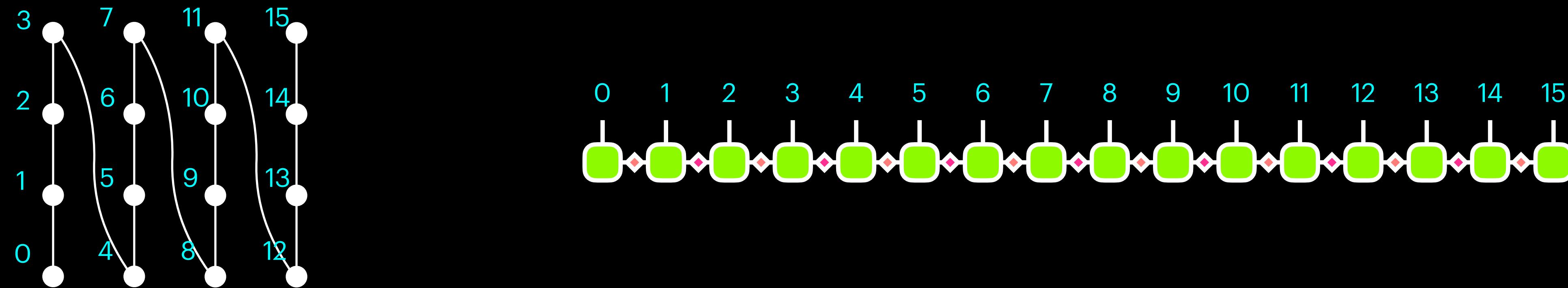
[R.-Y. Sun, T. Shirakawa, S. Yunoki, arXiv:2312.02667 (2023)]



Parallelization of TEBD (pTEBD)



Simulation for 2D quantum circuit

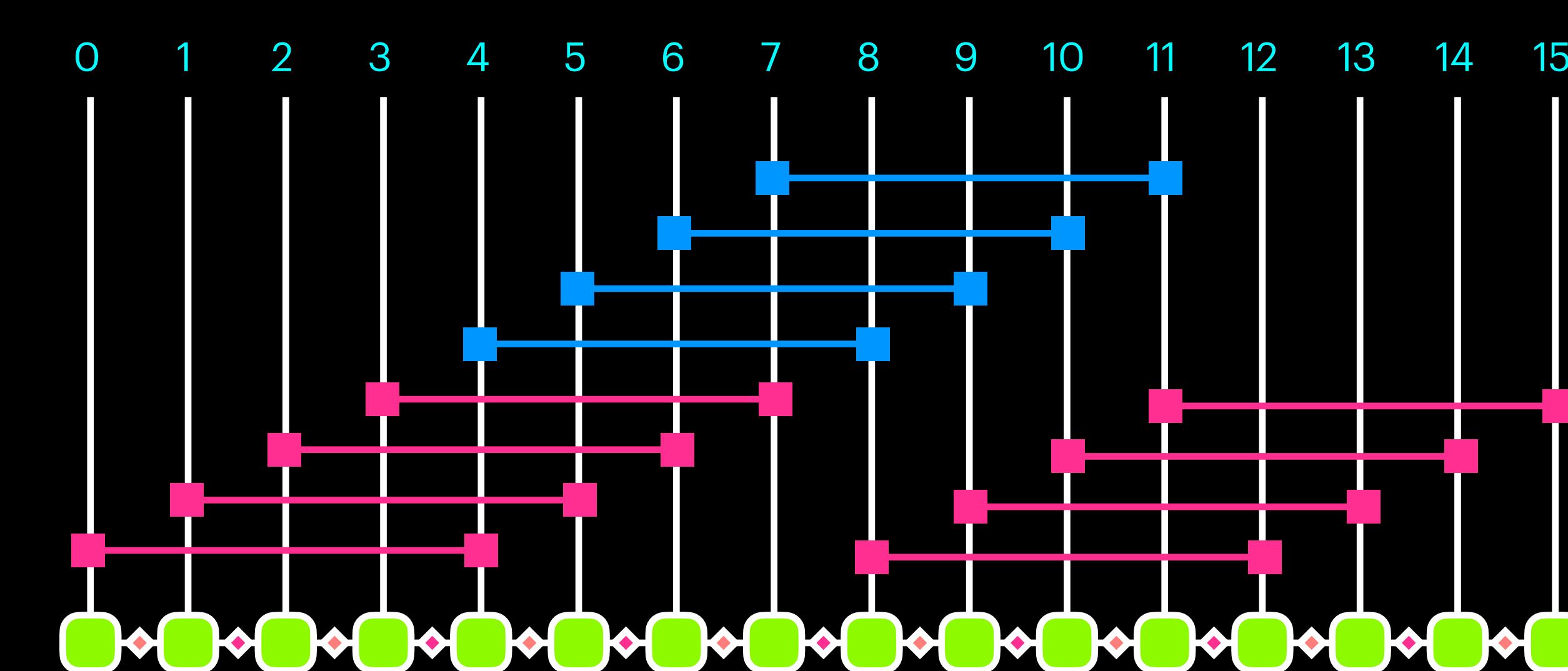
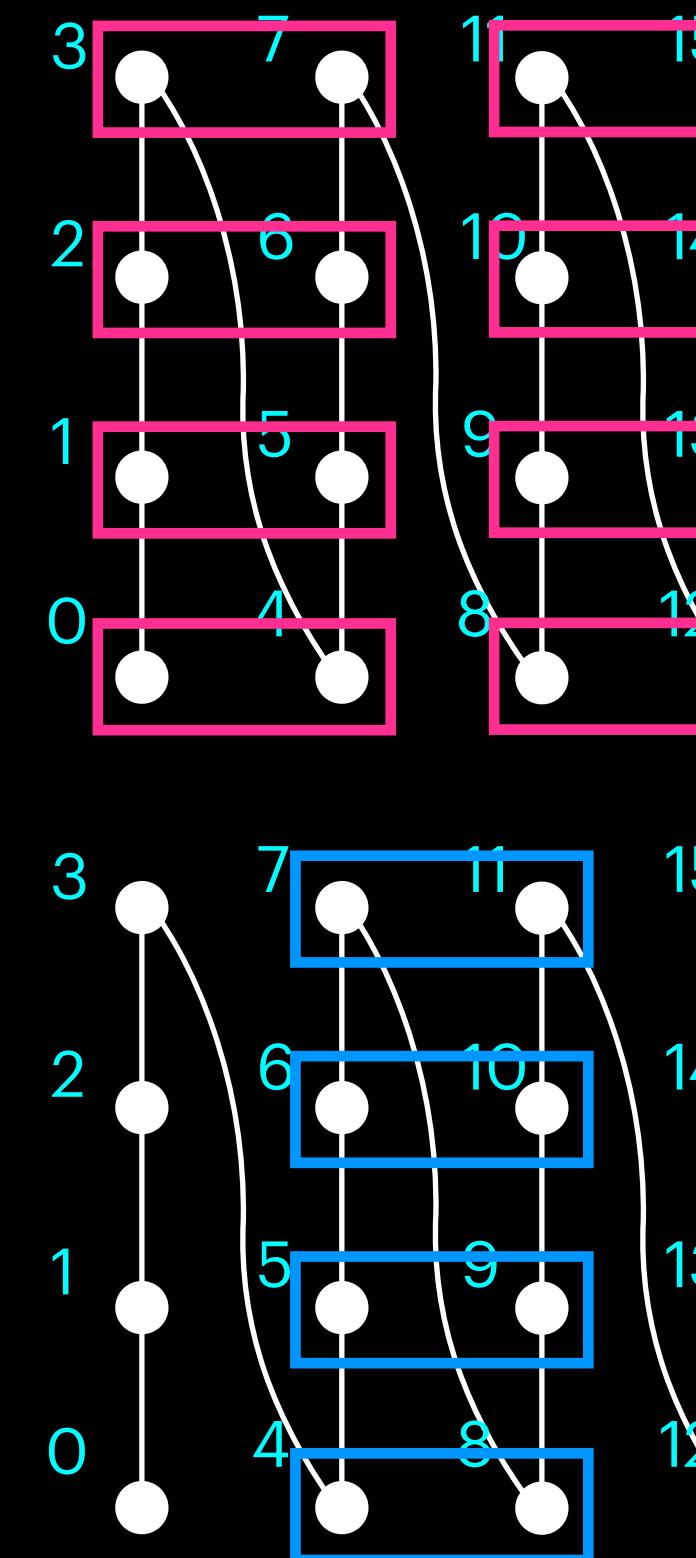


In order to calculate a 2D system using MPS, the 2D system is forcibly regarded as a 1D system.

Simulation for 2D quantum circuit

 Position of 1st layer operators
 Position of 2nd layer operators

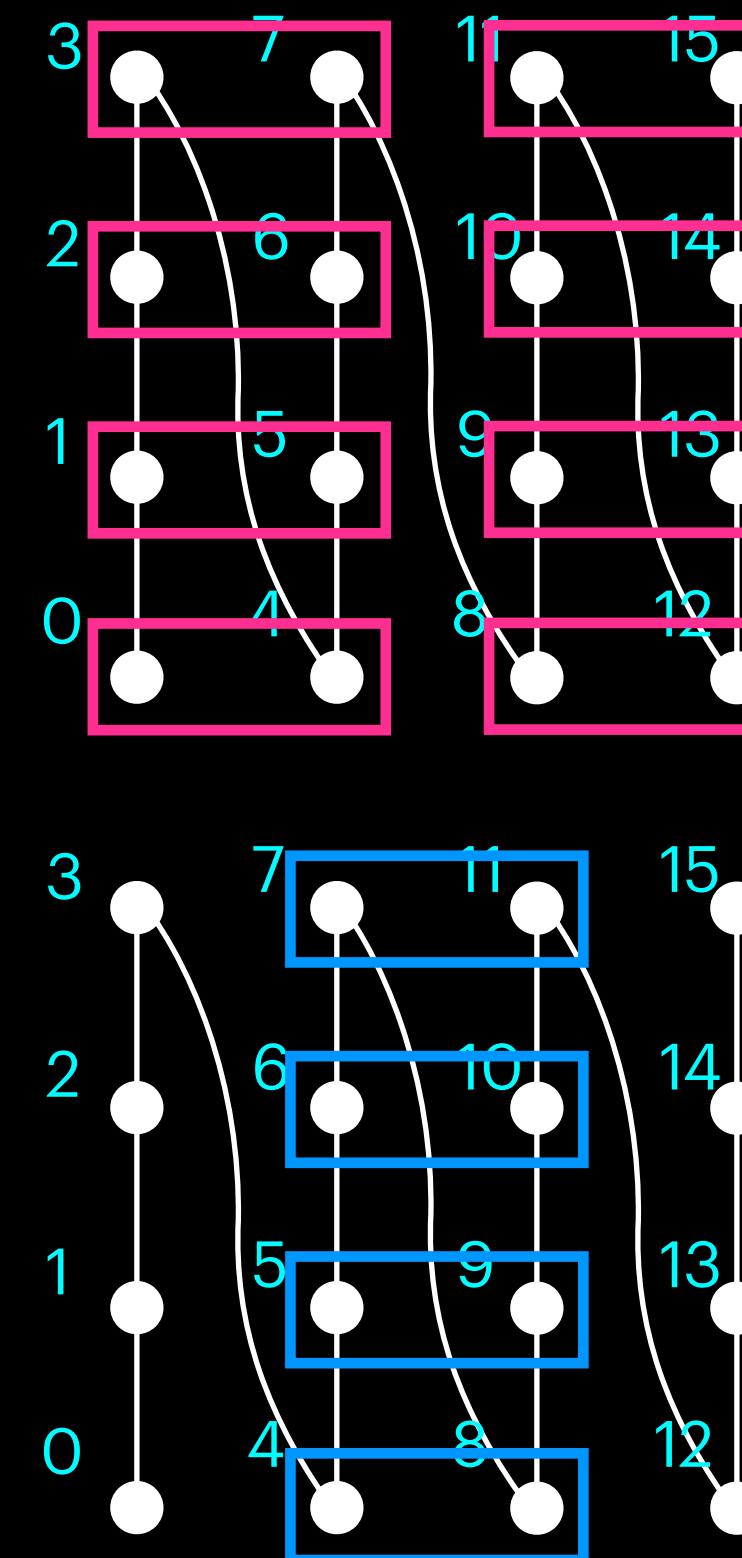
 1st layer operators
 2nd layer operators



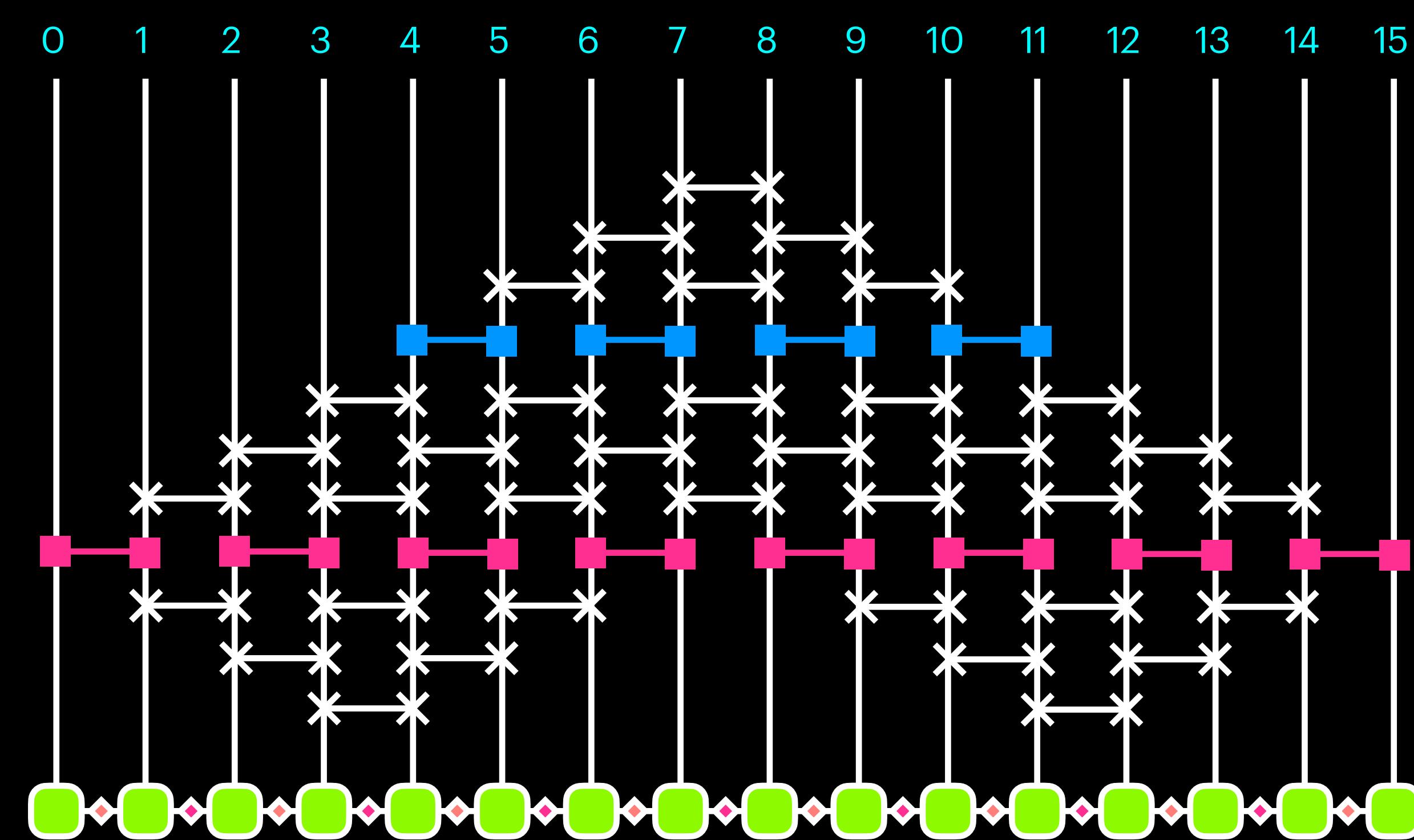
Then, the nearest-neighbor operators in the 2D system become distant operators in the virtual 1D system.

Simulation for 2D quantum circuit

 Position of 1st layer operators
 Position of 2nd layer operators



—■— 1st layer operators
—■— 2nd layer operators
× SWAP operator S_{ij}
 $S_{ij} |\sigma_i \sigma_j\rangle = |\sigma_j \sigma_i\rangle$



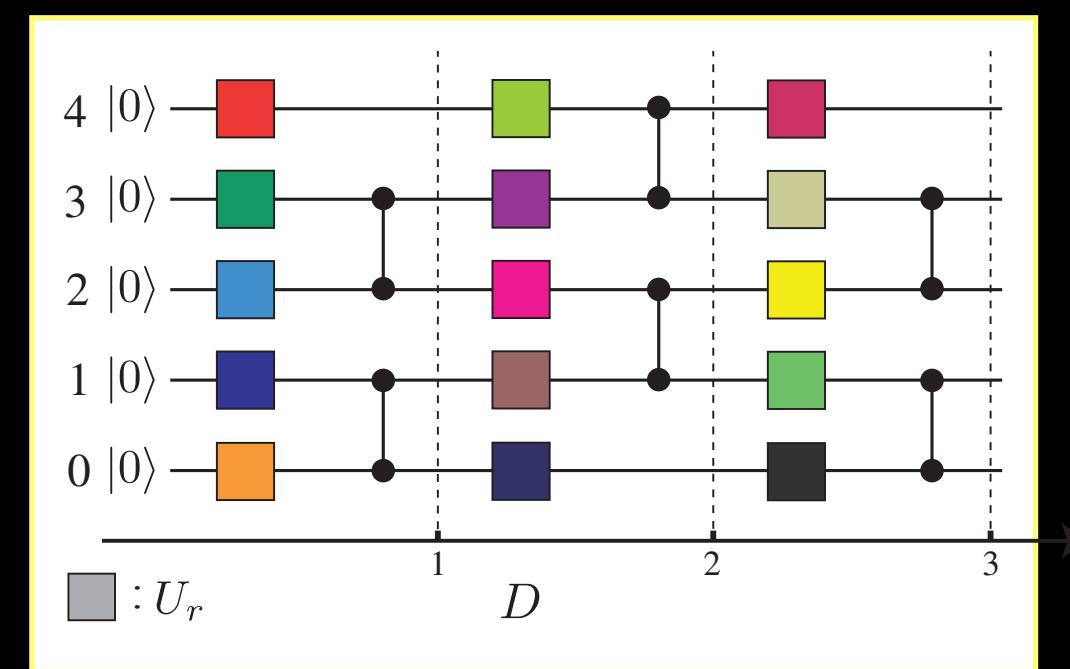
The simplest and most efficient way to handle these bonds in TEBD is by sandwiching the swap operator.

Benchmark circuits

Random quantum circuit

RQC-1D

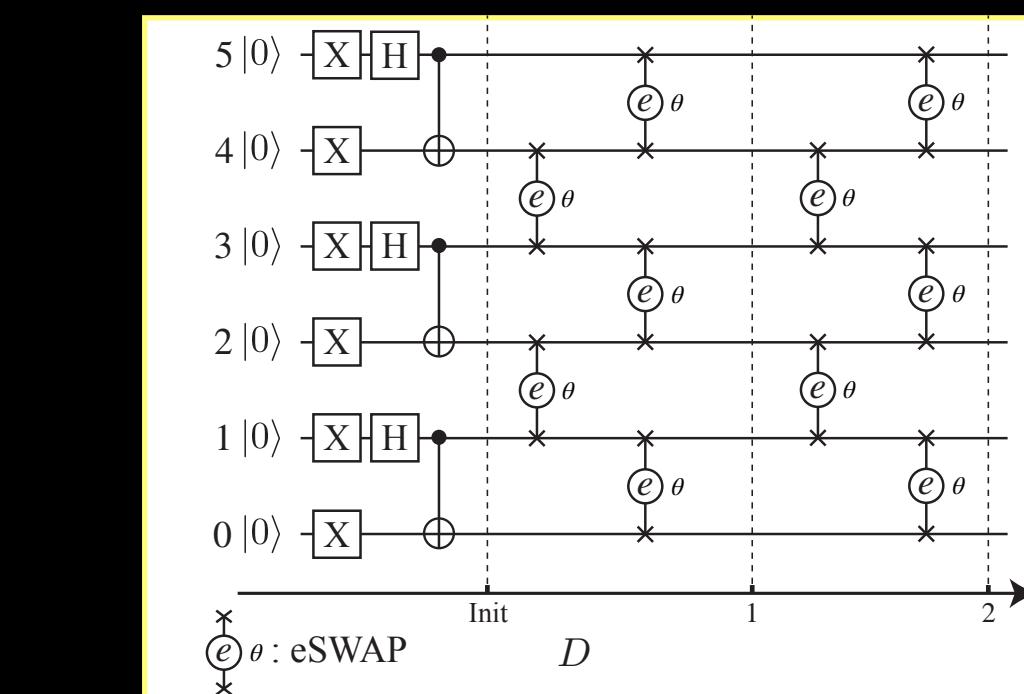
1D



[Y. Zhou et al., PRX **10**, 041038 (2020)]

Parametrized quantum circuit

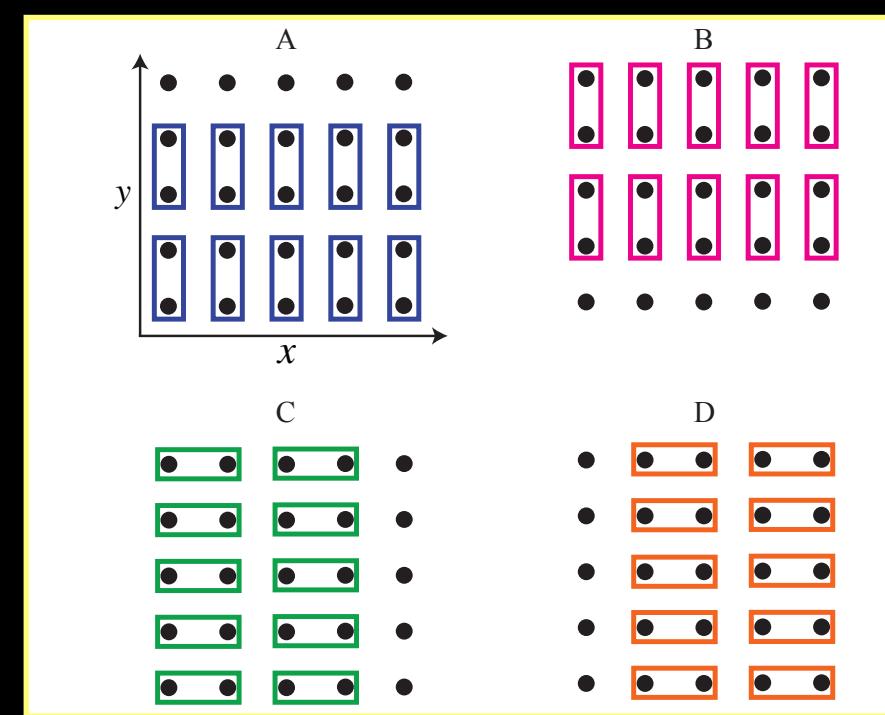
PQC-1D



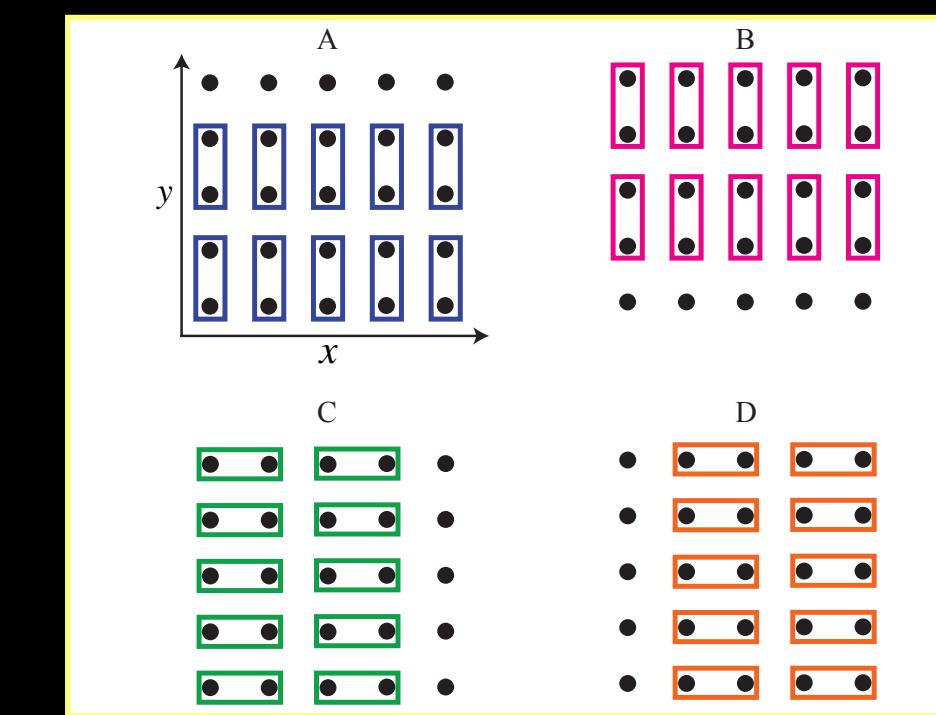
[R.-Y. Sun, T. Shirakawa, S. Yunoki, PRB **108**, 075127 (2023)]

RQC-2D(ABCDABCD...)

2D

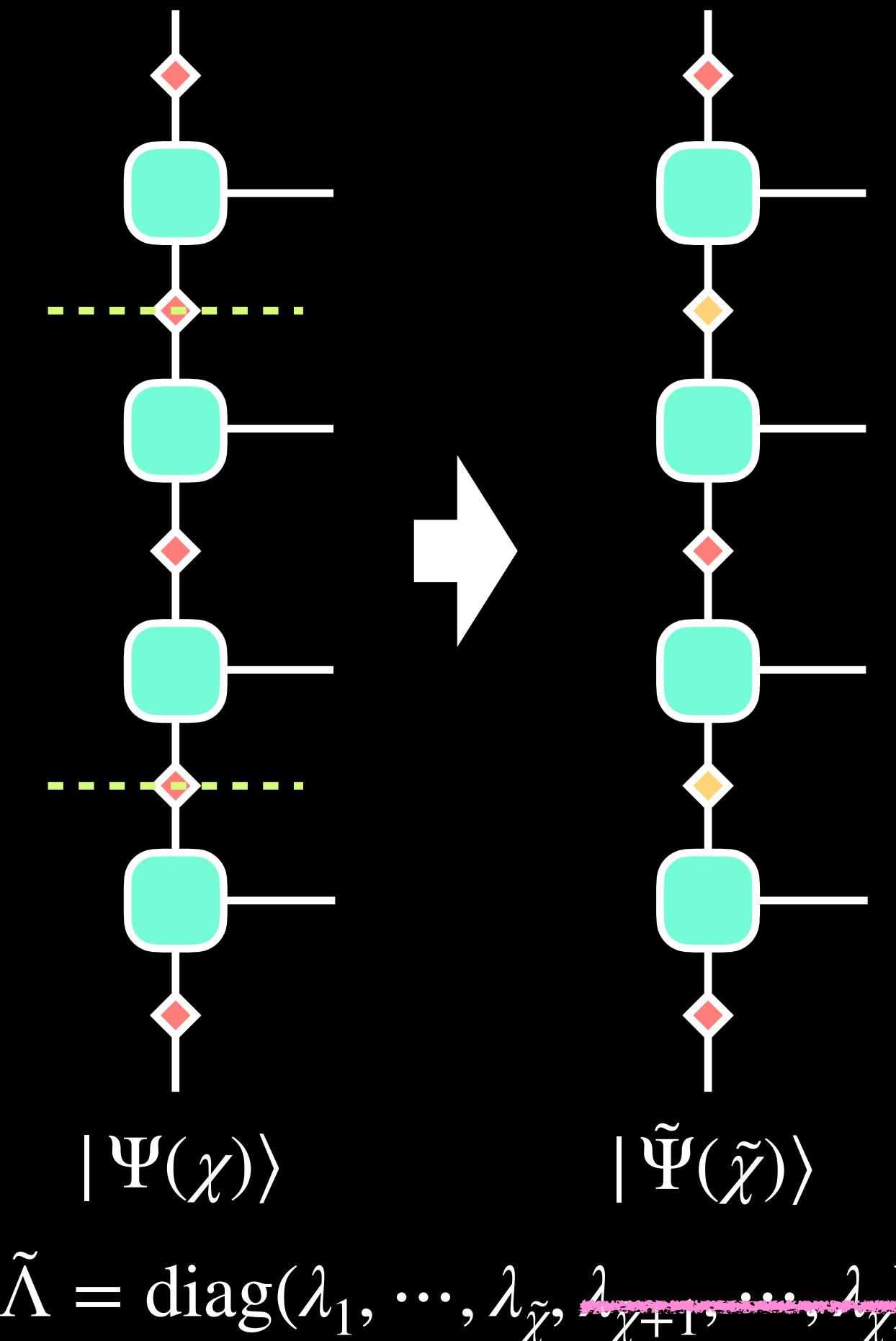


PQC-2D(ABCDABCD...)



[R.-Y. Sun, T. Shirakawa, S. Yunoki, arXiv:2312.02667 (2023)]

Parallel MPS compression



Theorem [Verstraete and Cirac, PRB **73**, 094423 (2006)]

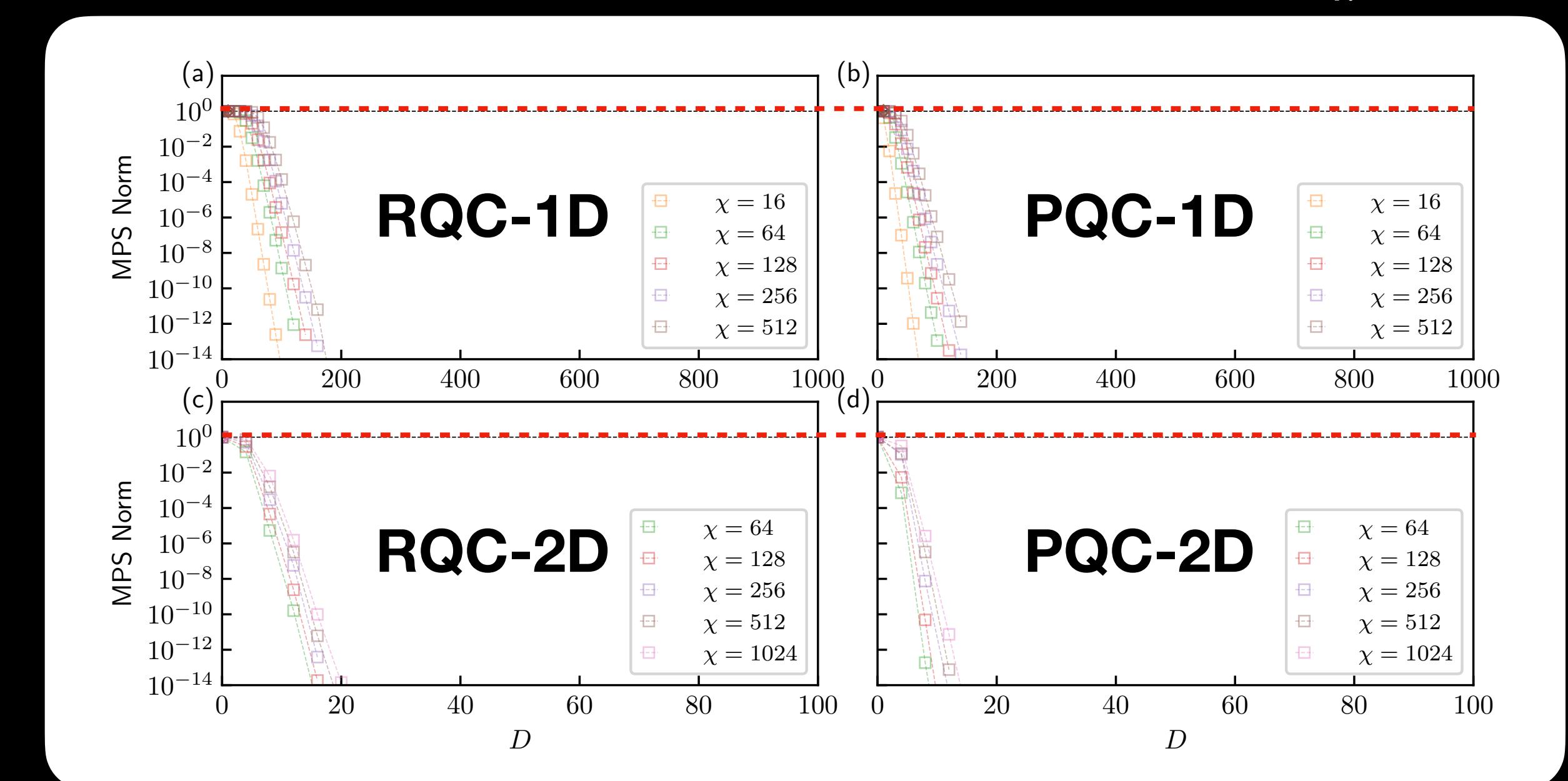
$$\left| |\Psi(\chi)\rangle - |\tilde{\Psi}(\tilde{\chi})\rangle \right| \leq 2\varepsilon(\tilde{\chi})$$

$$\varepsilon(\tilde{\chi}) = \sum_{i=1}^{N-1} \varepsilon_i(\tilde{\chi})$$

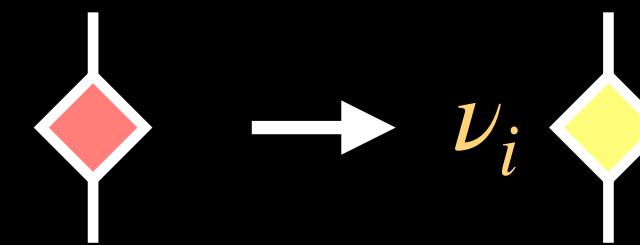
Issue: wavefunction norm decay

$$1 - \sqrt{2\varepsilon(\tilde{\chi})} \leq \left| |\tilde{\Psi}(\tilde{\chi})\rangle \right| \leq 1$$

$$\varepsilon_i(\tilde{\chi}) = \sum_{n=\tilde{\chi}+1}^{\chi} (\lambda_n^{[i]})^2$$



Stabilize the wavefunction norm in parallel



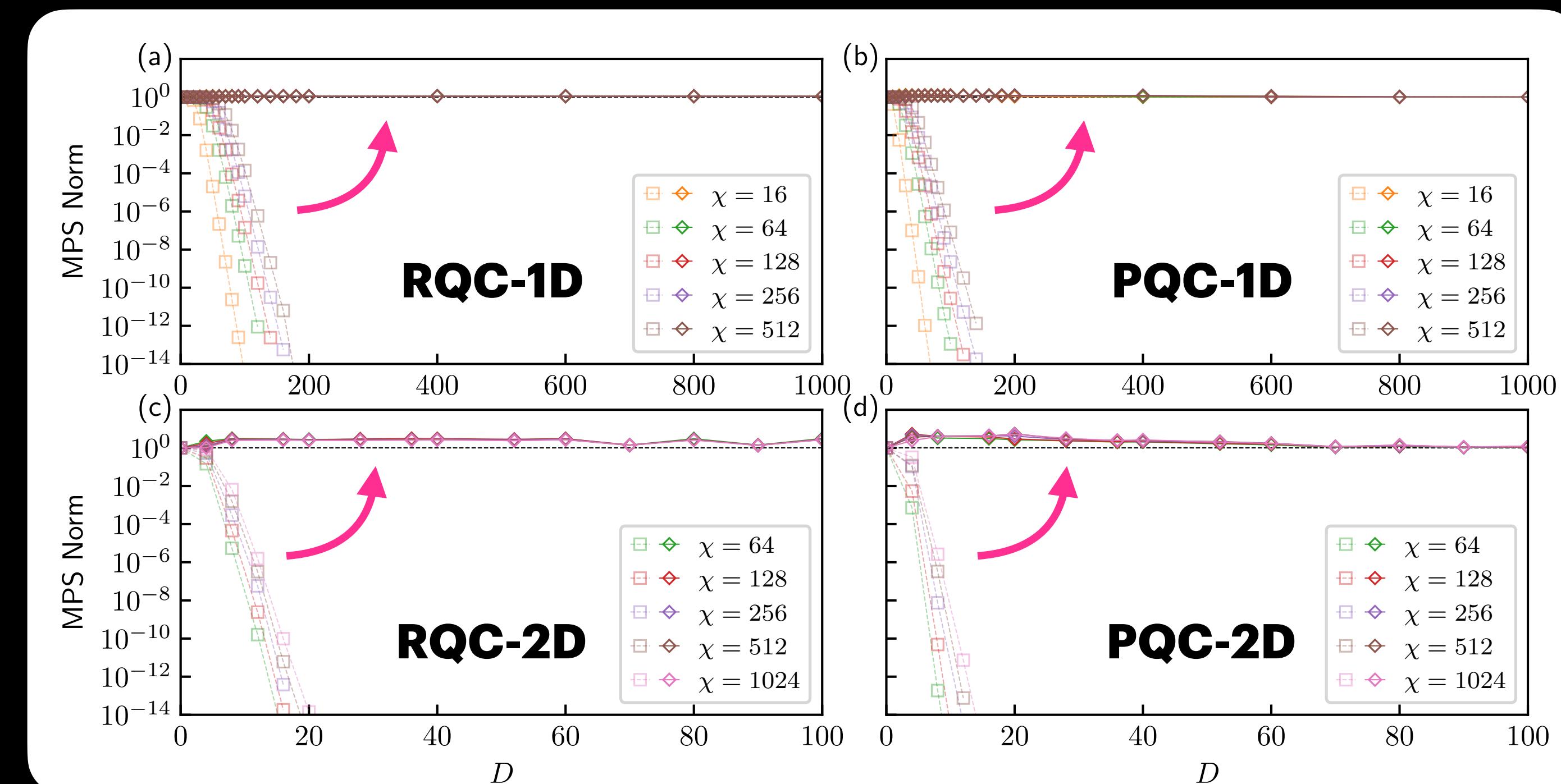
i.e., parallel rescaling.

$$\Lambda^{[i]}(\chi) \rightarrow \nu_i \tilde{\Lambda}^{[i]}(\tilde{\chi})$$

$$\nu_i = [1 - \varepsilon_i(\tilde{\chi})]^{-1/2}$$

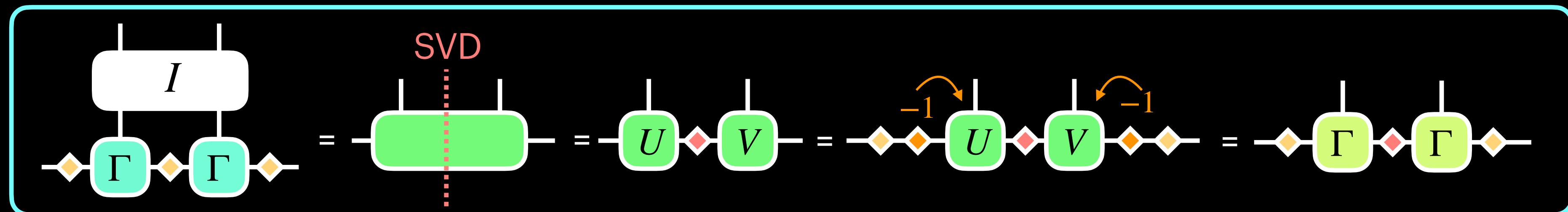
$$(1 - \sqrt{2\varepsilon(\tilde{\chi})}) \prod_{i=1}^{N-1} \nu_i \leq |\langle \Psi(\tilde{\chi}) \rangle| \leq \prod_{i=1}^{N-1} \nu_i$$

$$(1 - \sqrt{2\varepsilon(\tilde{\chi})}) \prod_{i=1}^{N-1} \nu_i \leq 1, 1 \leq \prod_{i=1}^{N-1} \nu_i$$

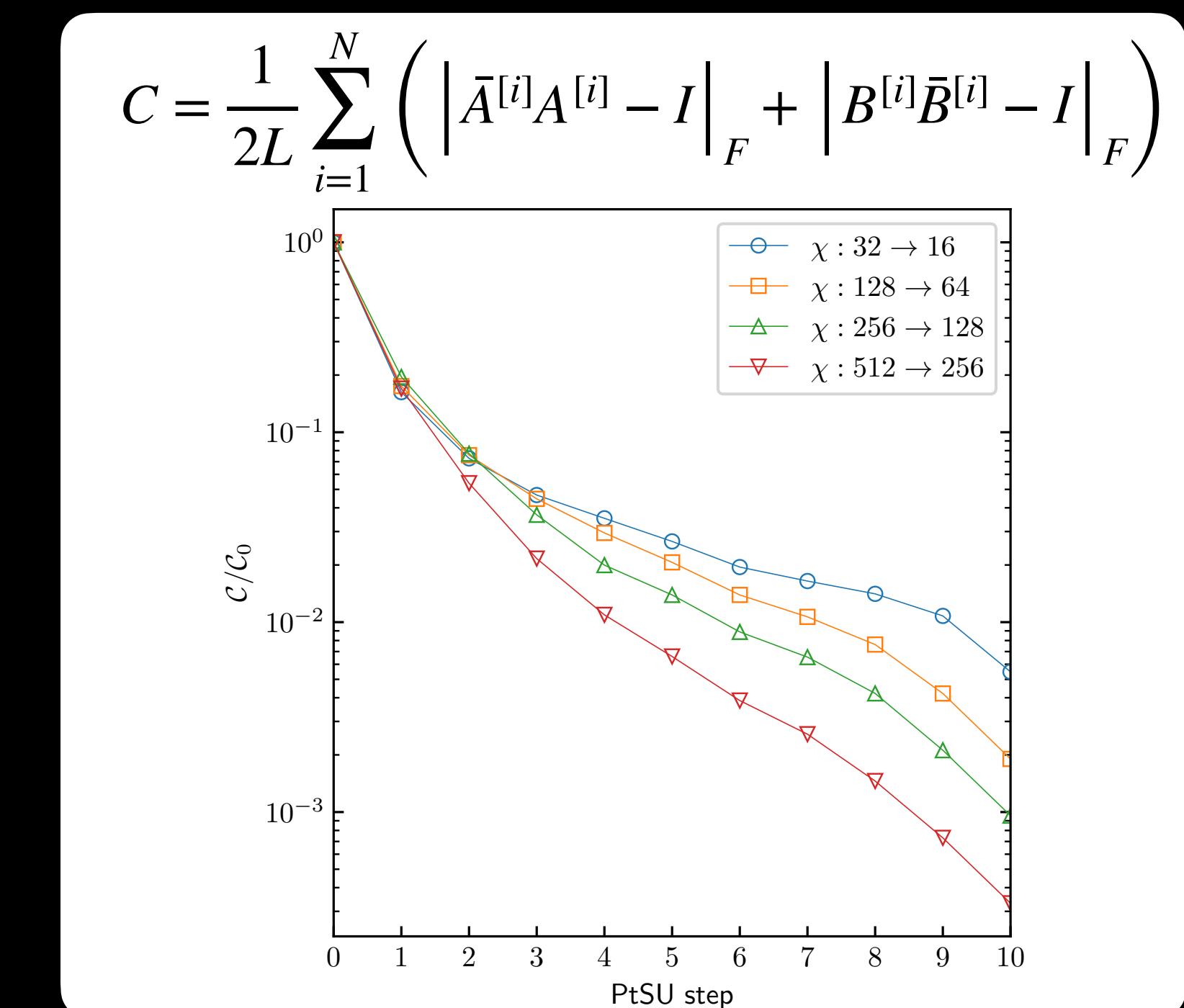
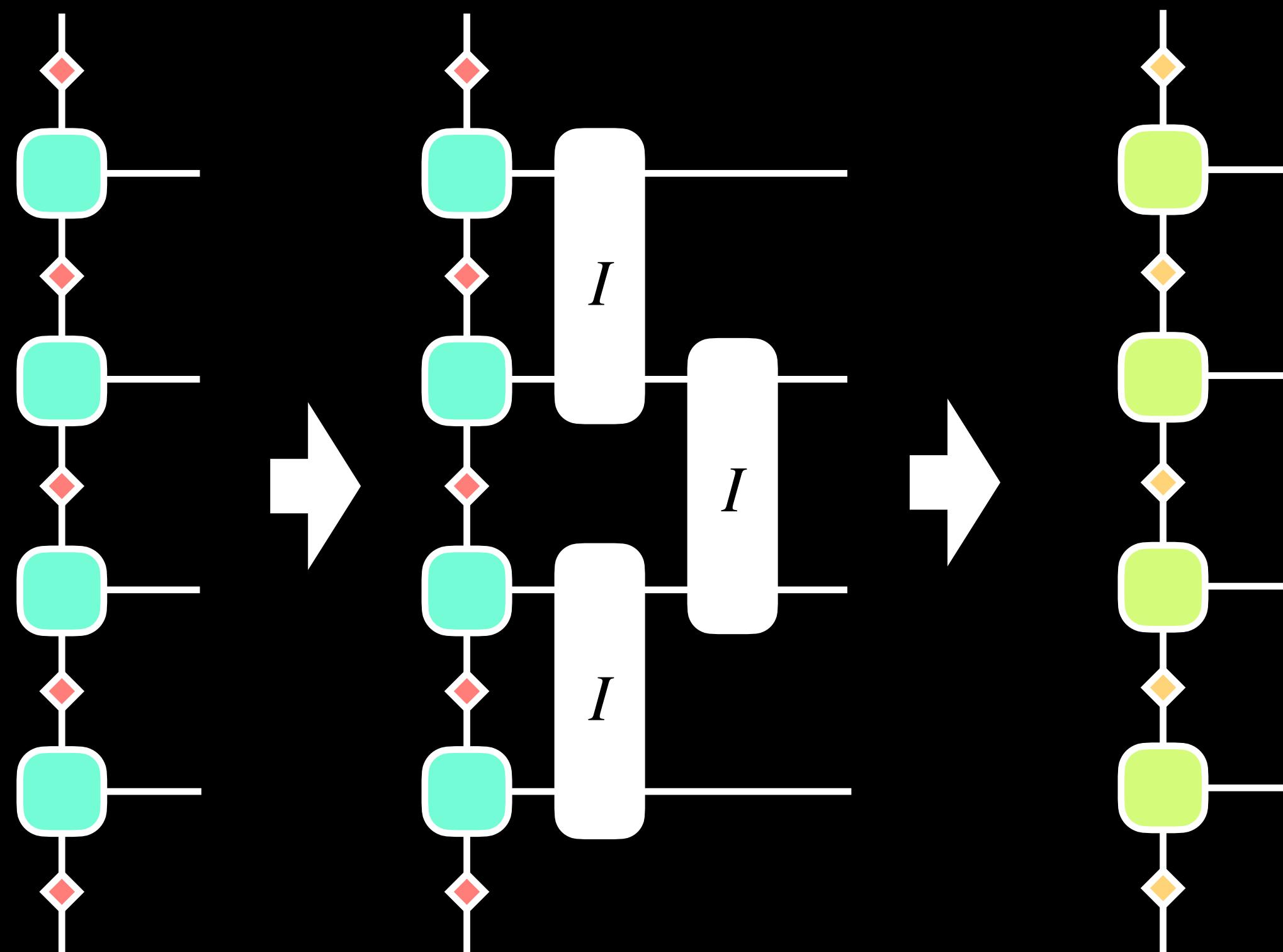


Restore the canonical form

Parallel trivial simple update (PtSU)



Equivalence between belief propagation and trivial simple update [R. Alkabetz and I. Arad, Phys. Rev. Research **3**, 023073 (2021)]



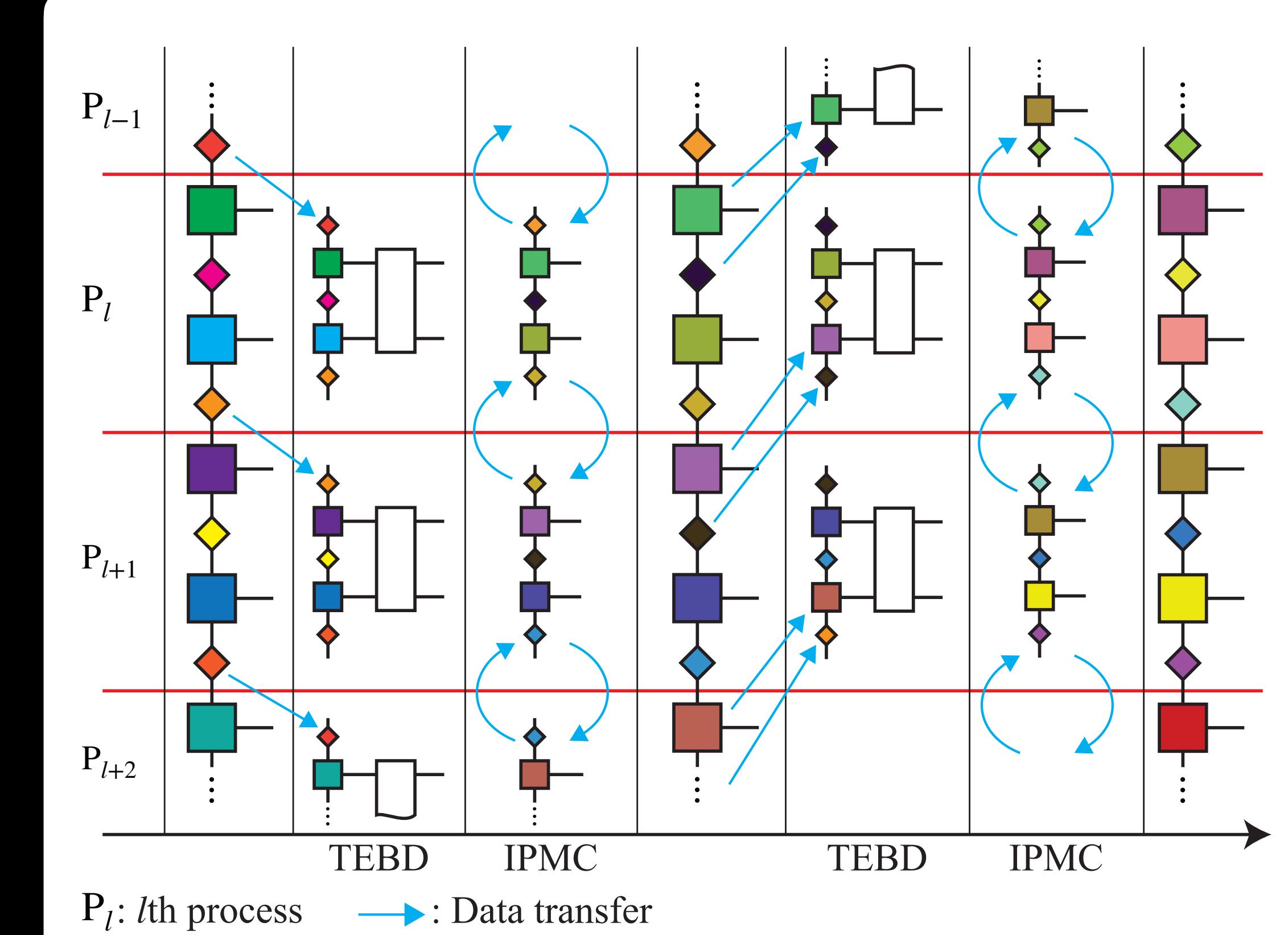
IPMC and pTEBD

IPMC: Improved parallel MPS compression

IPMC = **Parallel wavefunction norm stabilization**
+ **Parallel partial regauging**

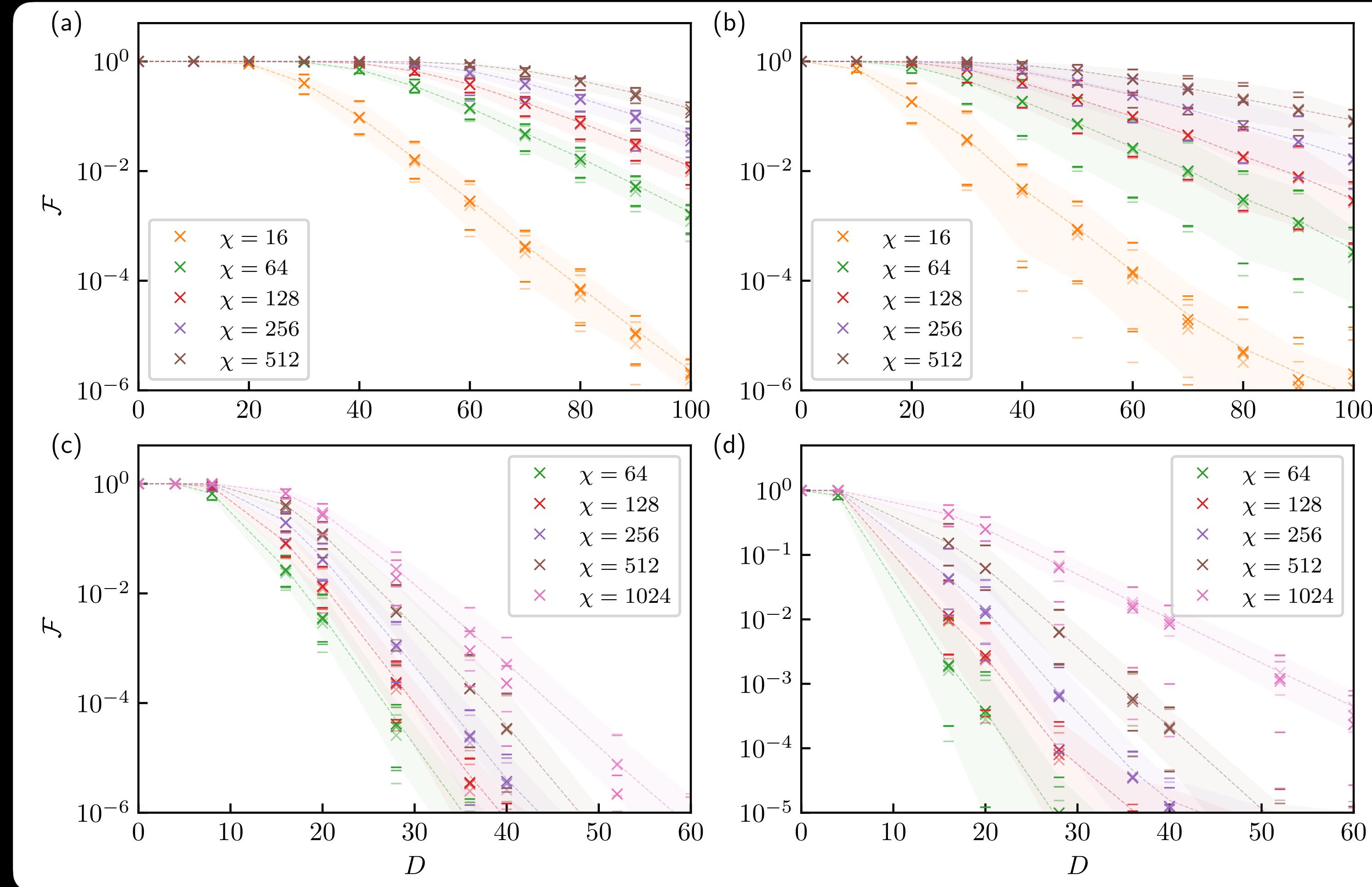
pTEBD = **Parallel gates applications**
+ **Parallel bond dimension compression**
+ **IPMC**

Only neighboring communications !



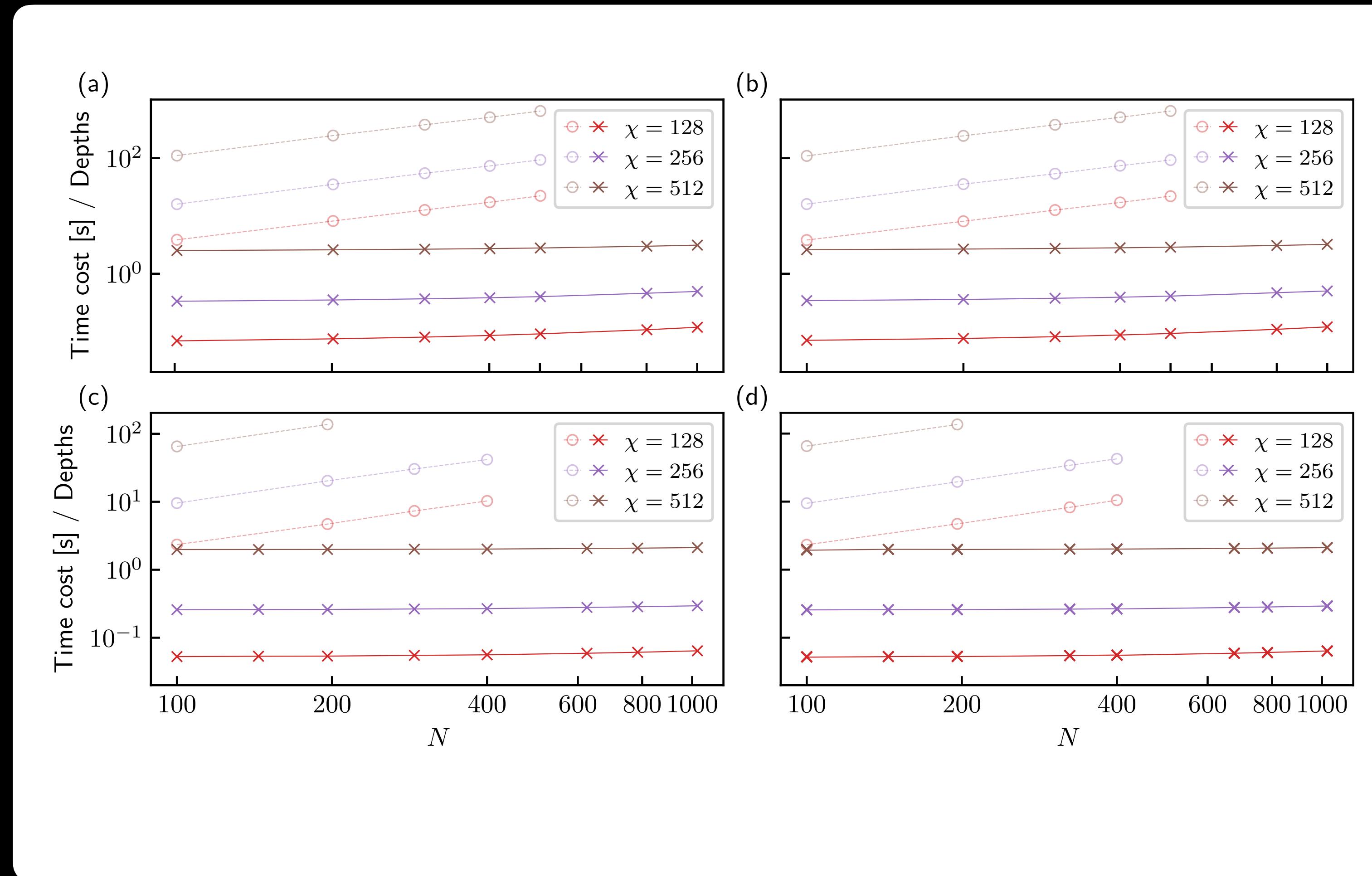
pTEBD: Accuracy

$$\mathcal{F} = \left| \langle \Psi_{\text{exact}} | \Psi(\chi) \rangle \right|$$



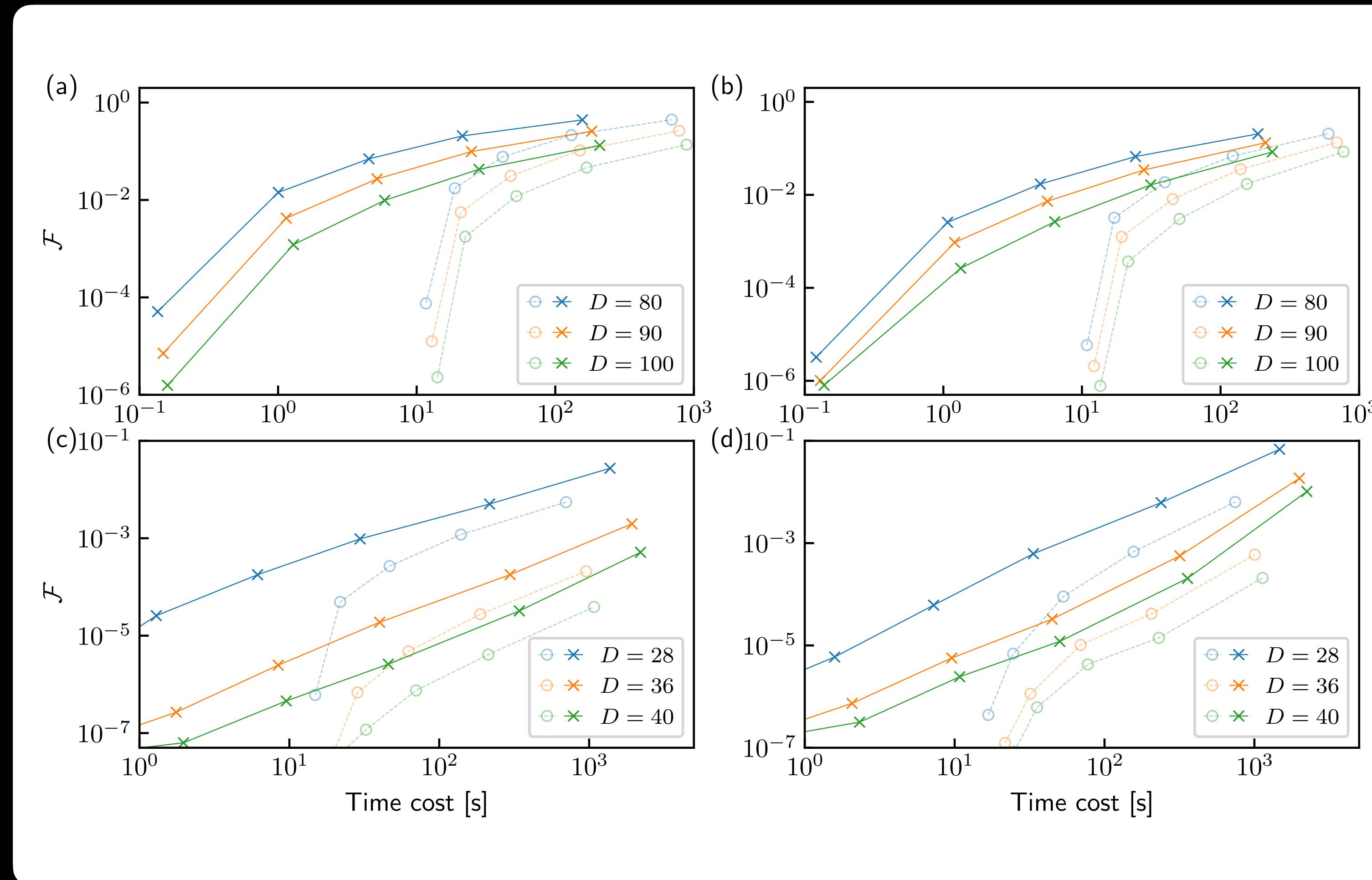
$$\mathcal{F}_{\text{pTEBD}} \sim \mathcal{F}_{\text{SeqMPS}}$$

pTEBD: Parallel performance



**Perfect
weak scaling**

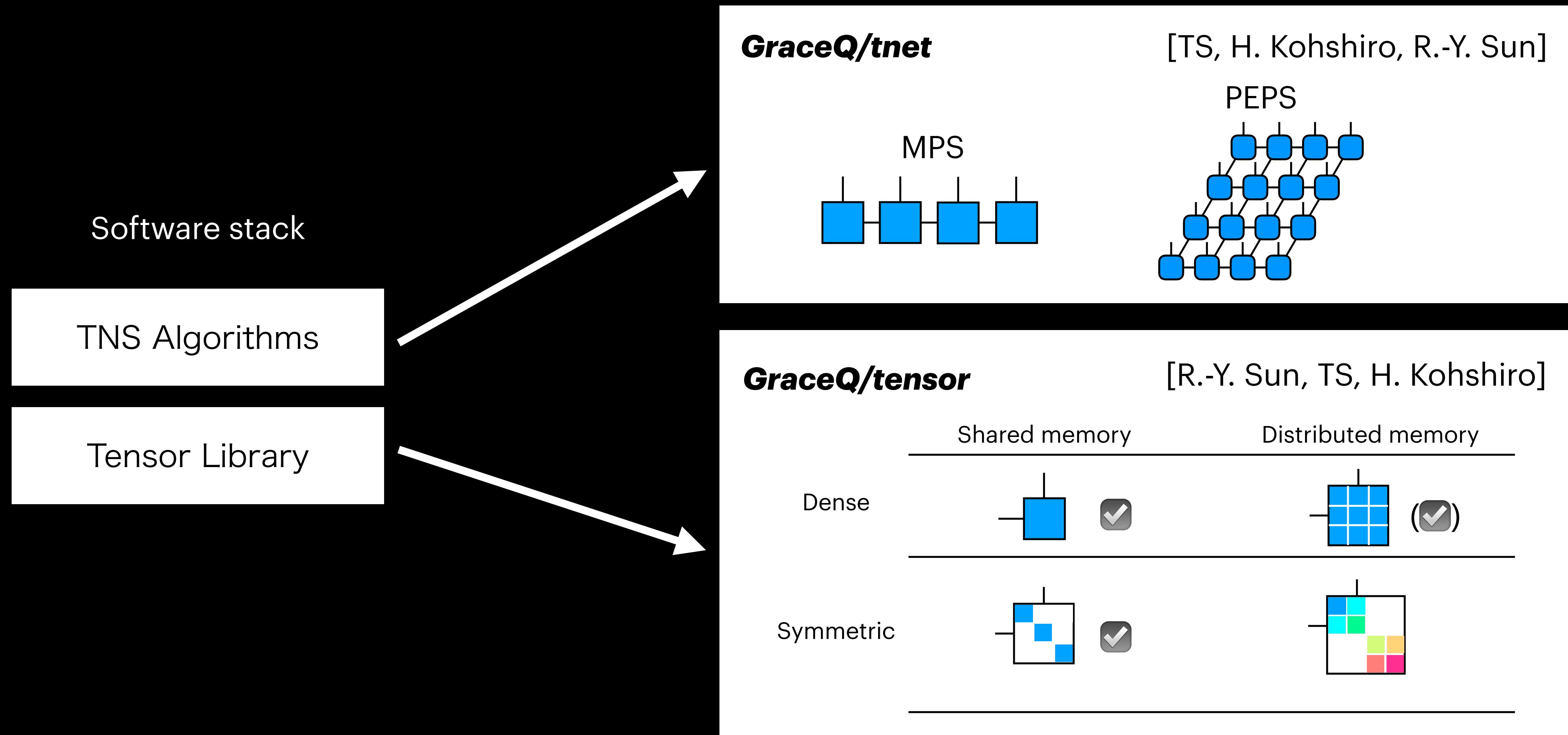
pTEBD: Cost v.s. Performance



$$T_{\text{pTEBD}} \sim T_{\text{SeqMPS}}/N$$

TNS Software

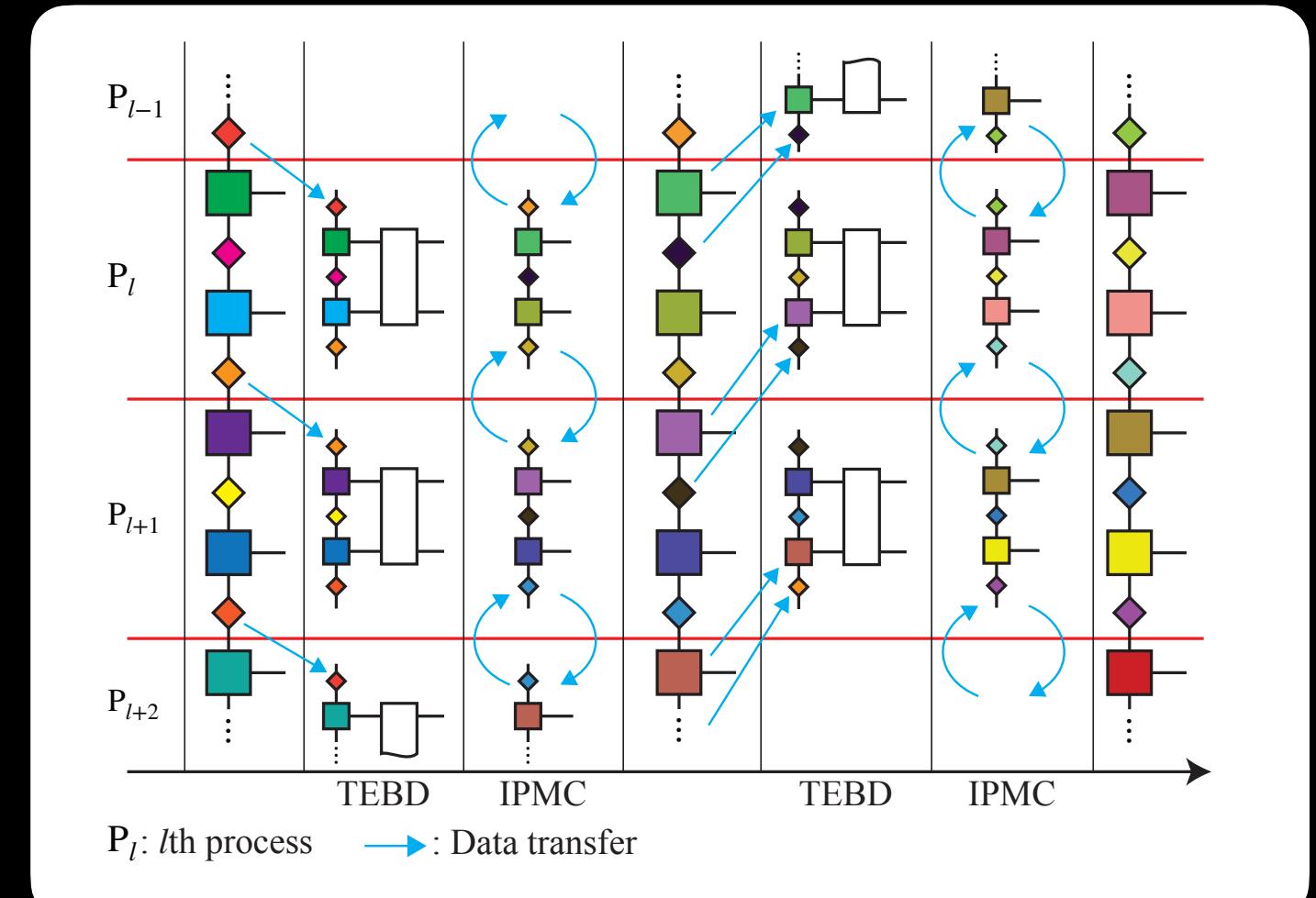
Goal: Develop high-performance scalable TNS software for Fugaku



Summary

Development of tensor network method for parallel computer

- Keeping canonical gauge structure is crucial and difficult point in parallelization.
- We introduced a scheme to improve the performance of parallel TEBD: improved parallel MPS compression (**IPMC**).
- Partial gauge fixing by parallel trivial simple update (**PtSU**) may be useful.



[R.-Y. Sun, T. Shirakawa, S. Yunoki, arXiv:2312.02667 (2023)]

Thank you for your attention!