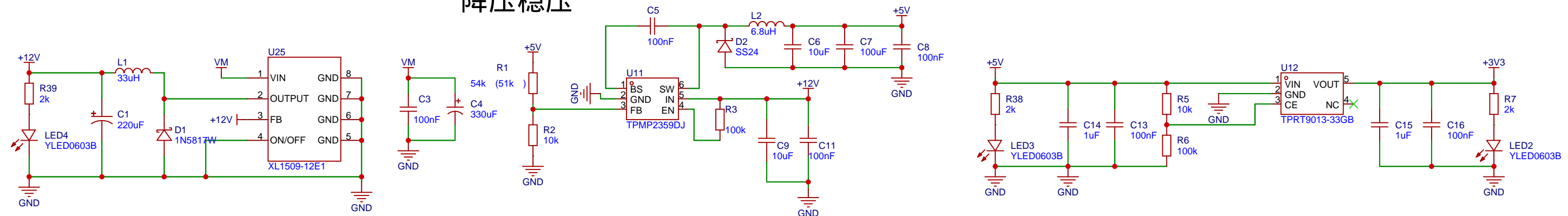
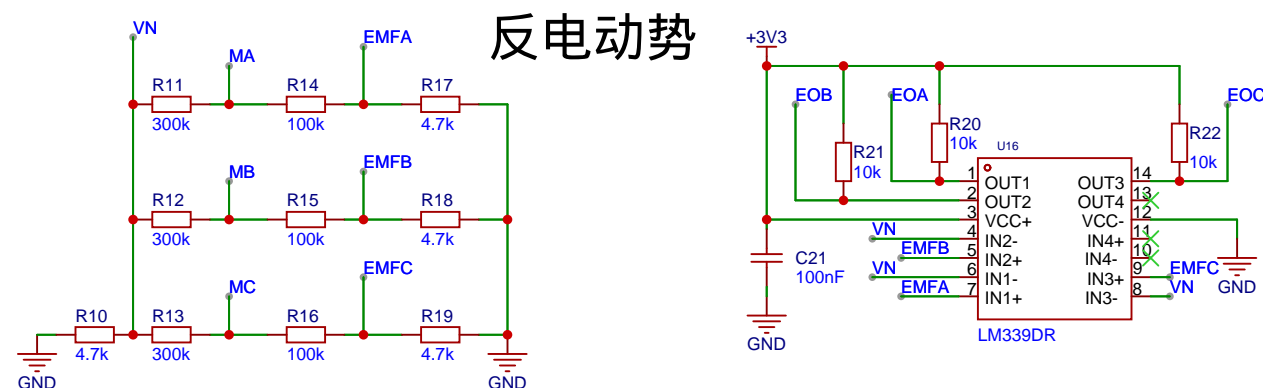


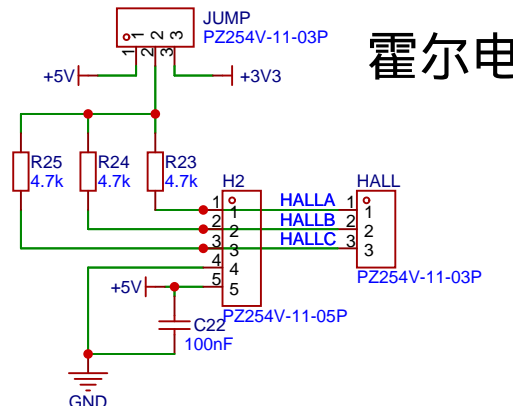
降压稳压



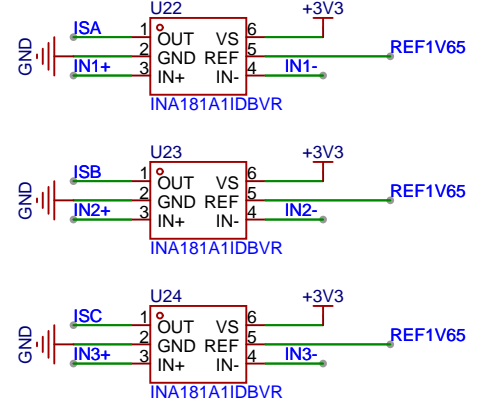
反电动势



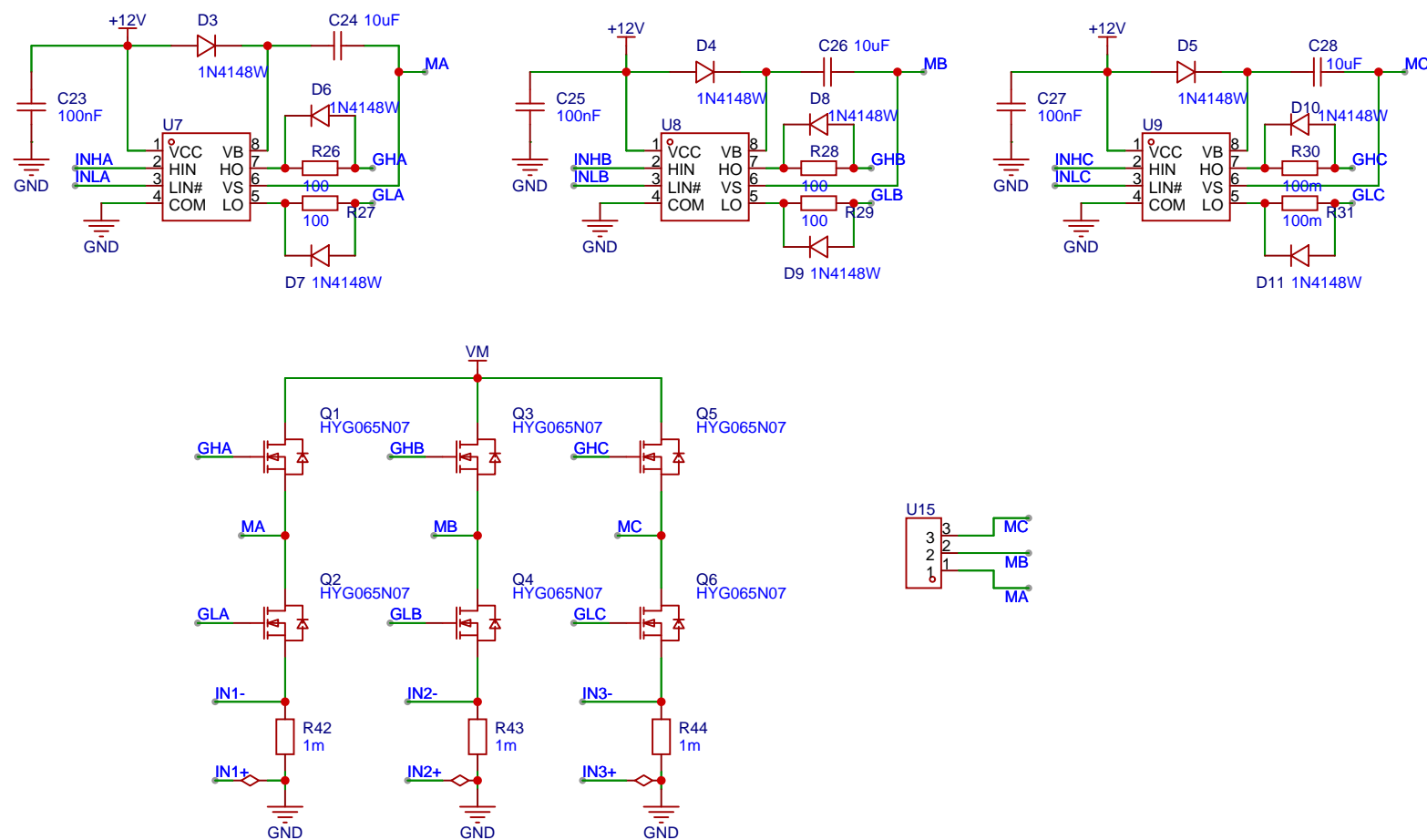
霍尔电路



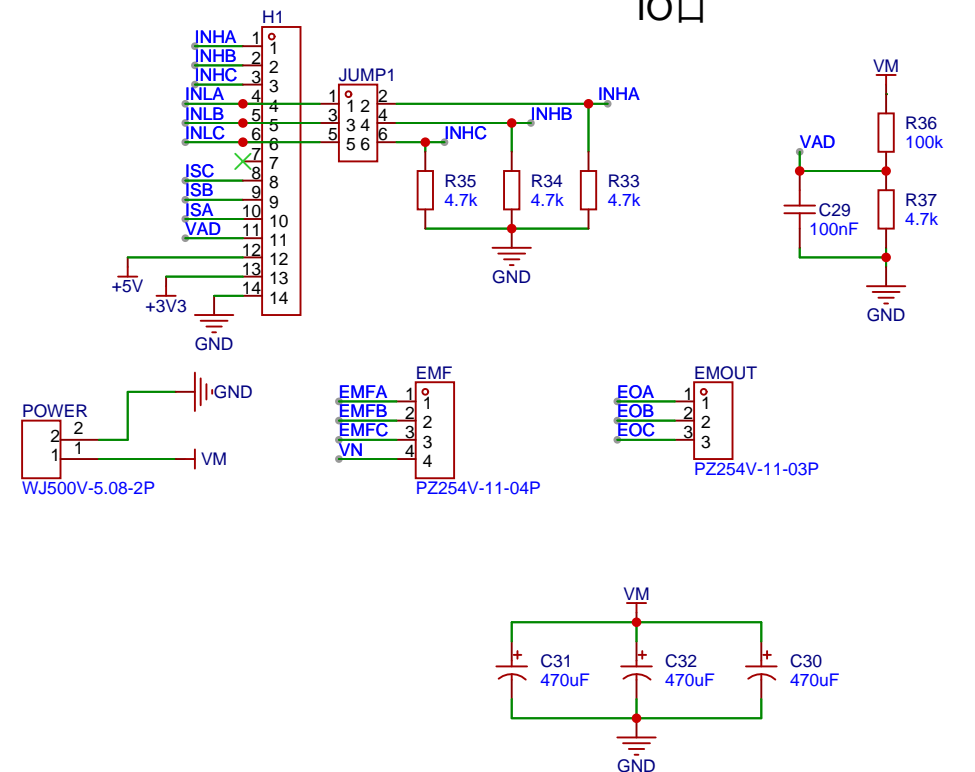
电流采样



逆变电路



IO口



Date:20250101

原理图	内置电流采样4181驱动板_9	更新日期	2025-06-13
		创建日期	2025-05-10
图页	P1	物料编码	
绘制		低侧采样驱动开发板	
审阅			
		版本	尺寸
		V1.0	A4
		页	1 共 1
		嘉立创EDA	