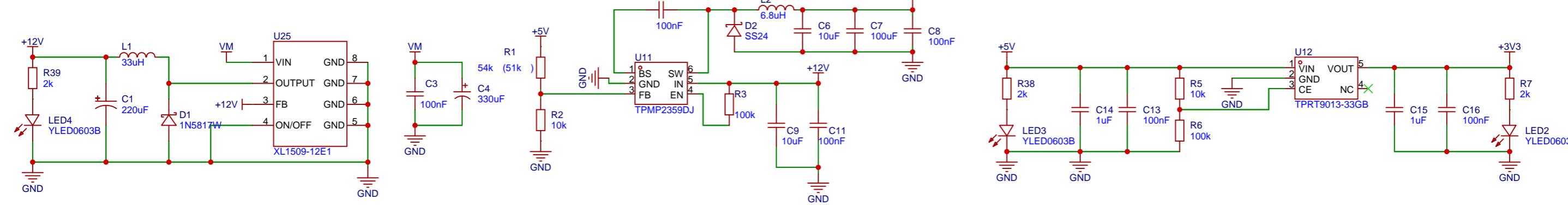
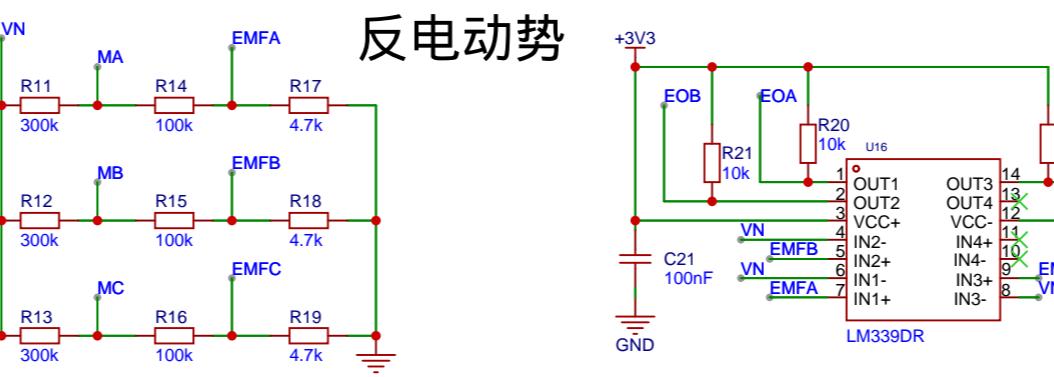


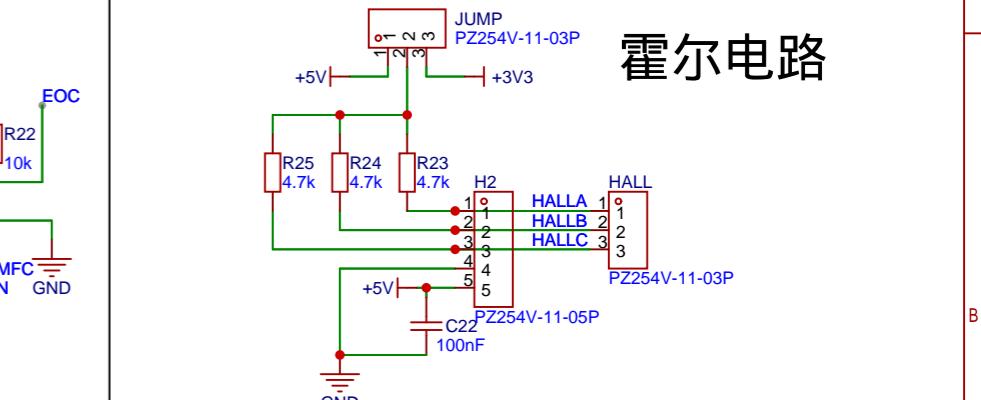
降压稳压



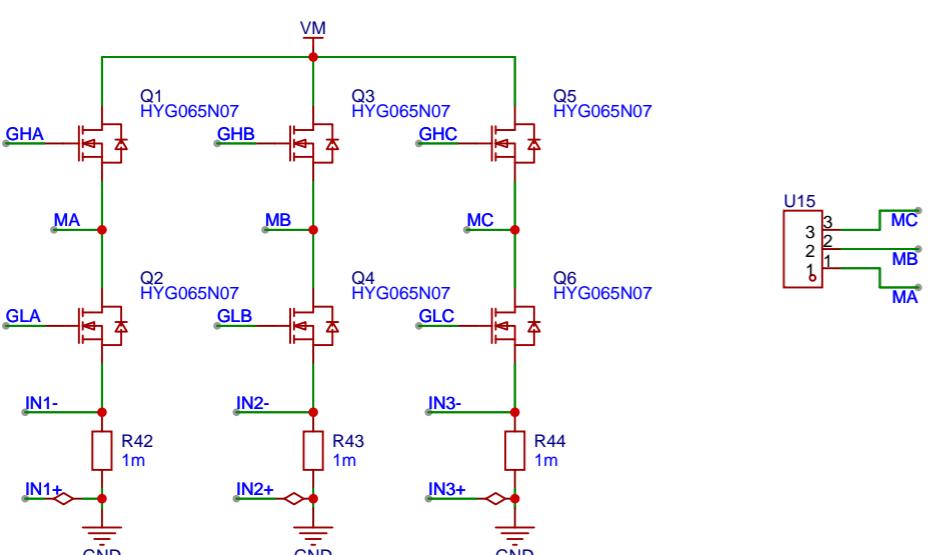
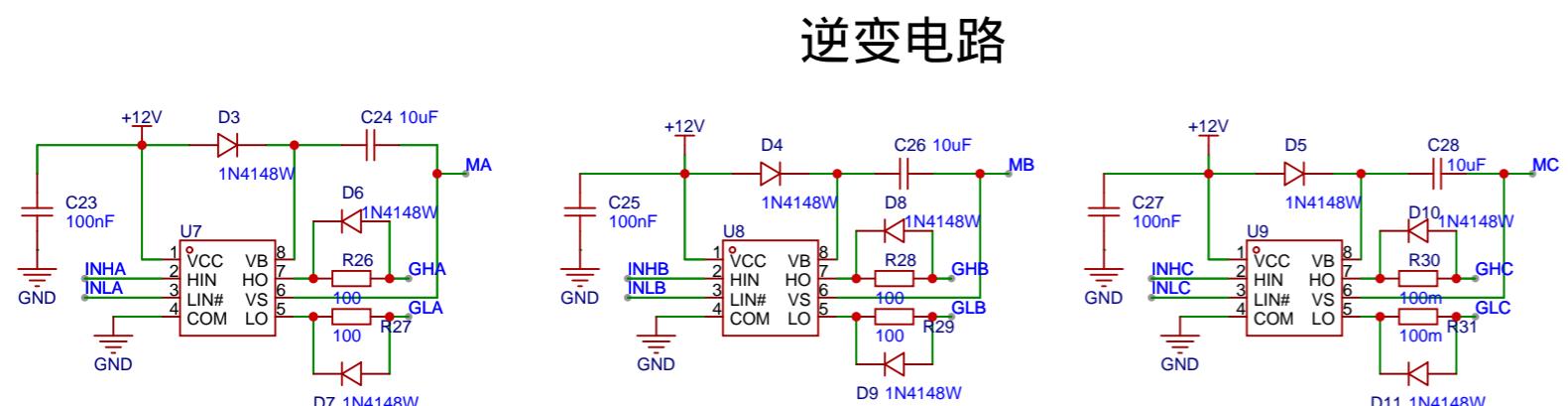
反电动势



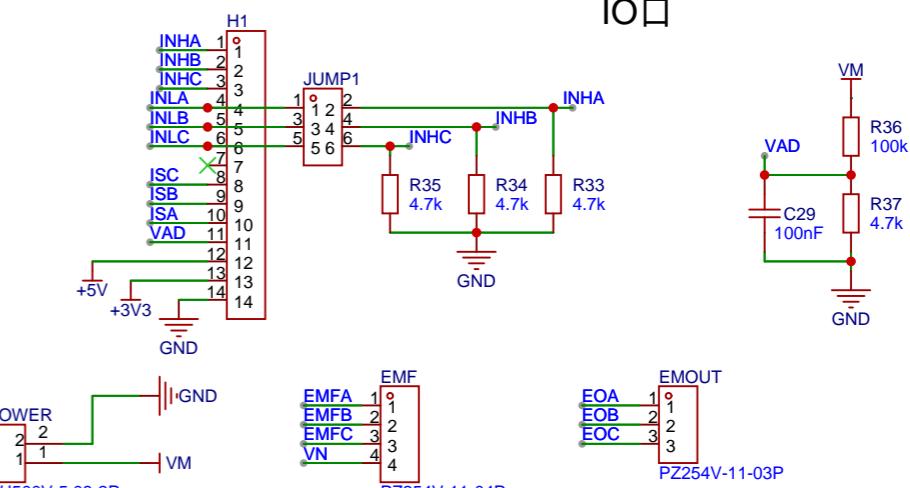
霍尔电路



逆变电路



IO口



Date:20250101

原理图	内置电流采样4181驱动板_9		更新日期	2025-06-13
图页	P1		创建日期	2025-05-10
绘制	低侧采样驱动开发板			
审阅				
版本	1		尺寸	1 共 1
			页	1
			嘉立创EDA	