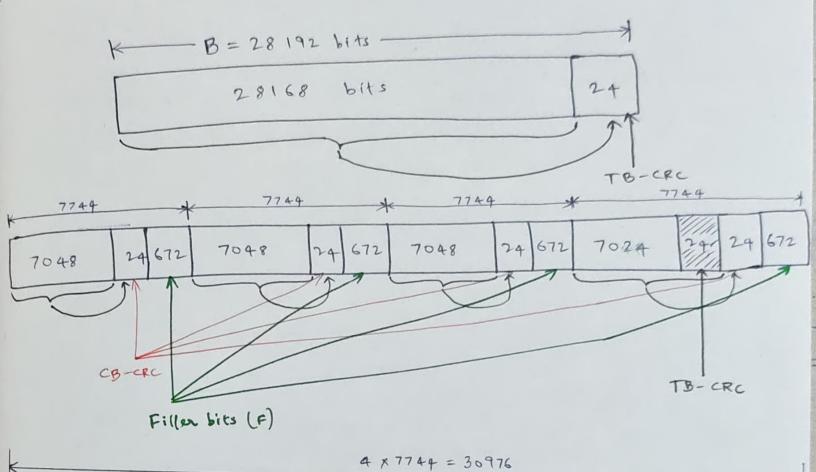
, our running example from last section.



- O Assume a wer is allocated 70 RB's over a slot of 14 Symbols.
- 0 Mcs-16 (16-QAM), which has a code vote of  $\frac{658}{1024} = 0.642$ .
- @ NO. of code blocks, C = 4;
- O Code block Size without filler bits K' = 7072 bits
- O code block size with filler bits K=7072 + 672 = 7744 bits.
- O CRC Size L = 24 bits, Lifting Size Zc = 352.

## LDPC coding in the standard

- O LDPC encoder input = K bits
  - O LDPC encoder output length

    N=66 Zc bits for bare graph 1

    N=50 Zc bits for bare graph 2
- O For our example, LDPC encoder output for each segmented code block

 $N_{r} = 66 \times 352 = 23232.$ 

K bits [L]PC 
$$66 \times 2c$$
 bits  $7744$  [Emoder]  $23\overline{2}32$ 

Code Rote,  $Y = \frac{K}{66 \times 2c} = \frac{7744}{23232} = \frac{1}{3}$ 

- Filler bits are replaced with Zeros while encoding and added but after encoding
- Rate of each code bloch =  $\frac{7.744}{23232} = \frac{1}{3}$

mother-code-vate

- O. Bits input to LDPC encoder are denoted as Co, C, C, Cz, ..., Cx-1. Subscript r is dropped while feeding data to LDPC encoder.
- O Bits output from LDPC encoder are denoted as do, di, d2,..., dN-1.

