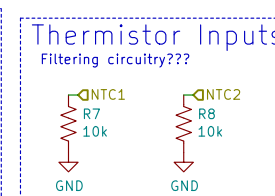
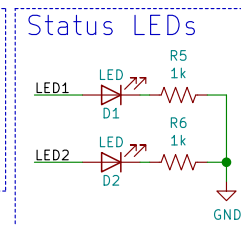
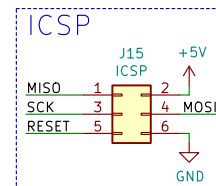
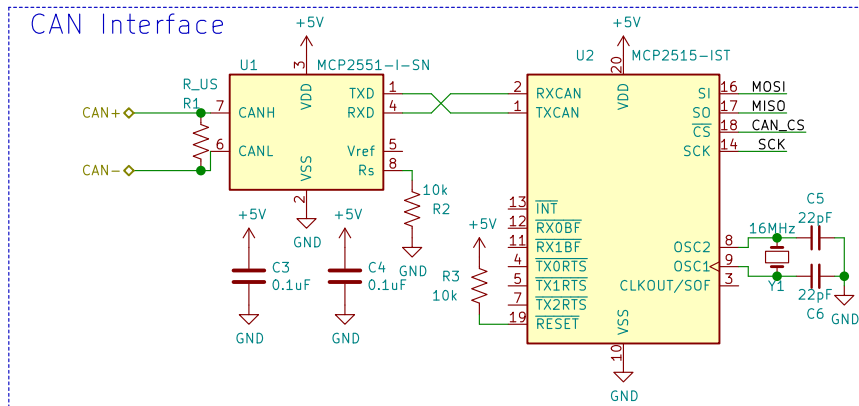
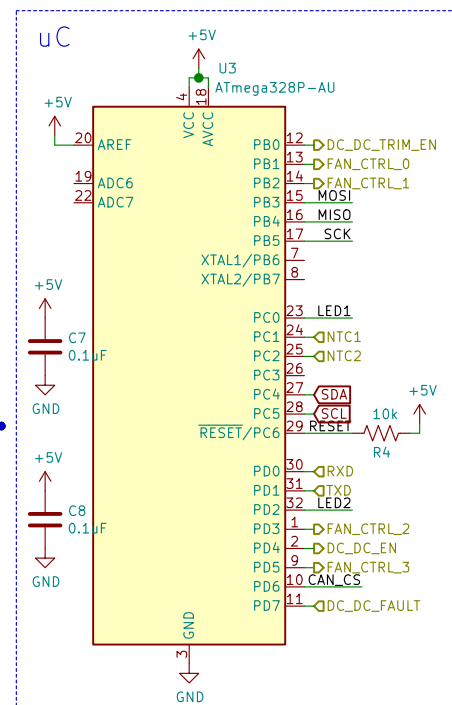


#



Tight constraints on which pins can be which. Hoping this isn't a routing nightmare.



Sheet: /uC+CAN/
File: uC+CAN.sch

Title:

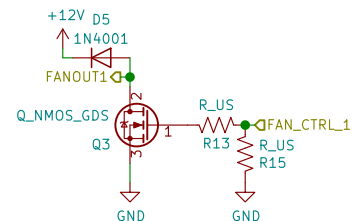
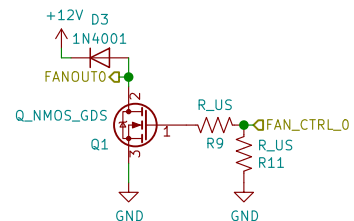
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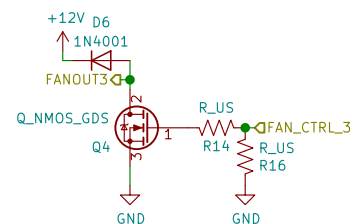
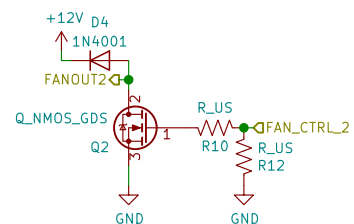
KiCad E.D.A. kicad (5.0.0)

Rev:

Id: 2/5



Caps across fan outputs a good idea maybe?



Sheet: /fanControl/
File: file5C5EE840.sch

Title:

Size: A4

Date:

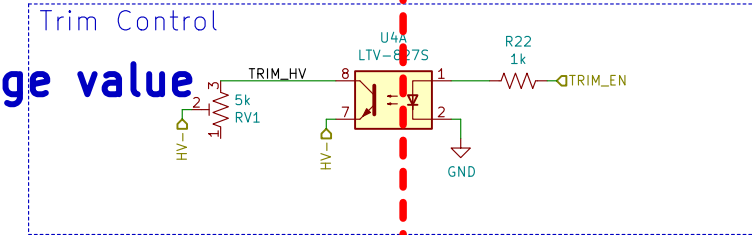
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Rev:

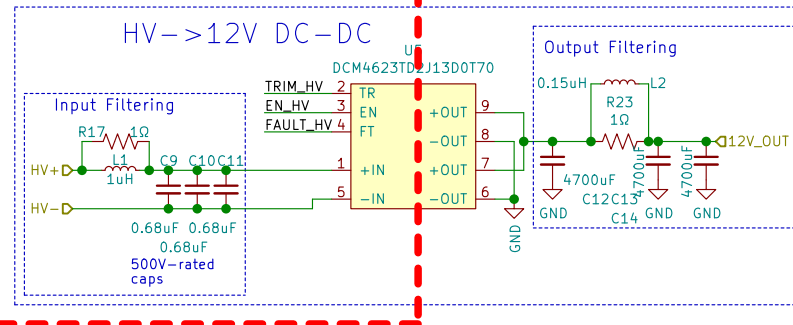
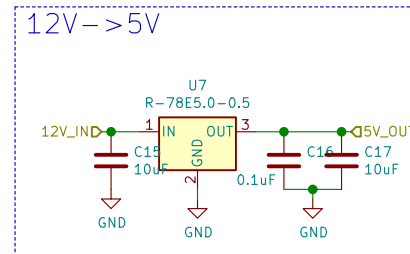
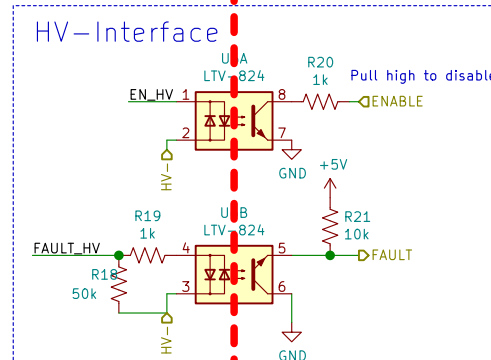
Id: 3/5

Fix all the HV/LV interface shit

May want to change value



HV
Danger!



Sheet: /DC->DC Converters/
File: file5C667FAE.sch

Title:

Size: A4

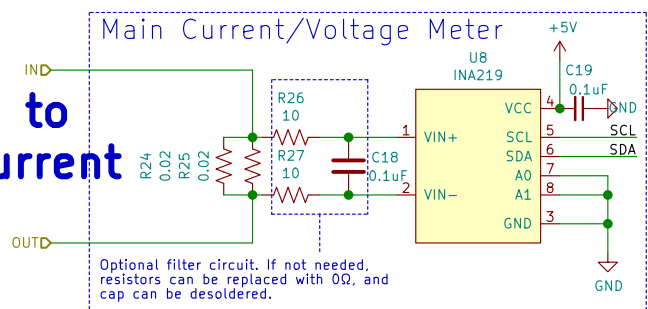
Date:

KiCad E.D.A. kicad (5.0.0)

Rev:

Id: 4/5

May want to
change value to
better suit current
range.



Sheet: /meter/
File: file5C4BAF40.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.0.0)

Date:

Rev:
Id: 5/5