



U1

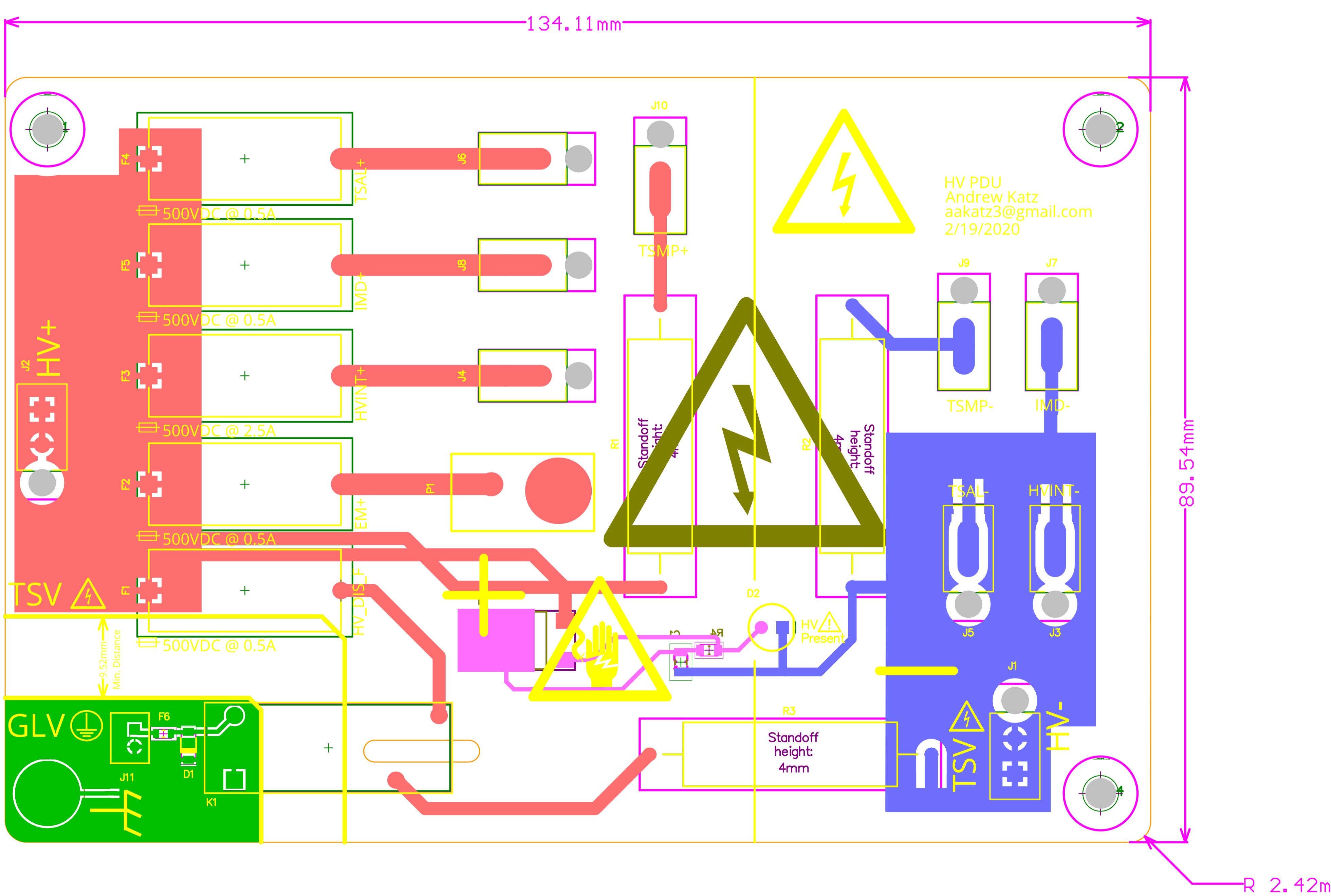
Microchip

1

R4

C1

SHDN+
GND


Title: HV PDU.PcbDoc

PDU for the HU Box

 Desc: For CS4.5
Designed to supply low current HV to boards that need it

Size: Letter Auth: Andrew Katz Proj: HU PDU

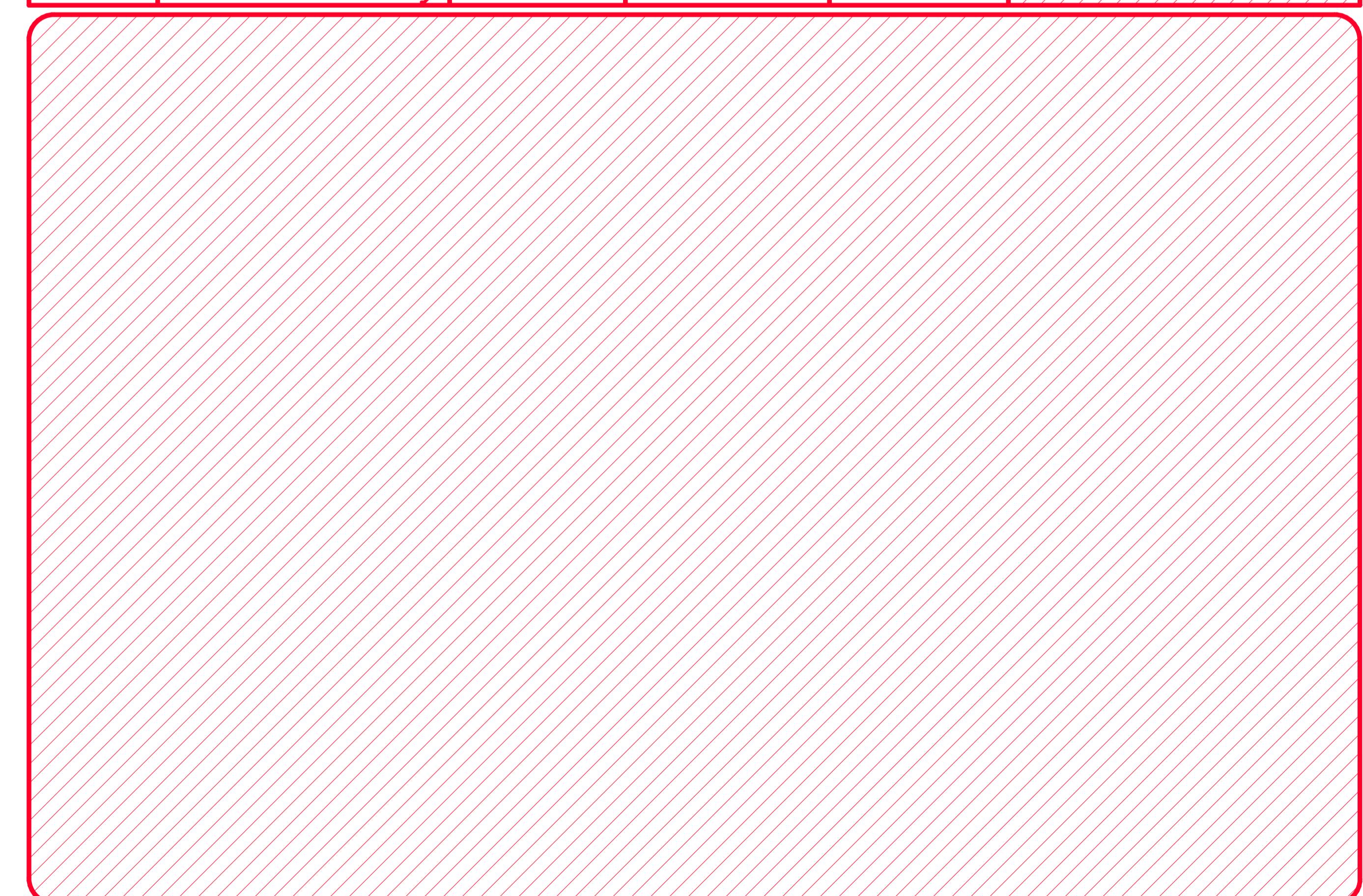
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File: C:\git\AERO_2019-2020\HV_PDU\HV_PDU.PcbDoc

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Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template
X	1	70.00mil (1.778mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c279h178
+	1	142.00mil (3.607mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c762h361
□	1	213.00mil (5.410mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c541h541z777x777
◊	2	27.56mil (0.700mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)
X	3	142.00mil (3.607mm)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c356hn361m561
▽	8	35.43mil (0.900mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)
O	10	125.00mil (3.175mm)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c318hn318m334
▽	14	51.18mil (1.300mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)
□	20	55.00mil (1.397mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)
60 Total								

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	SM-001	1.00mil	4	
1	Top Layer	Copper	1.40mil		
	Dielectric 1	FR-4	58.00mil	4.2	
2	Bottom Layer	Copper	1.40mil		
	Bottom Solder	SM-001	1.00mil	4	
	Bottom Overlay				



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PDU for the HV Box

For CS4.5

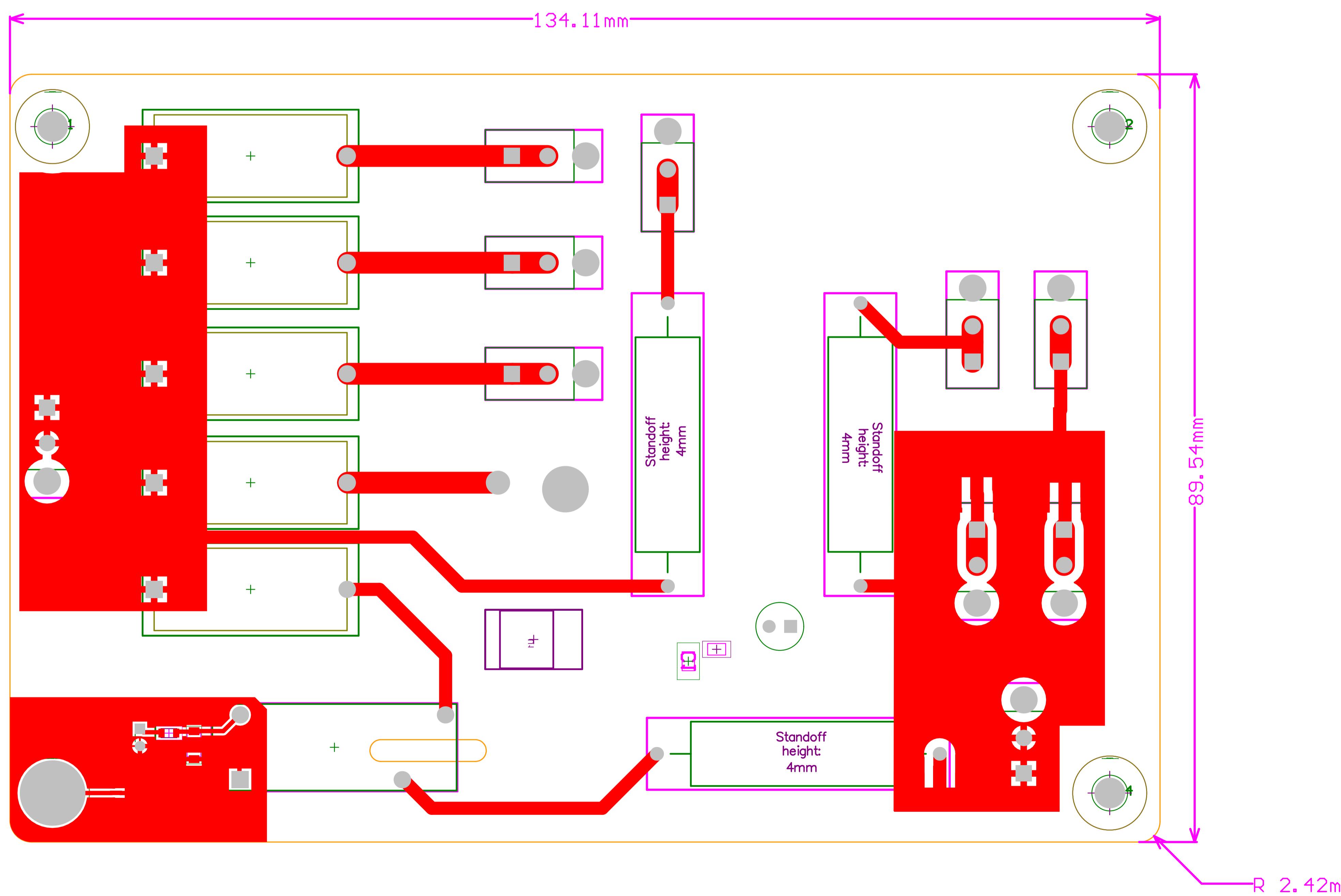
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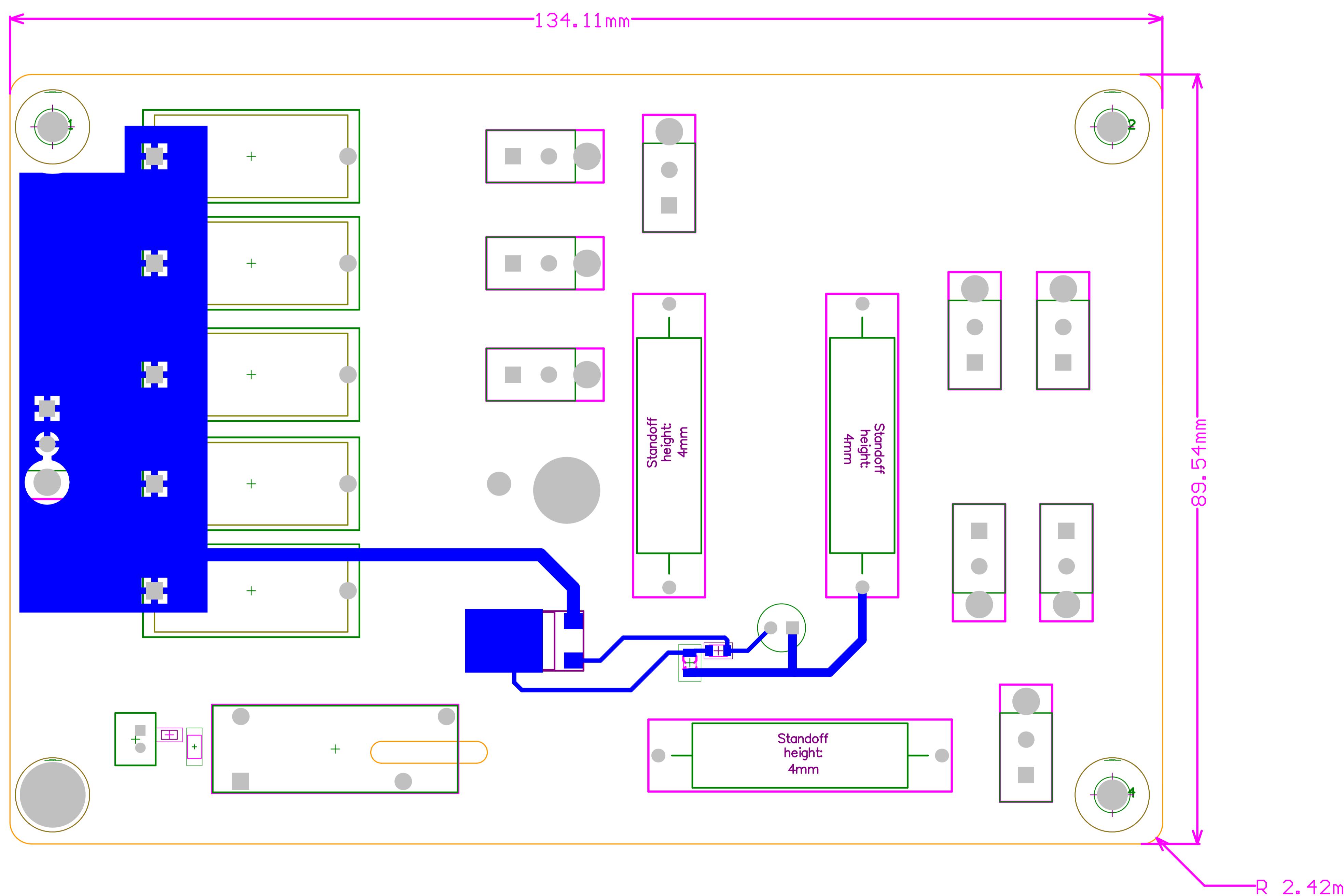
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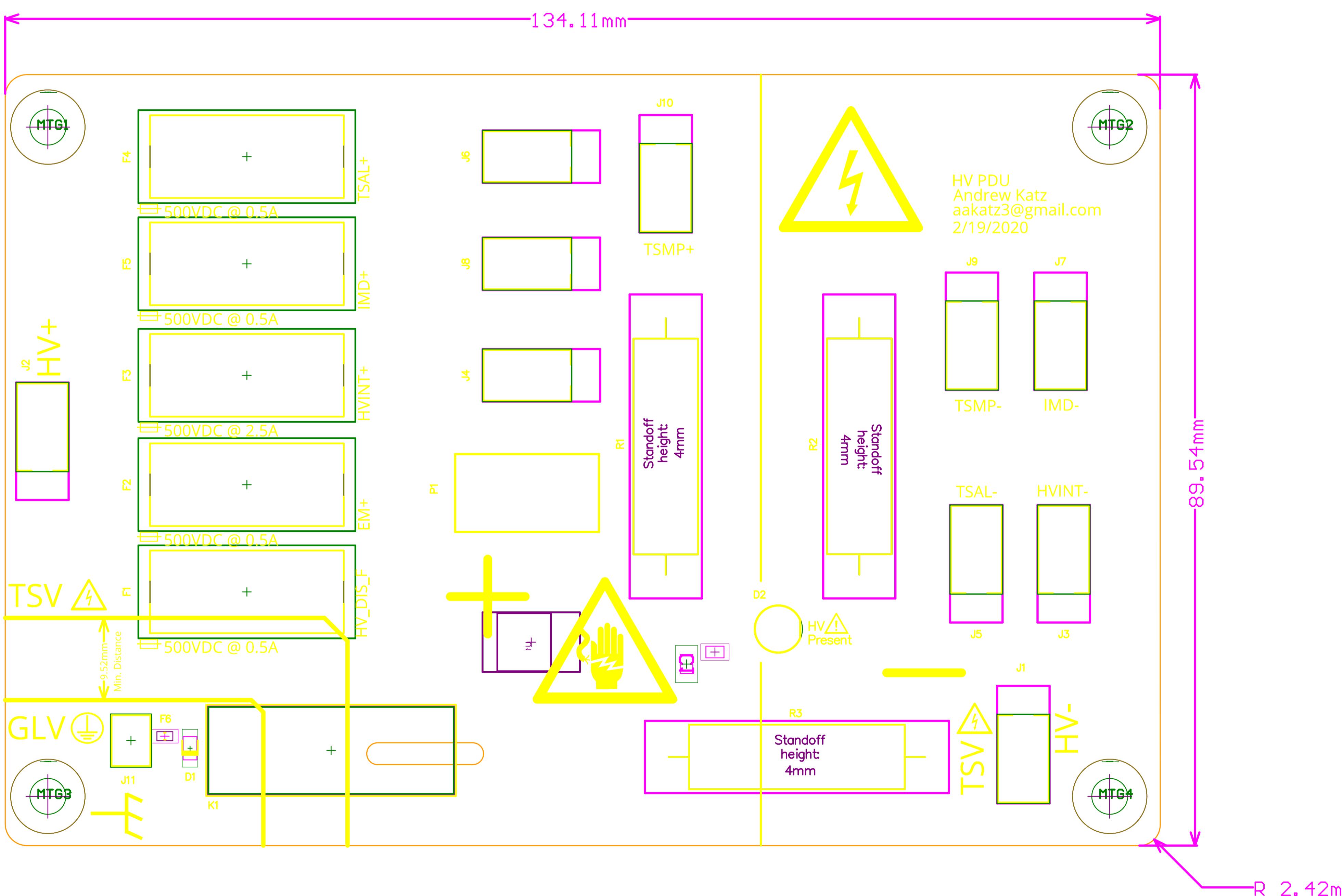
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Proj: HV_PDU

Date: 2/22/2020

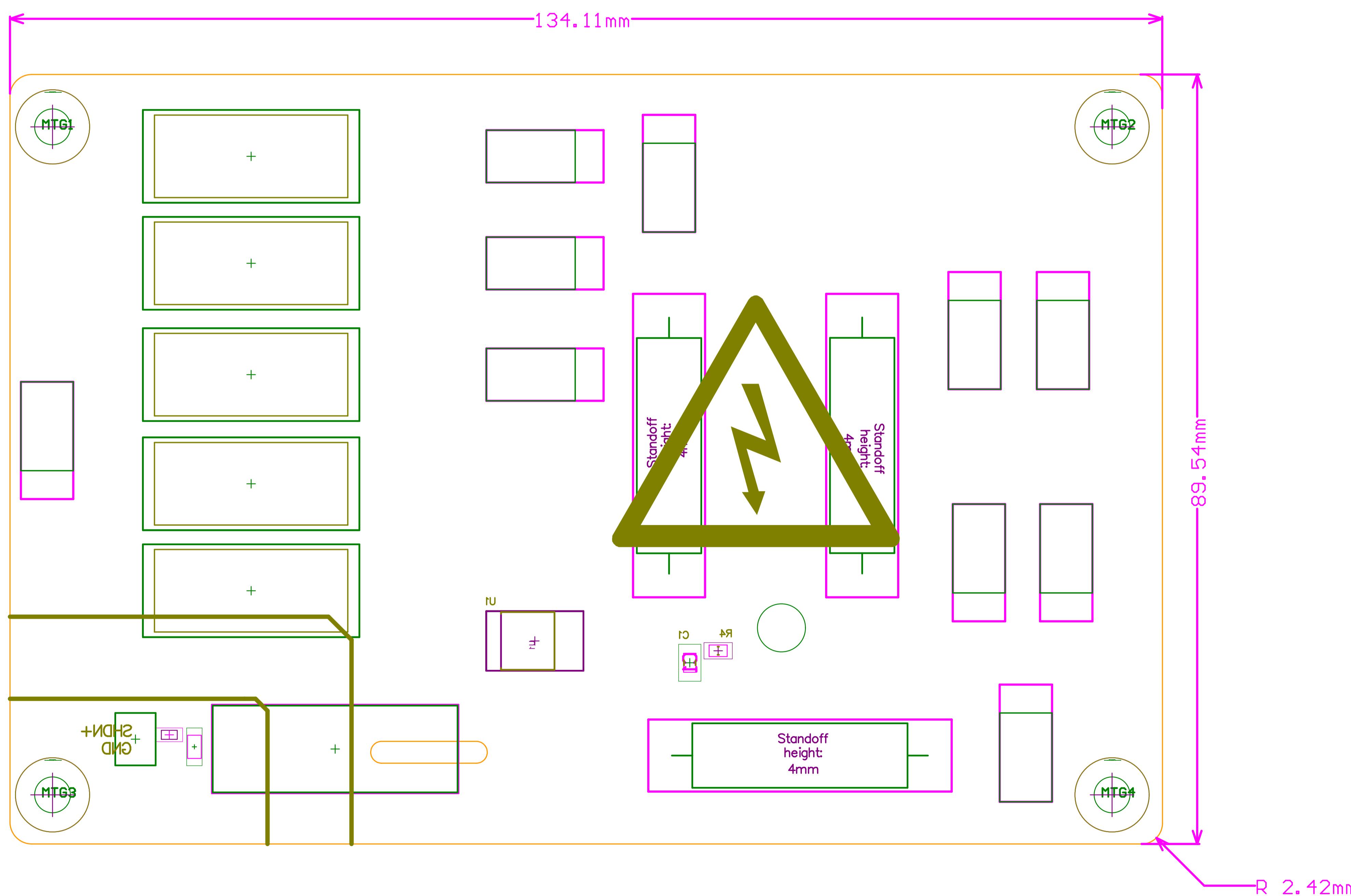
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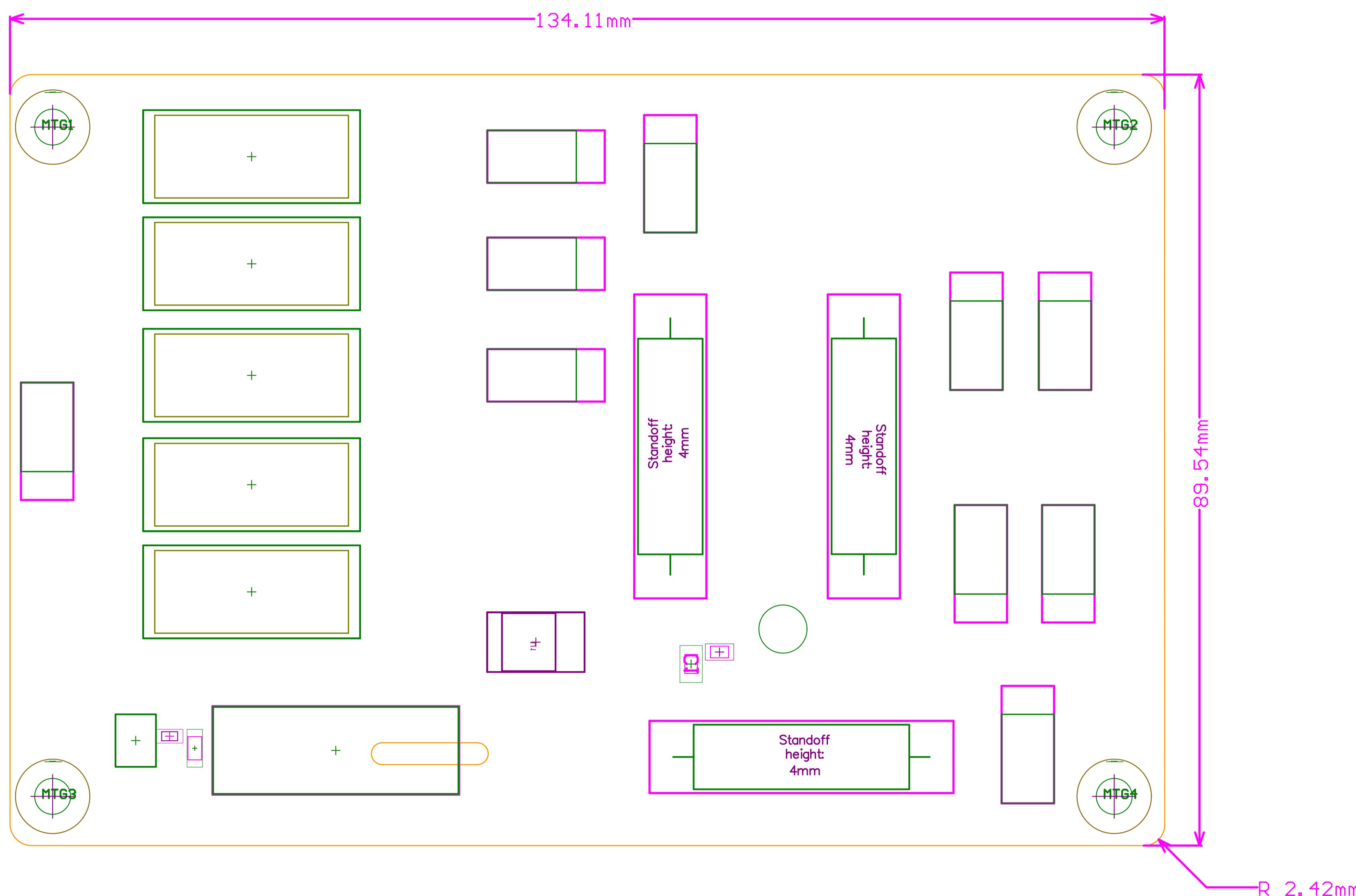
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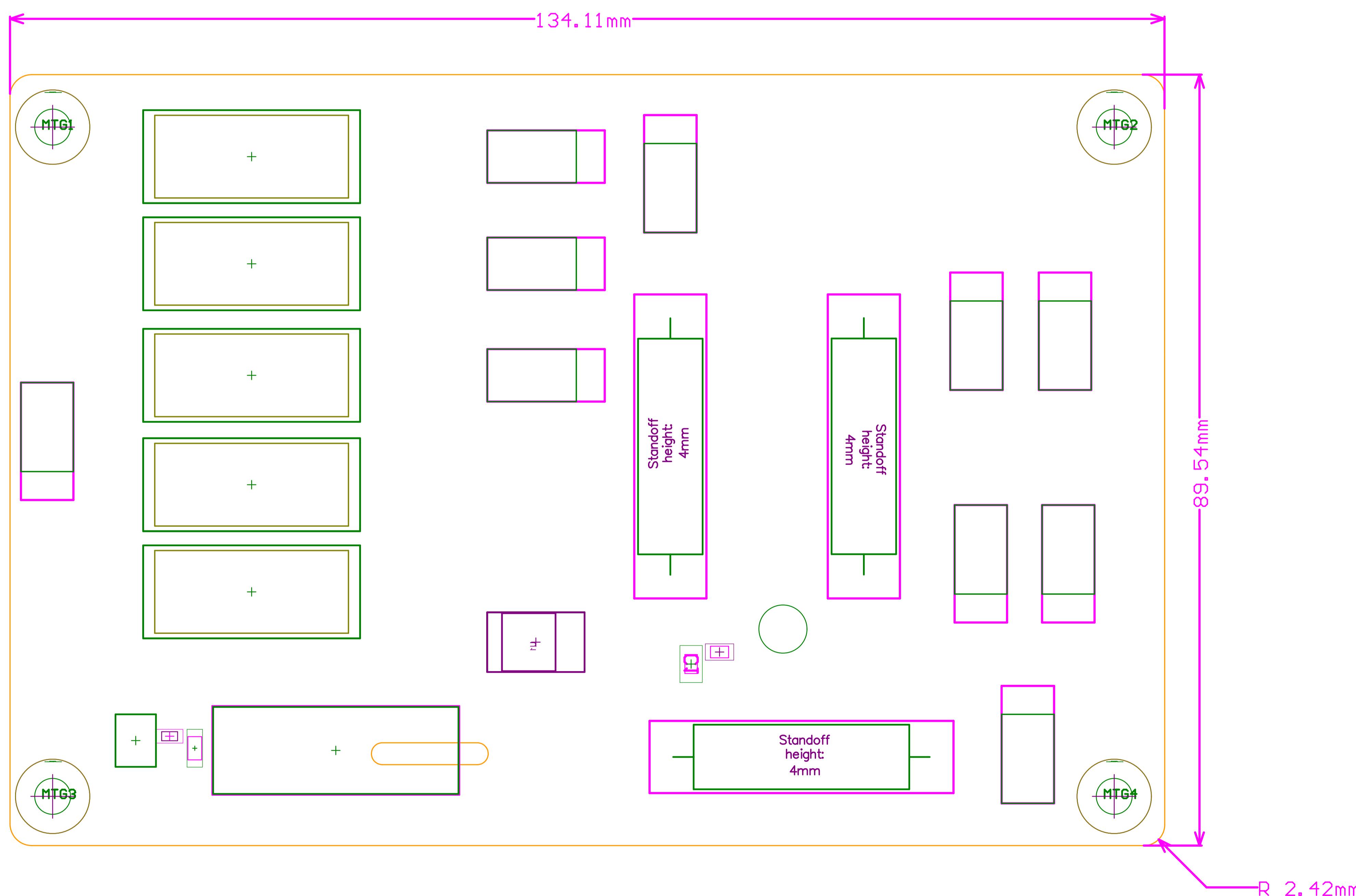
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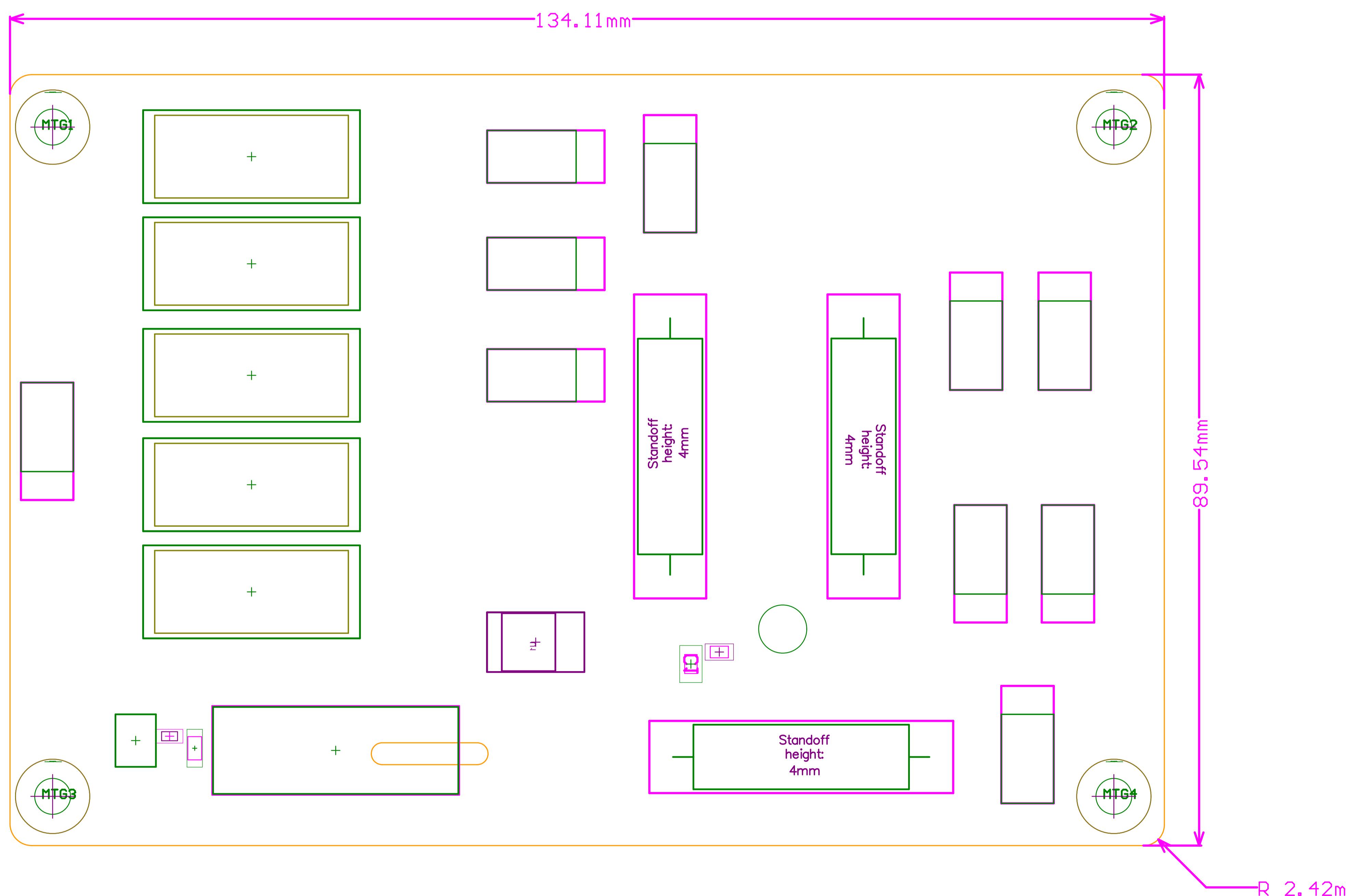
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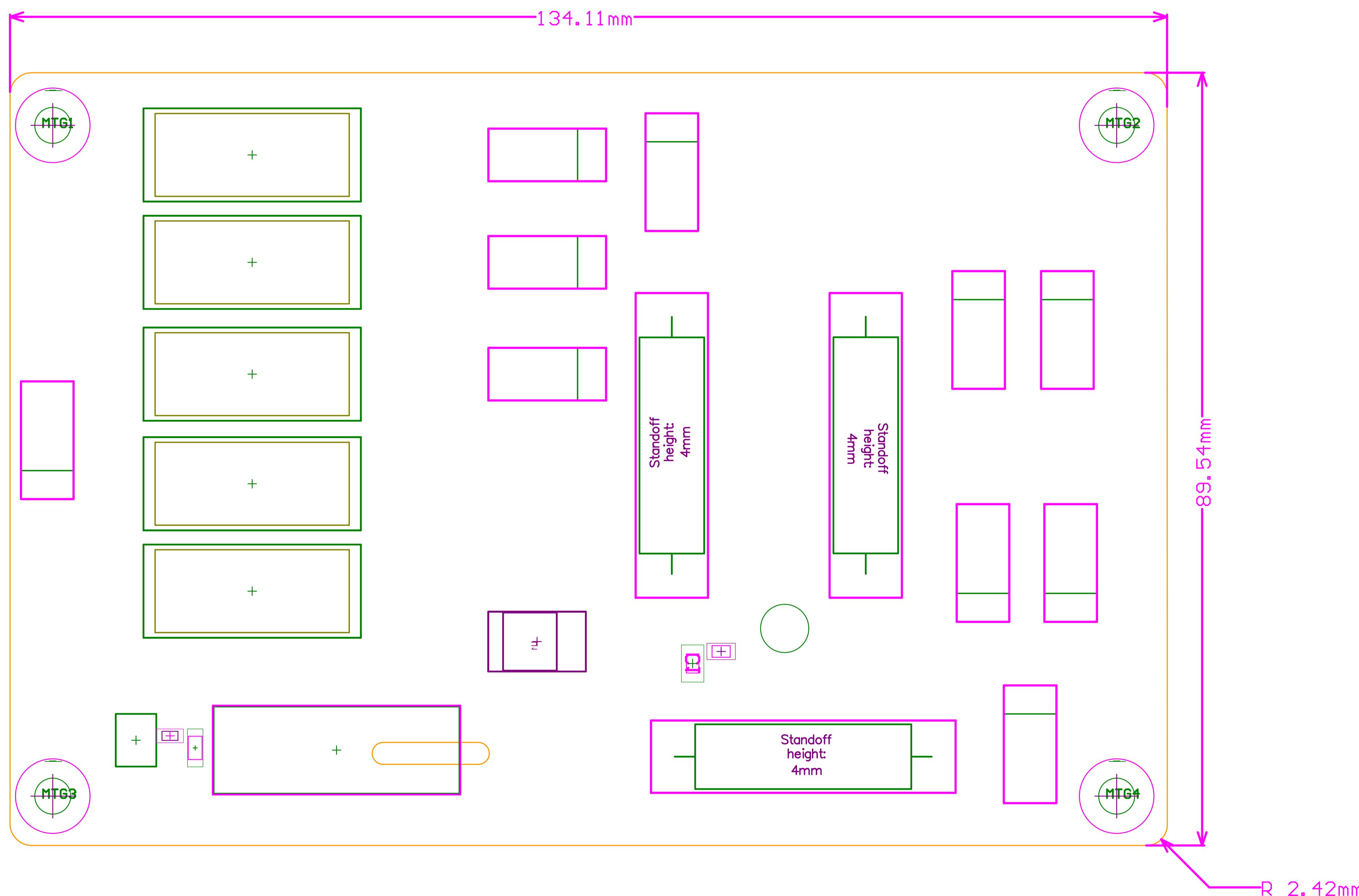
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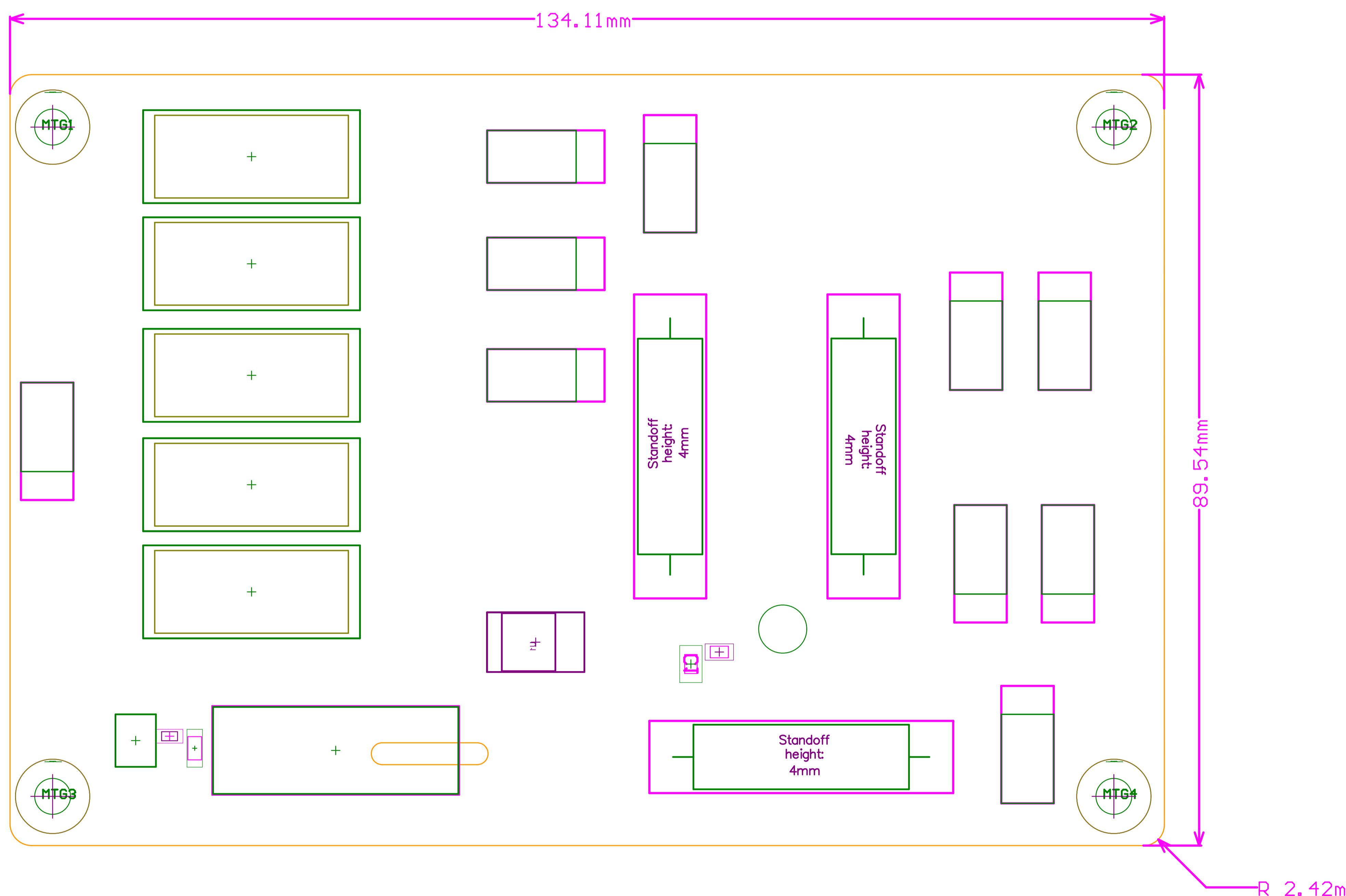
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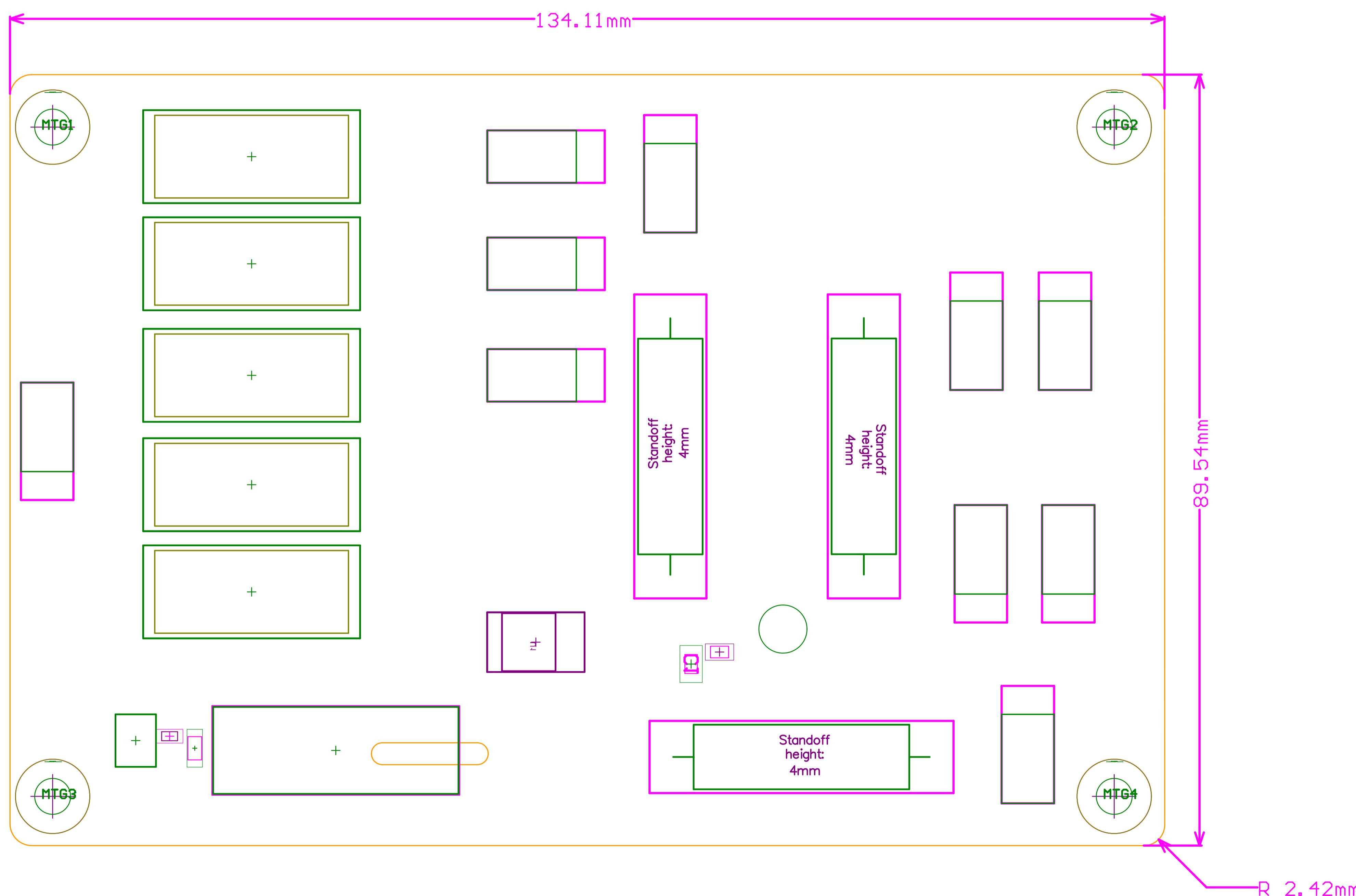
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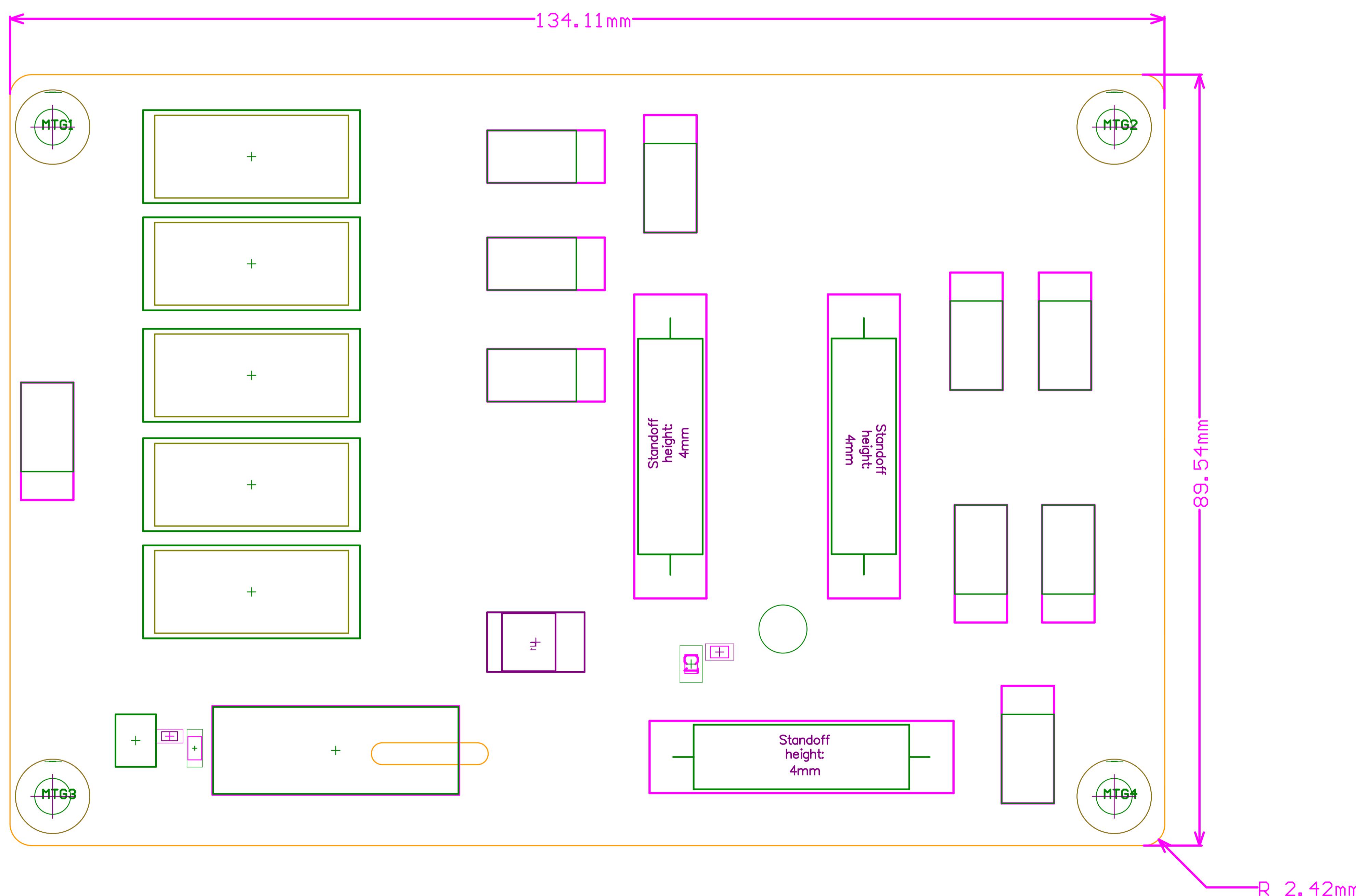
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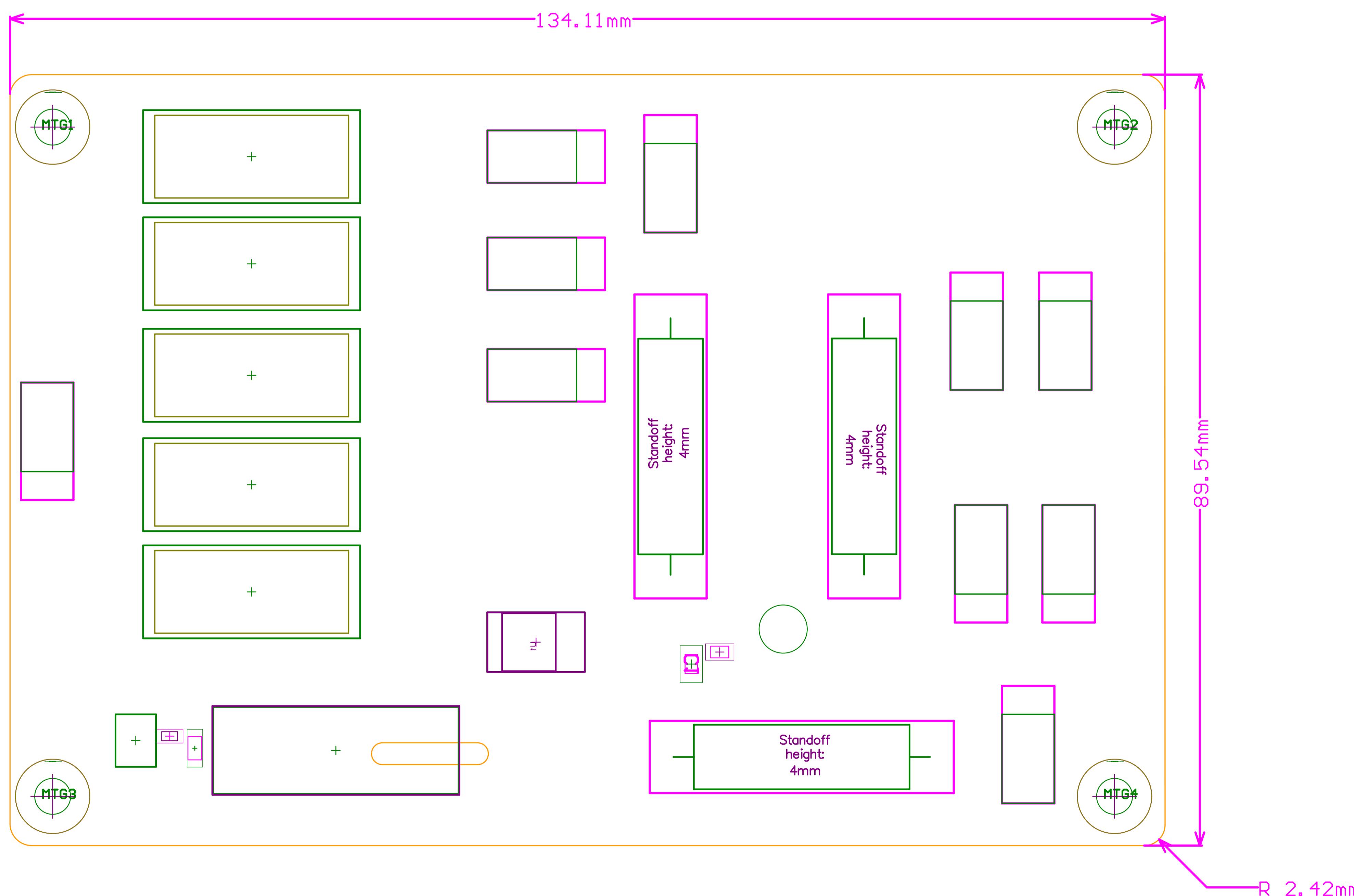
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File: C:\git\AERO_2019-2020\HV PDU	

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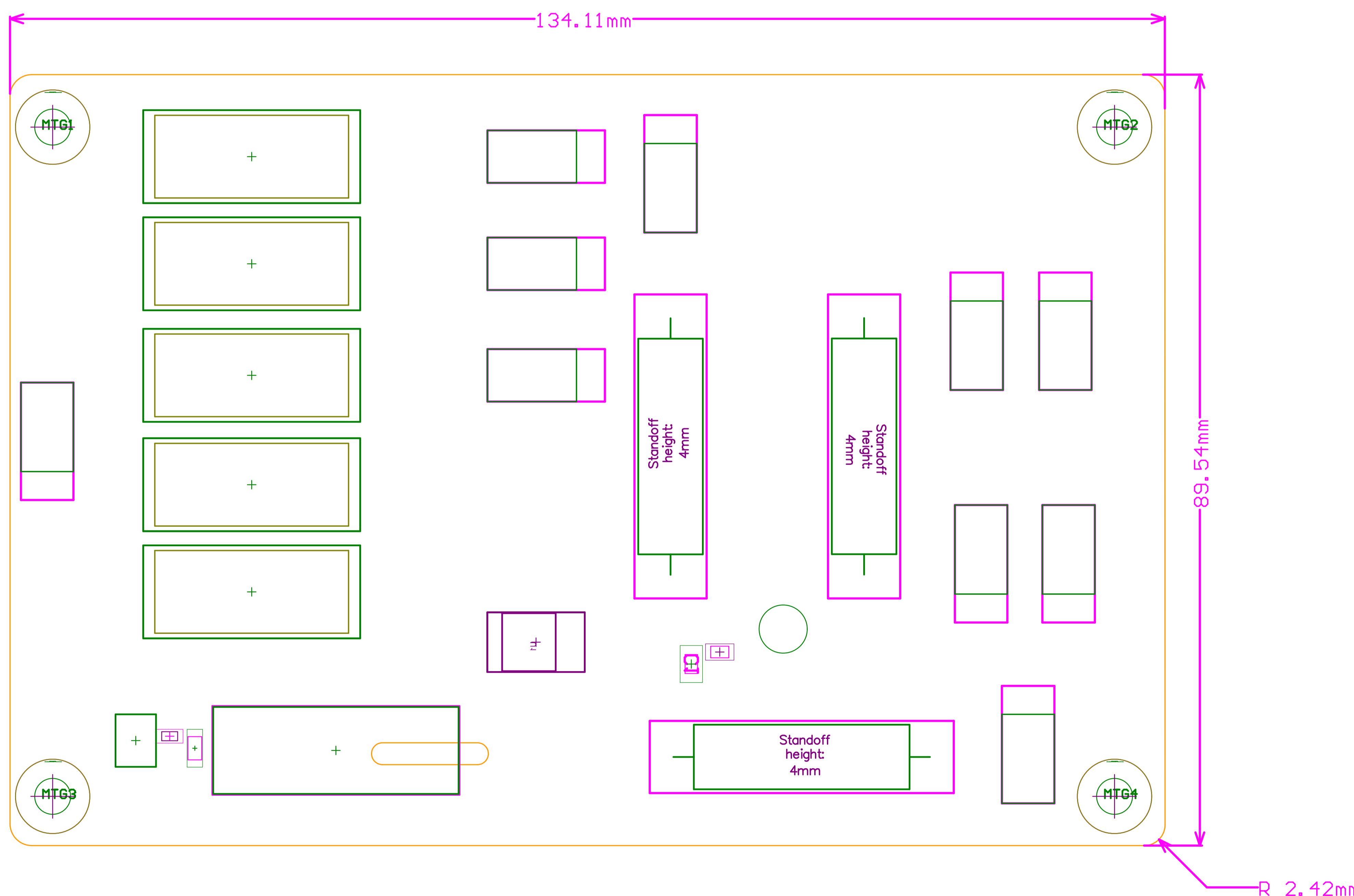
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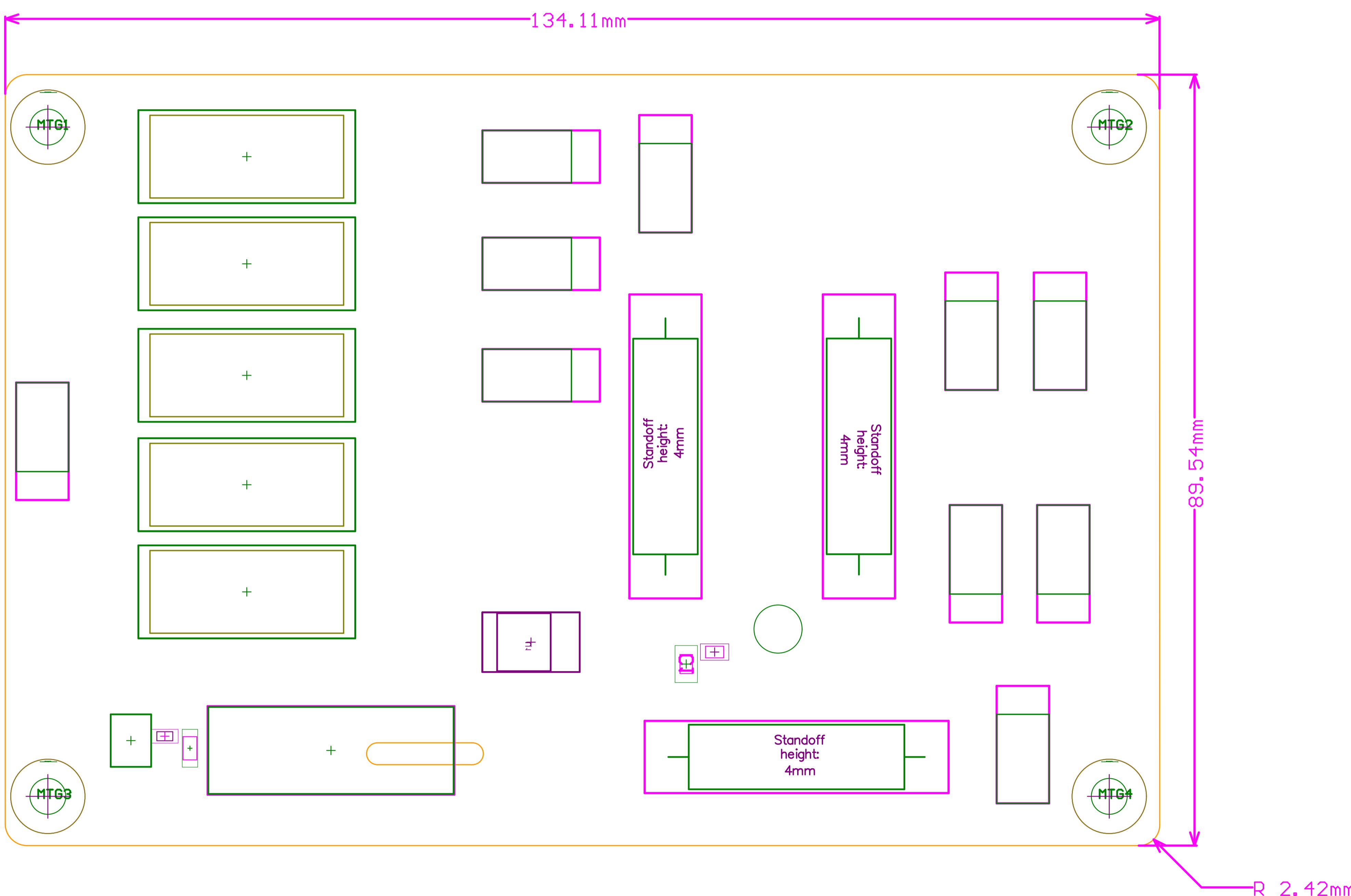
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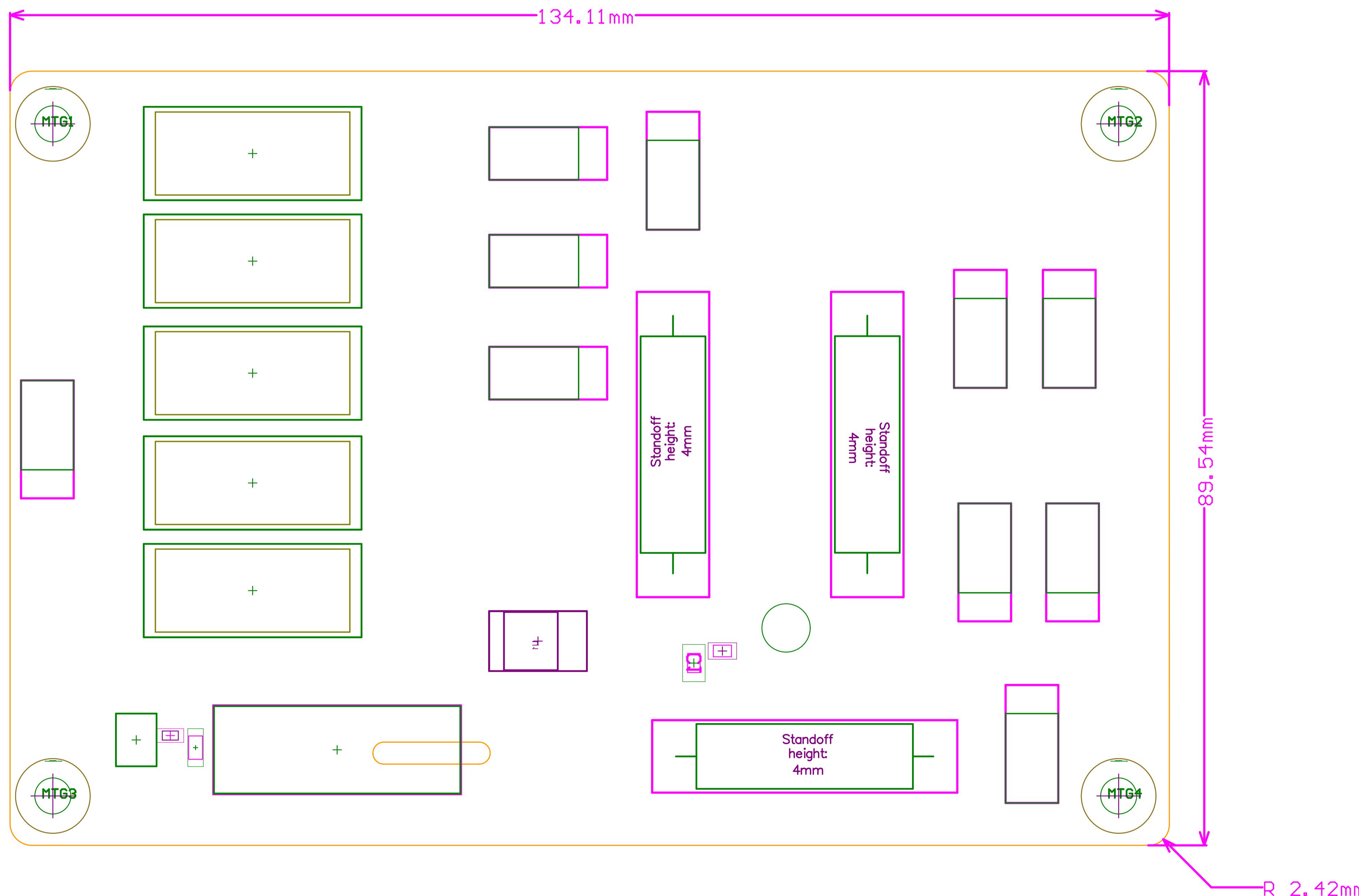
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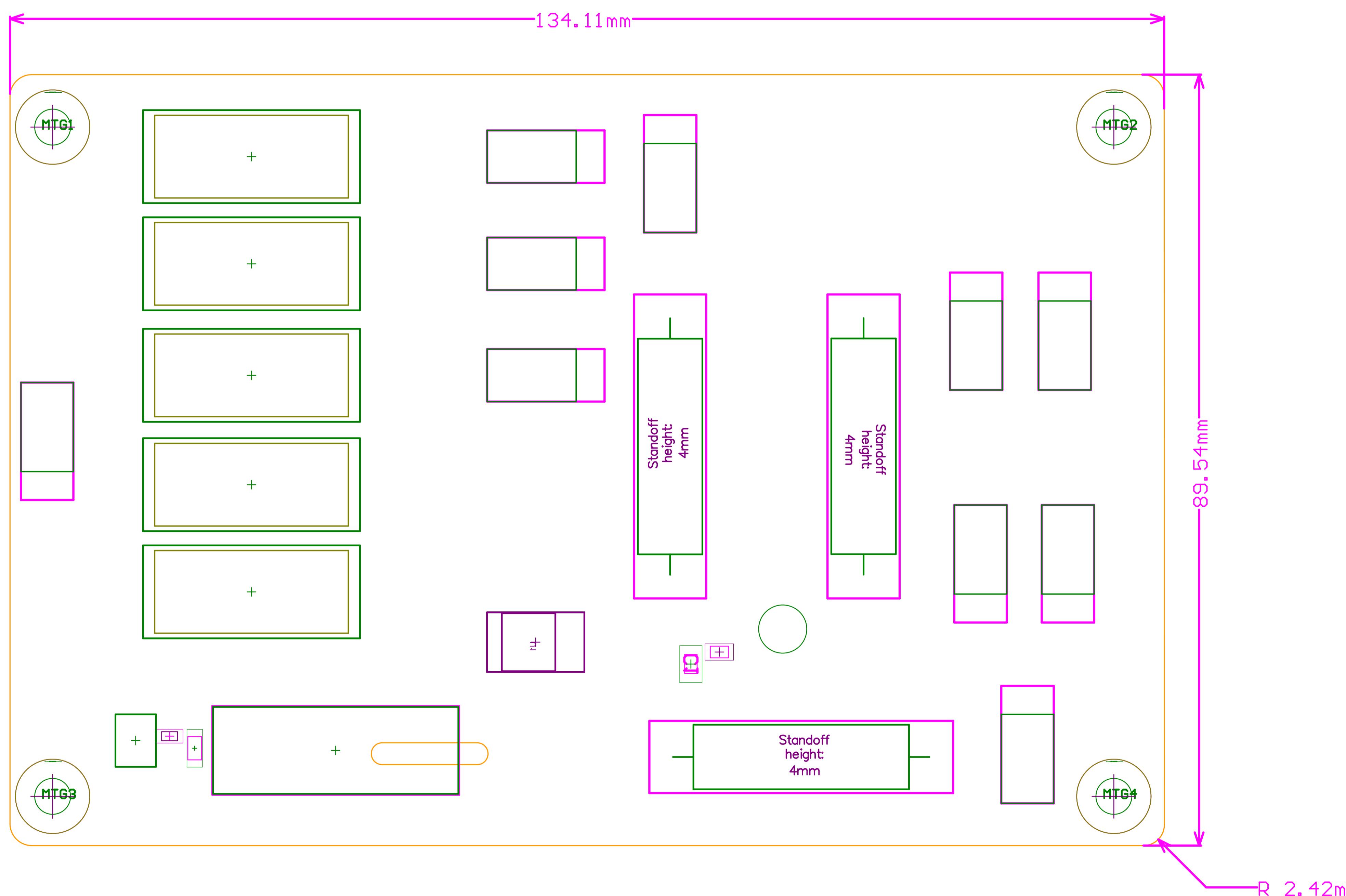
Title: HU RDH SchDoc

PDU for the HV Box
For CS4.5
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Size: letter Auth: Andrew Katz Proj: HLL DPL

Date: 3/23/2023 1:54:32 PM Page: 16

AERO



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Auth: Andrew Katz

Proj:

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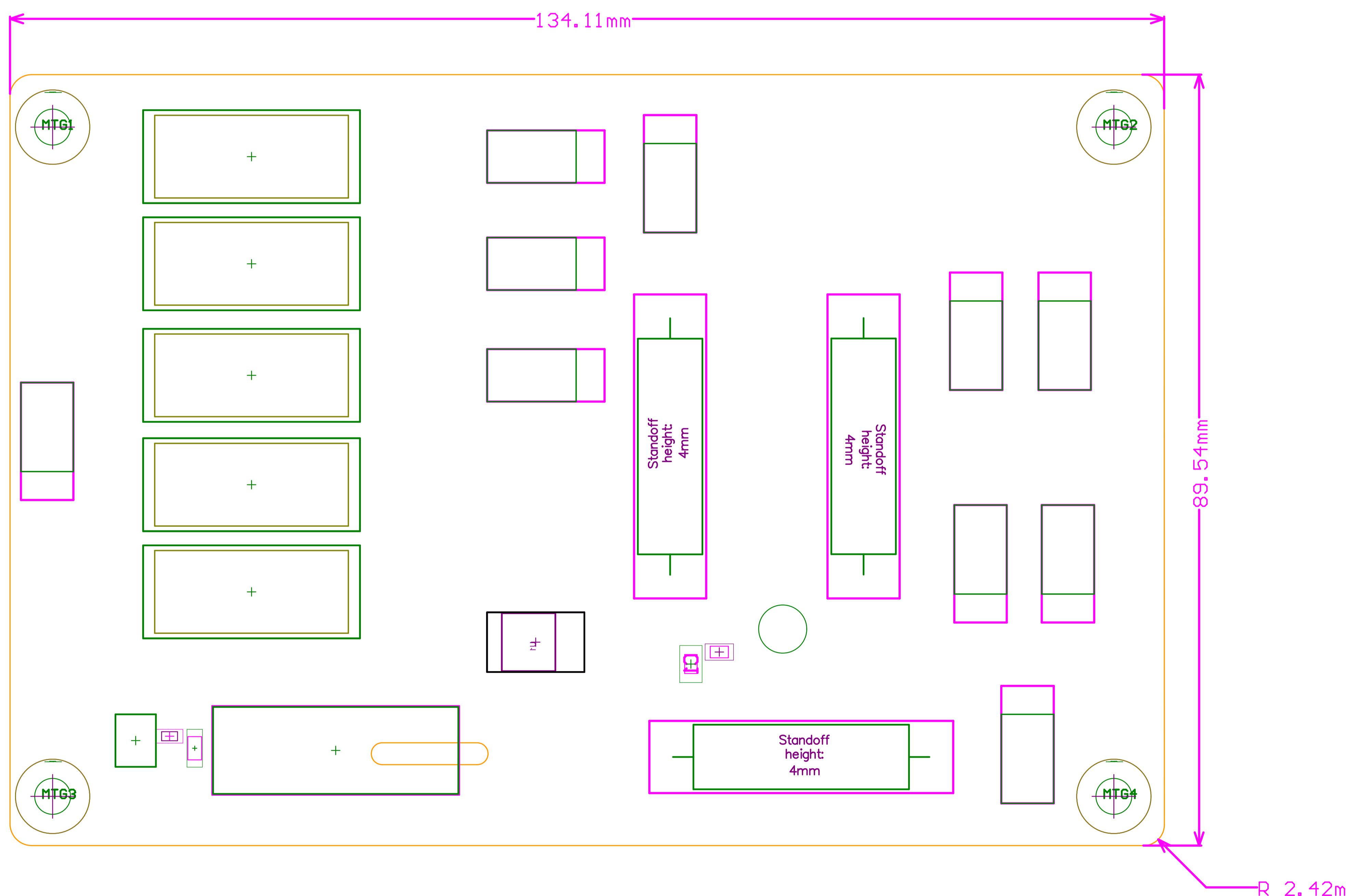
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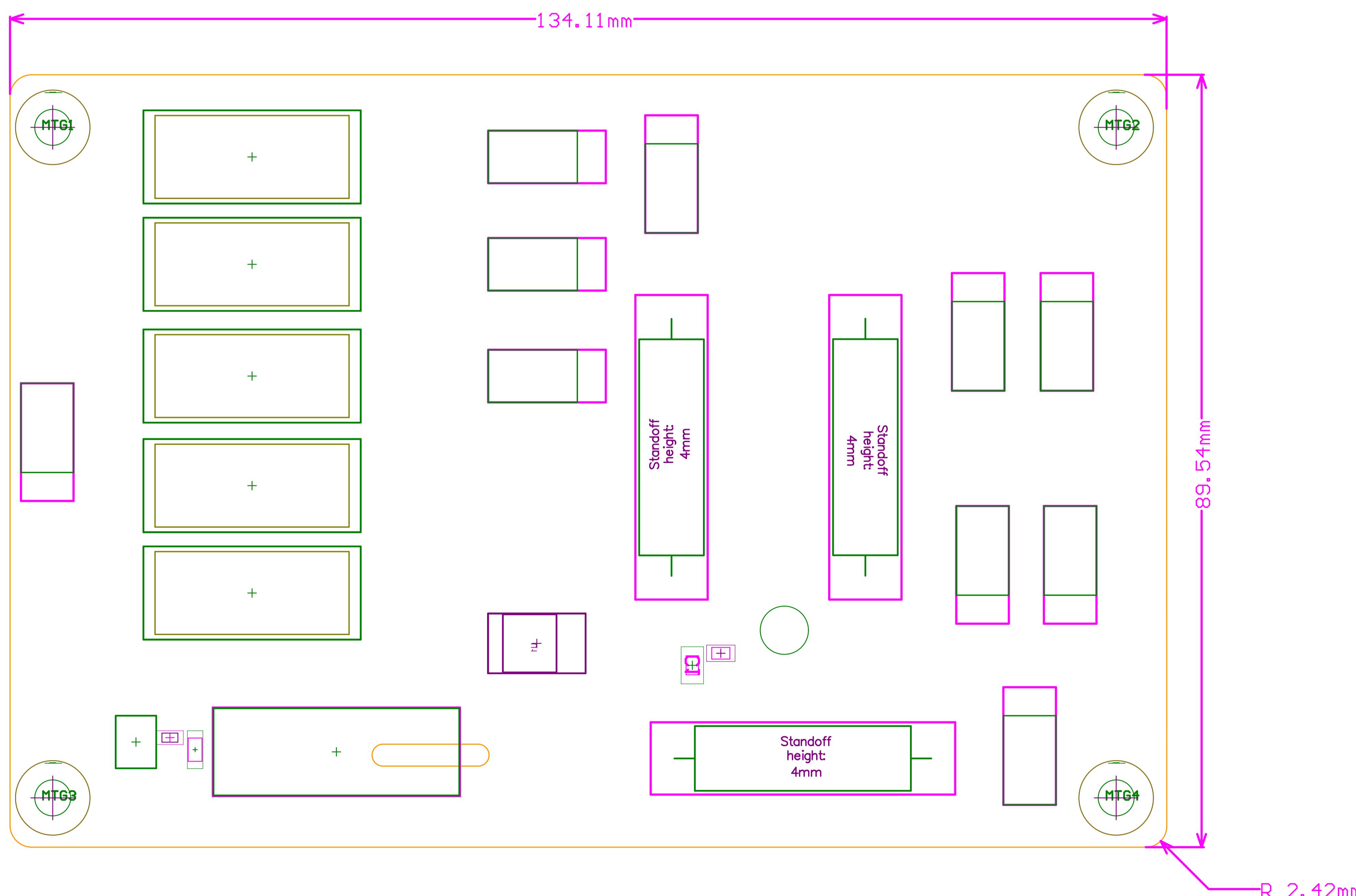
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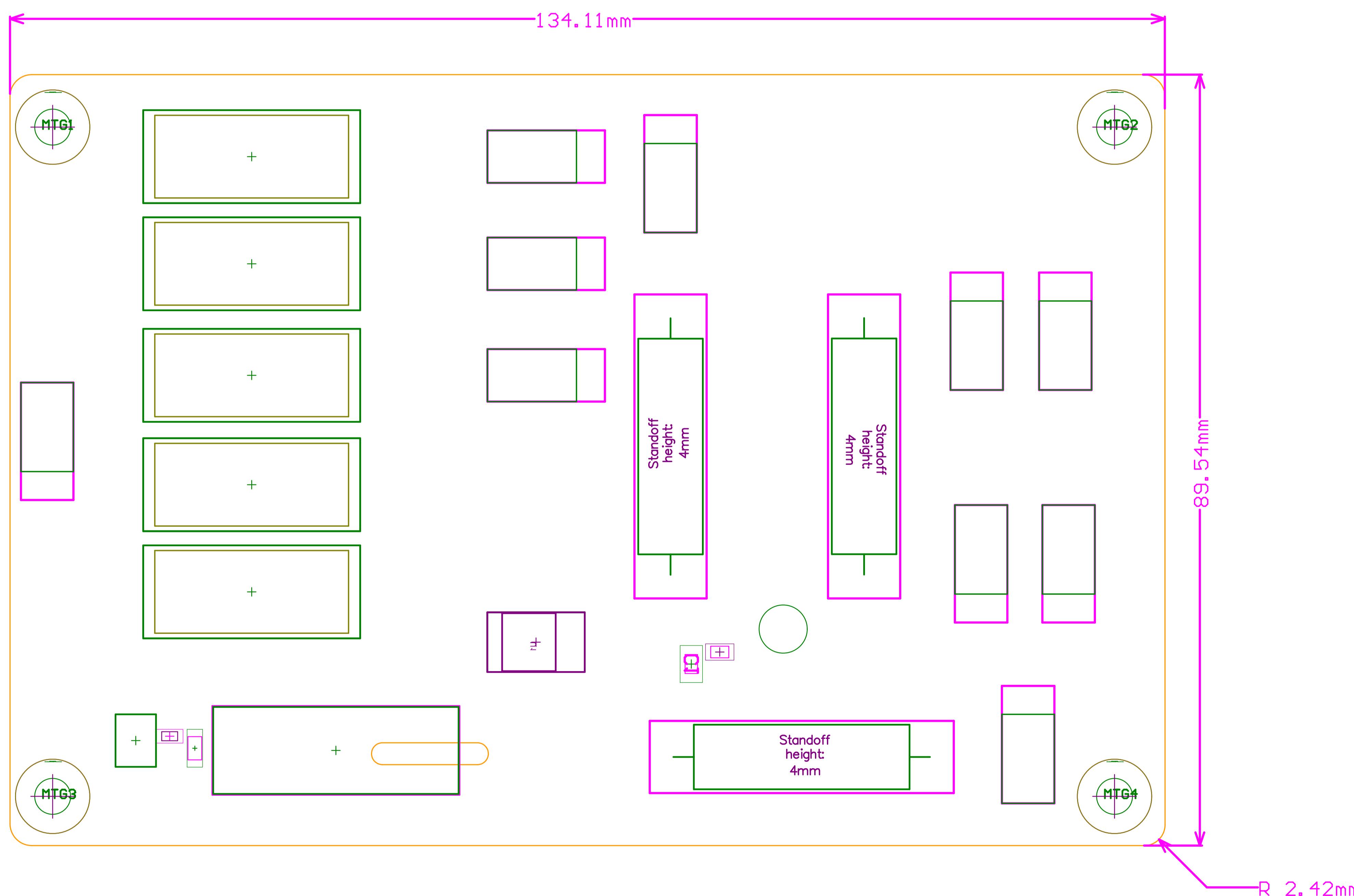
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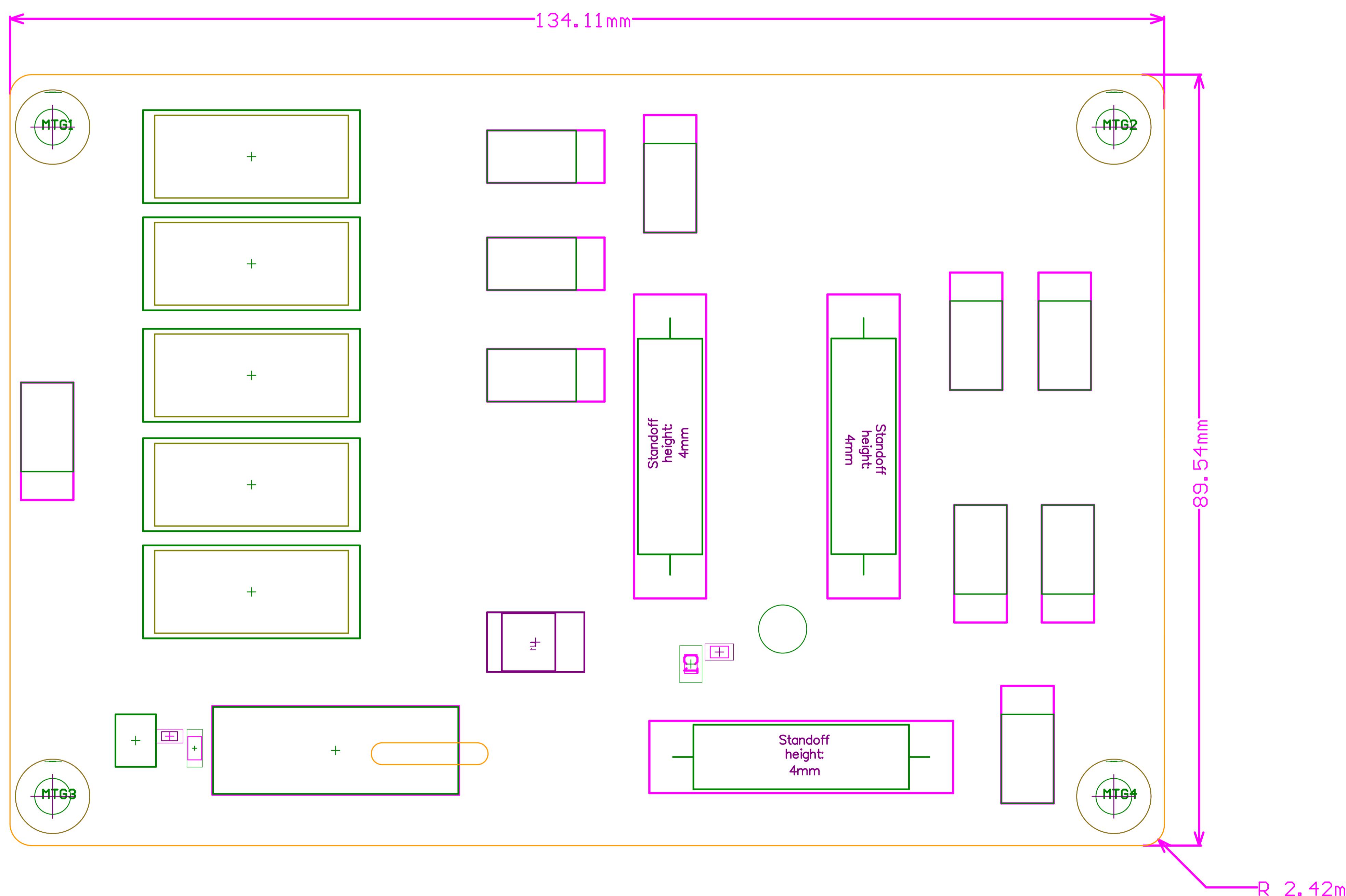
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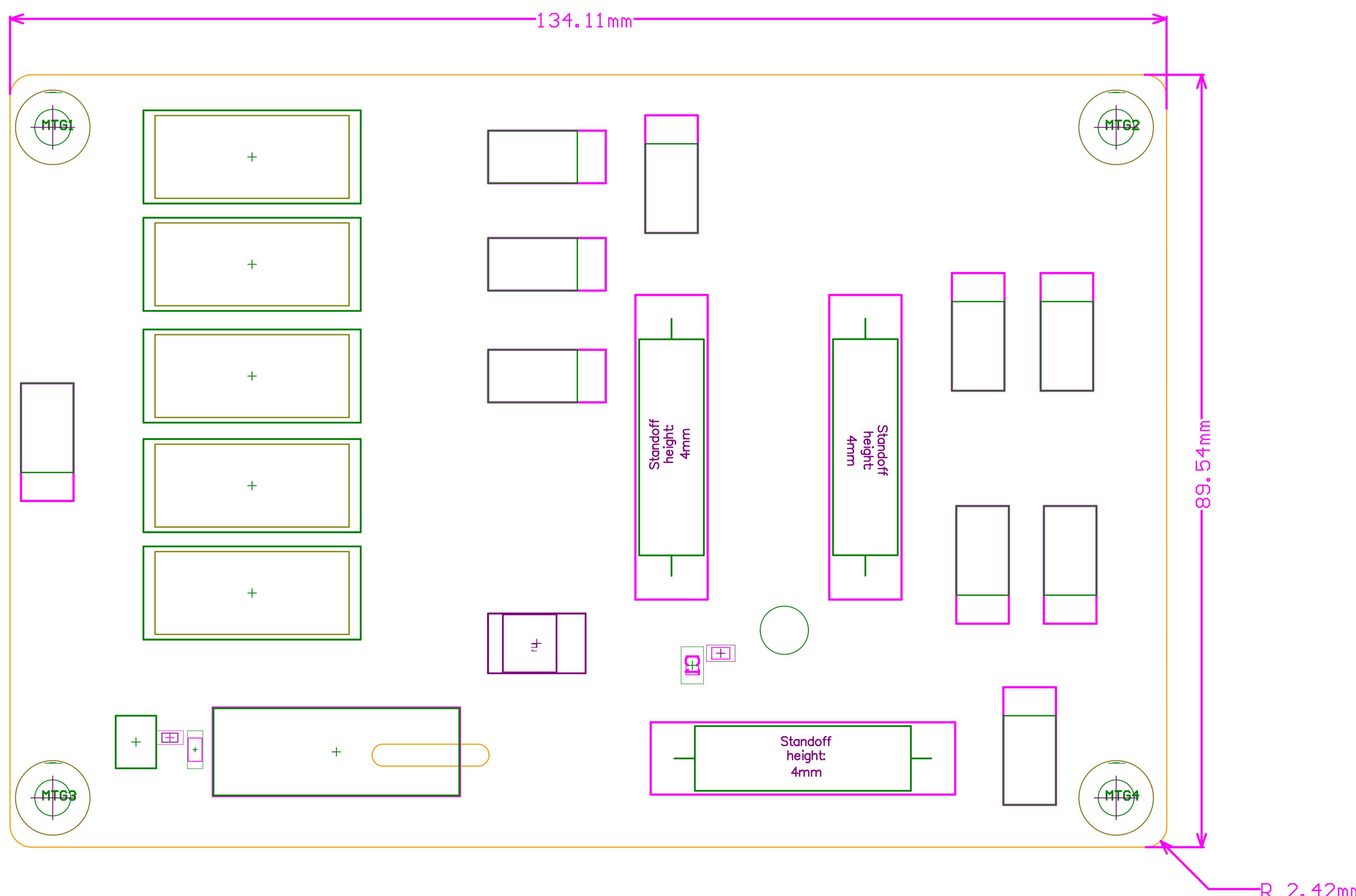
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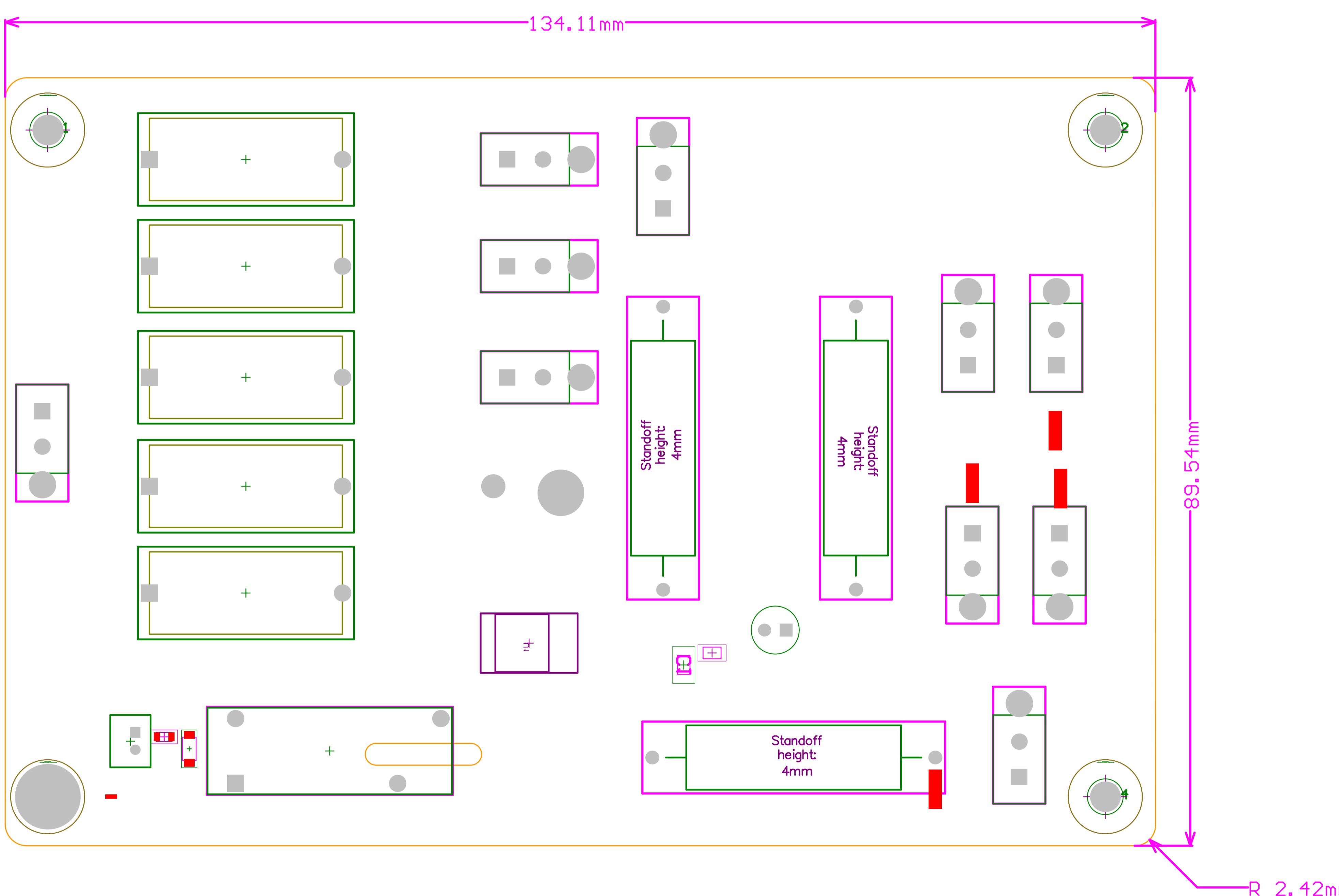
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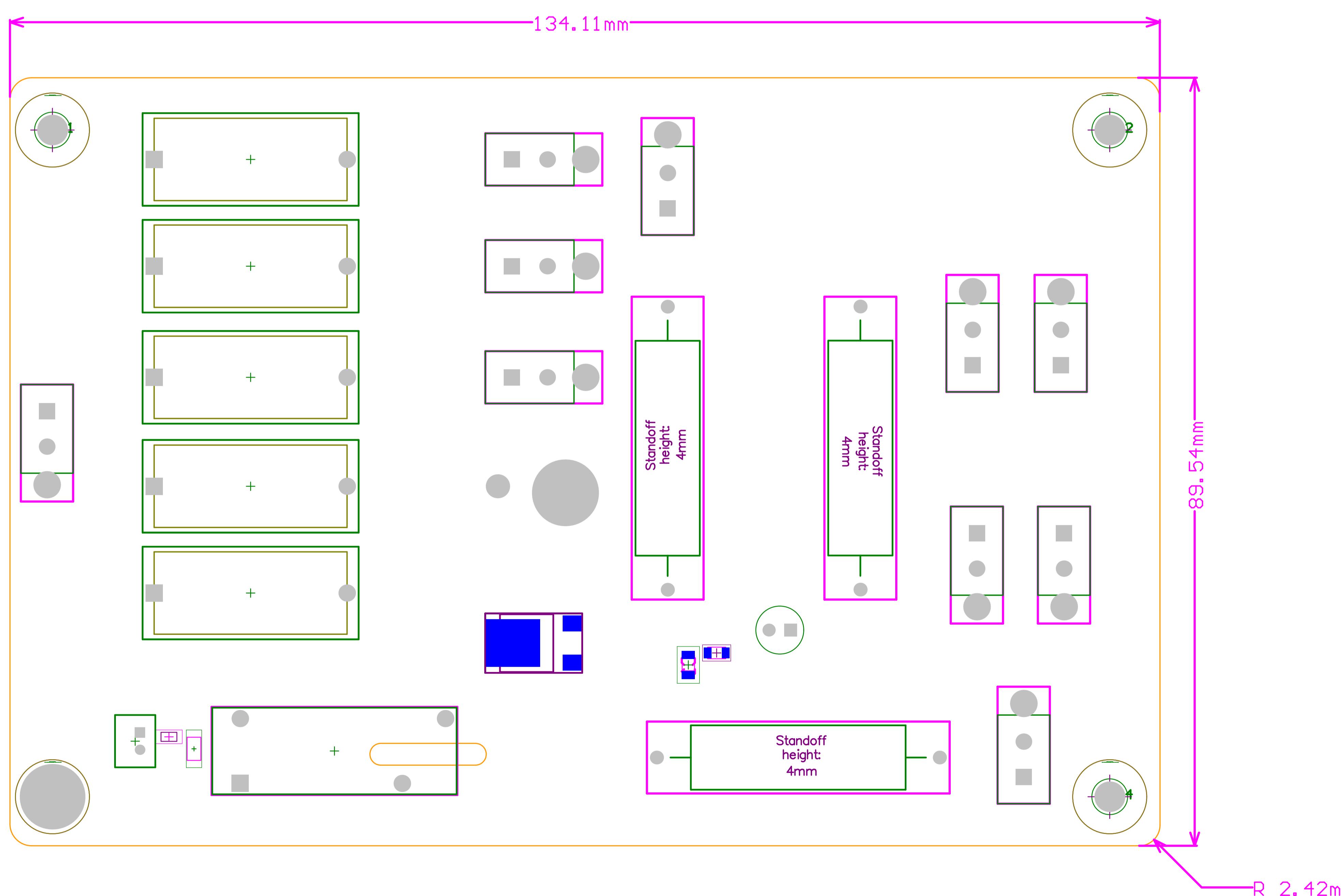
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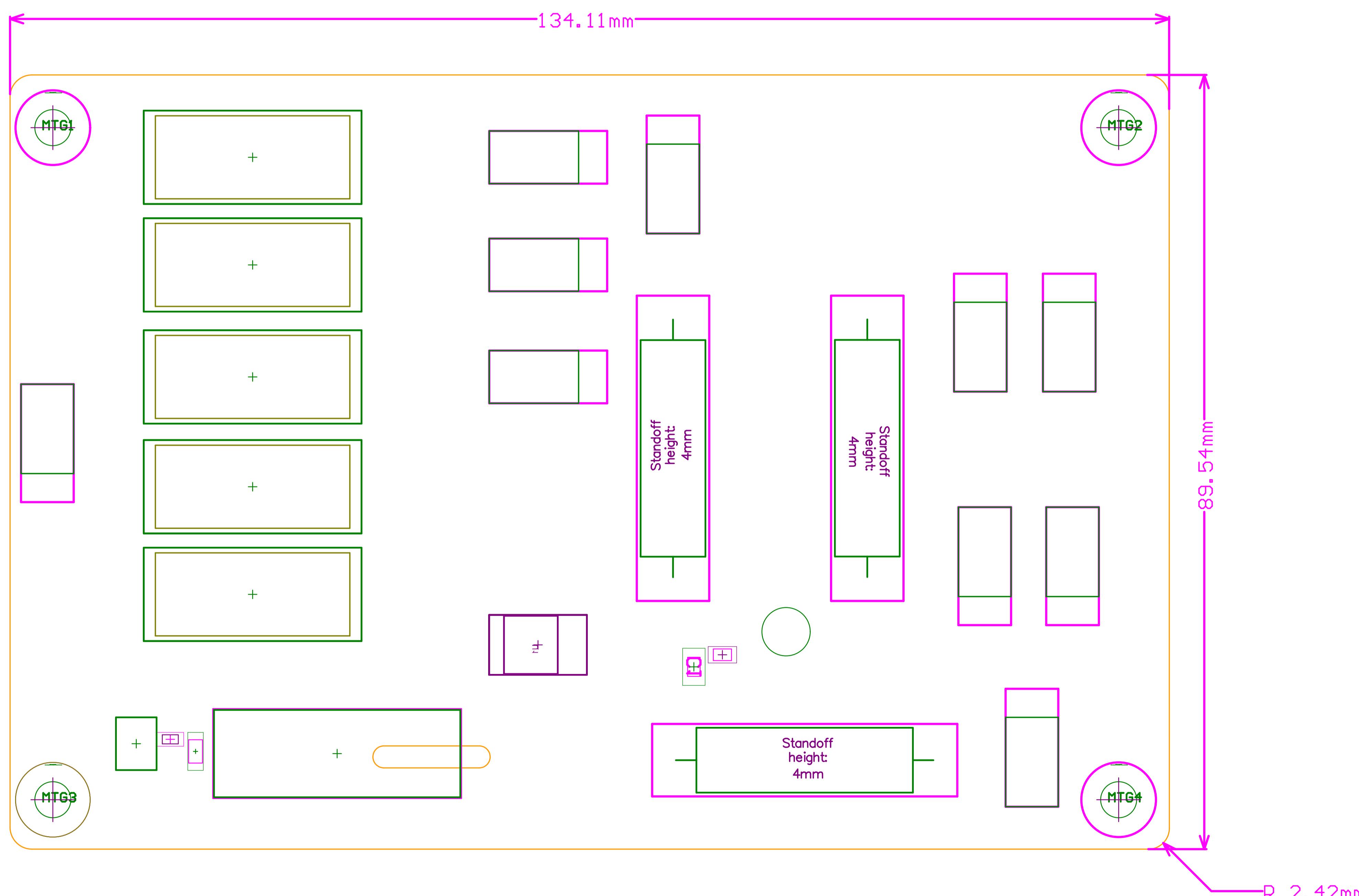
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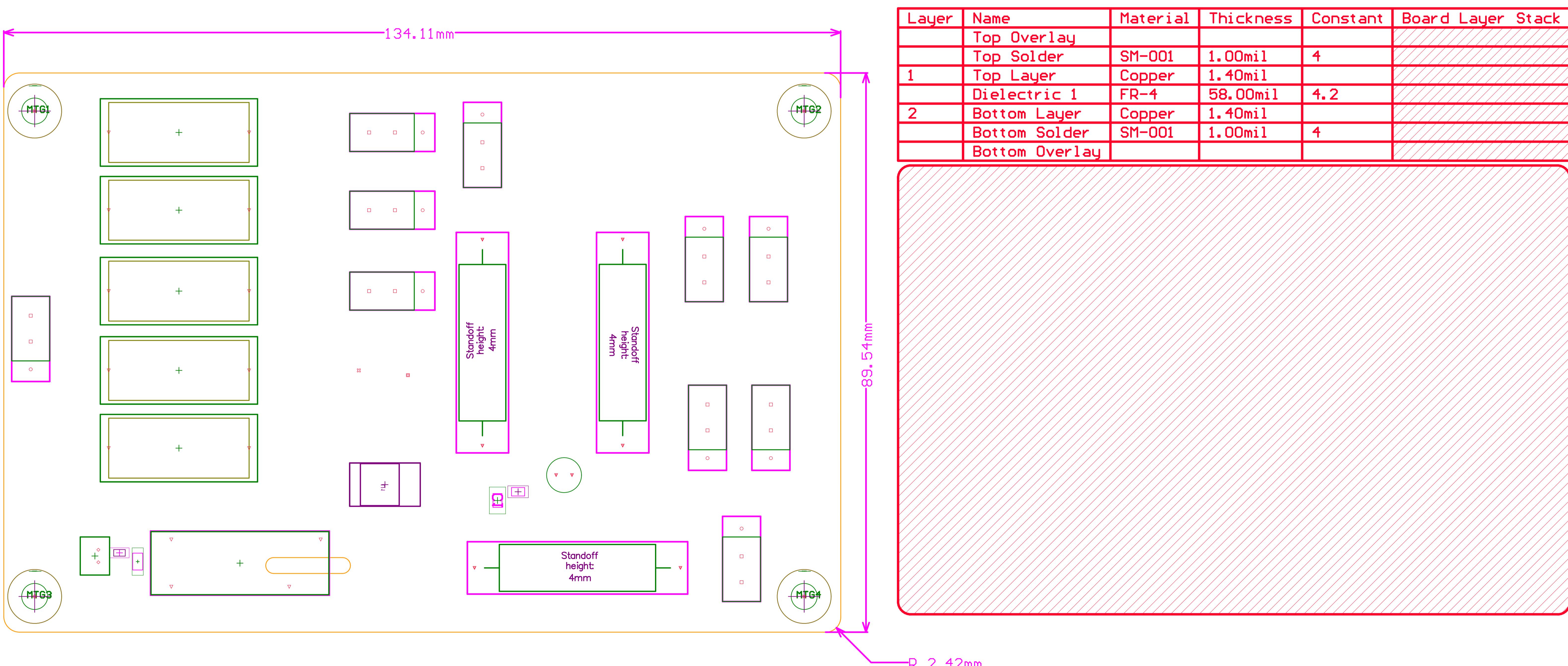
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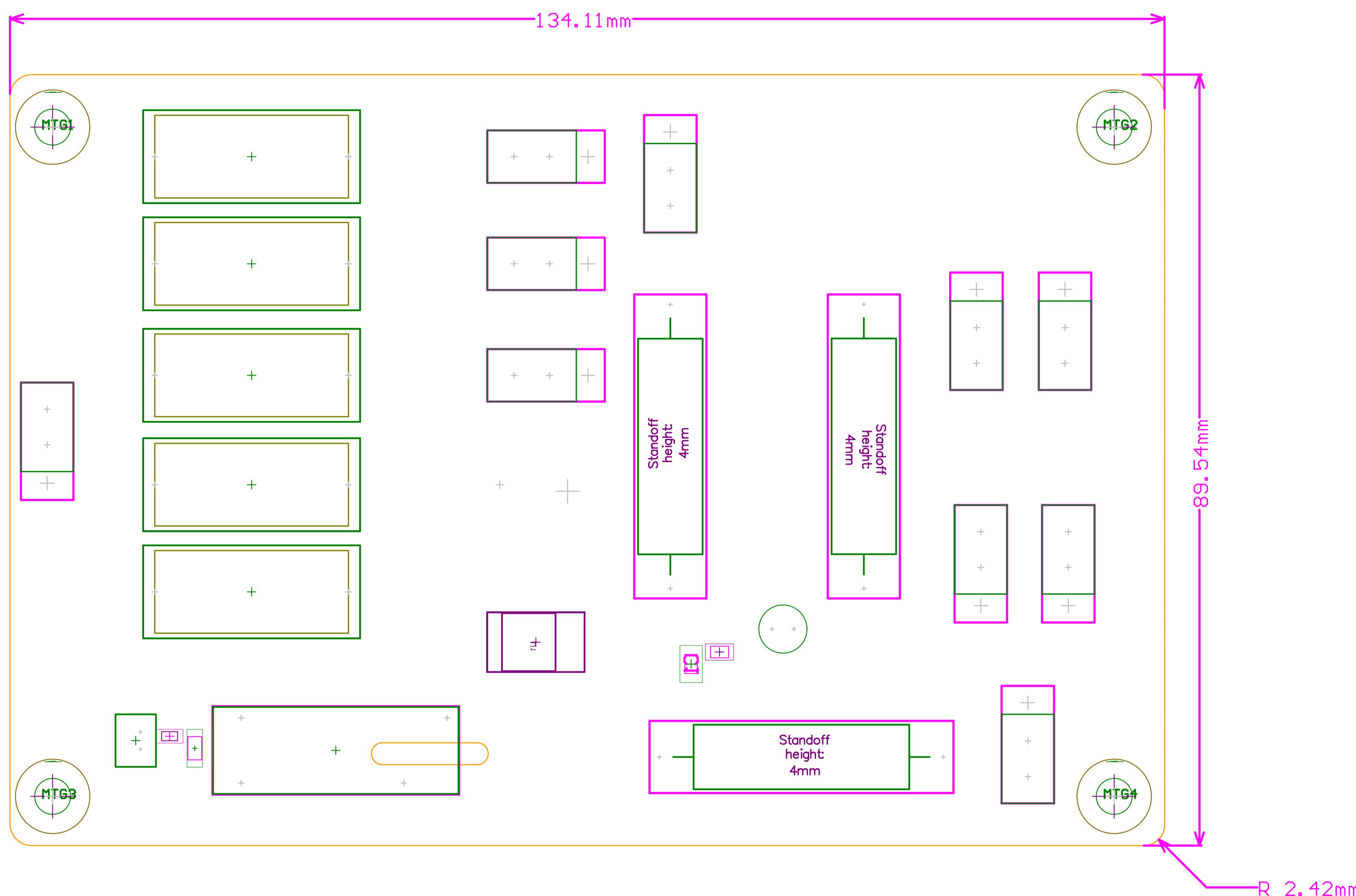
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