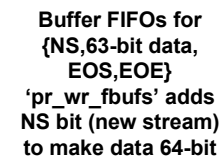


110-bit input bus barrel -shifted to a position in a 315-bit output bus.
All other output bits are zero.

bs_dout is OR'ed into 5x 63-bit reg
for shifter output



End-of-stream, End-of-event, FIFO full, empty etc., logic not shown

