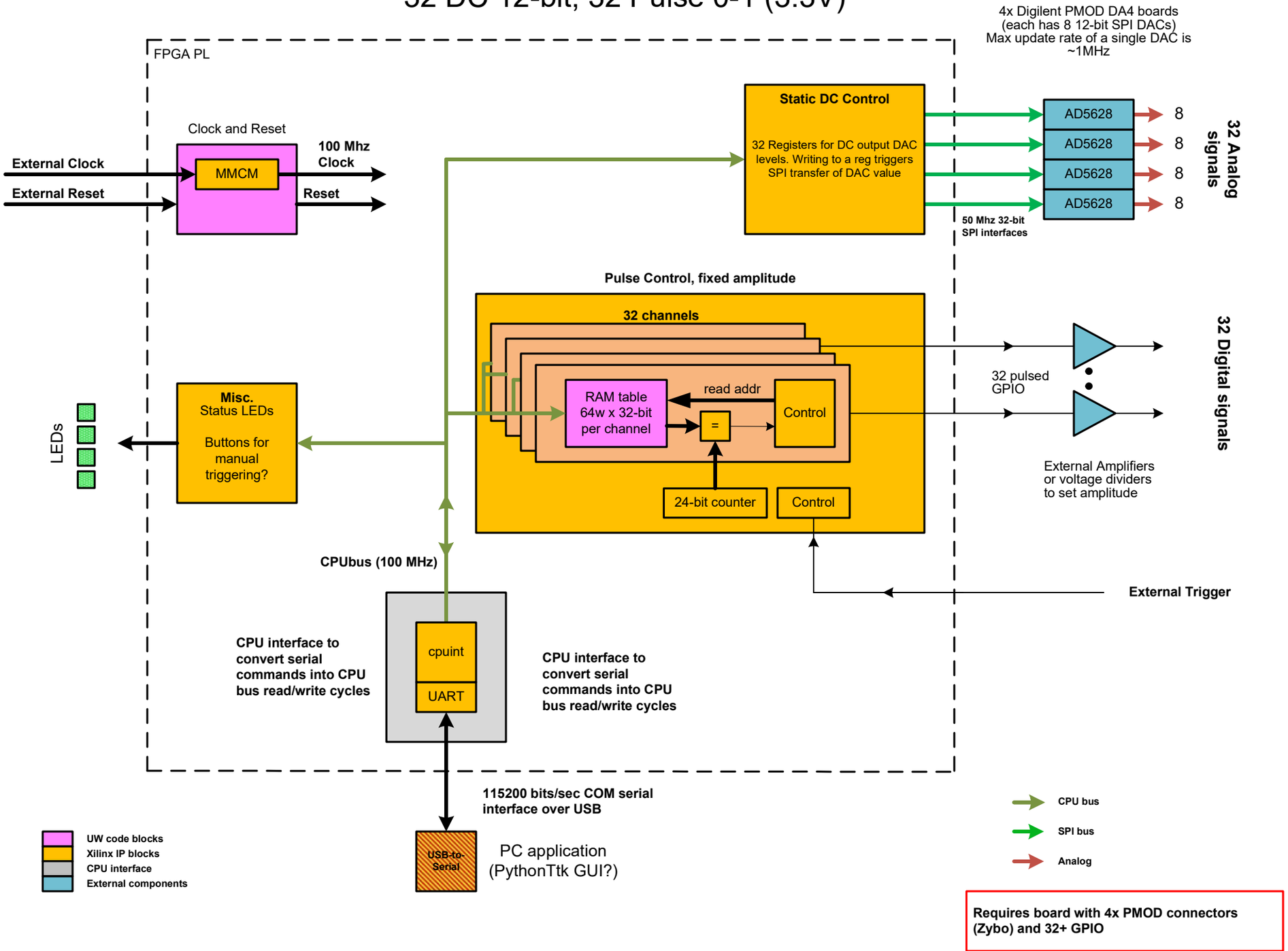
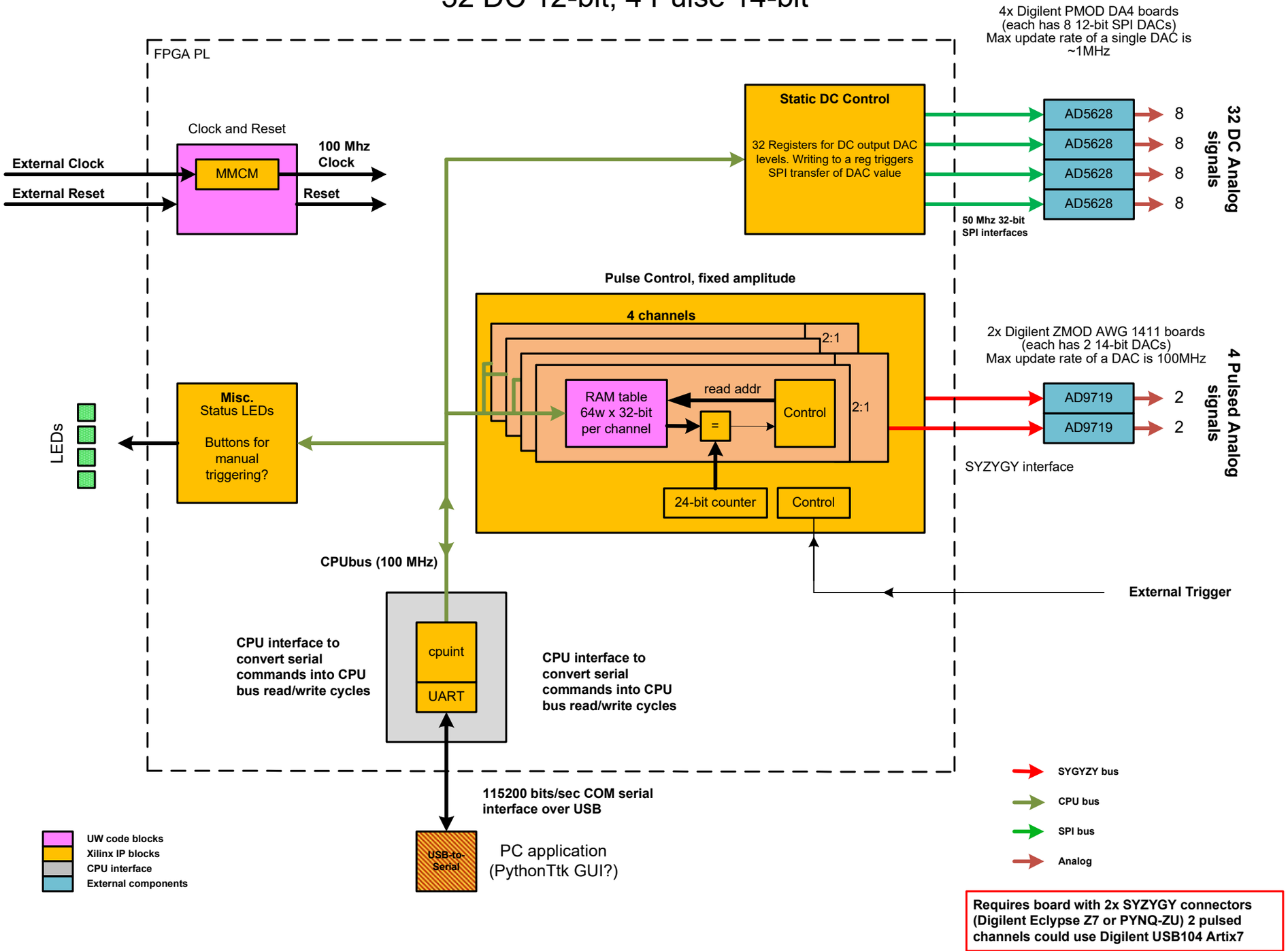
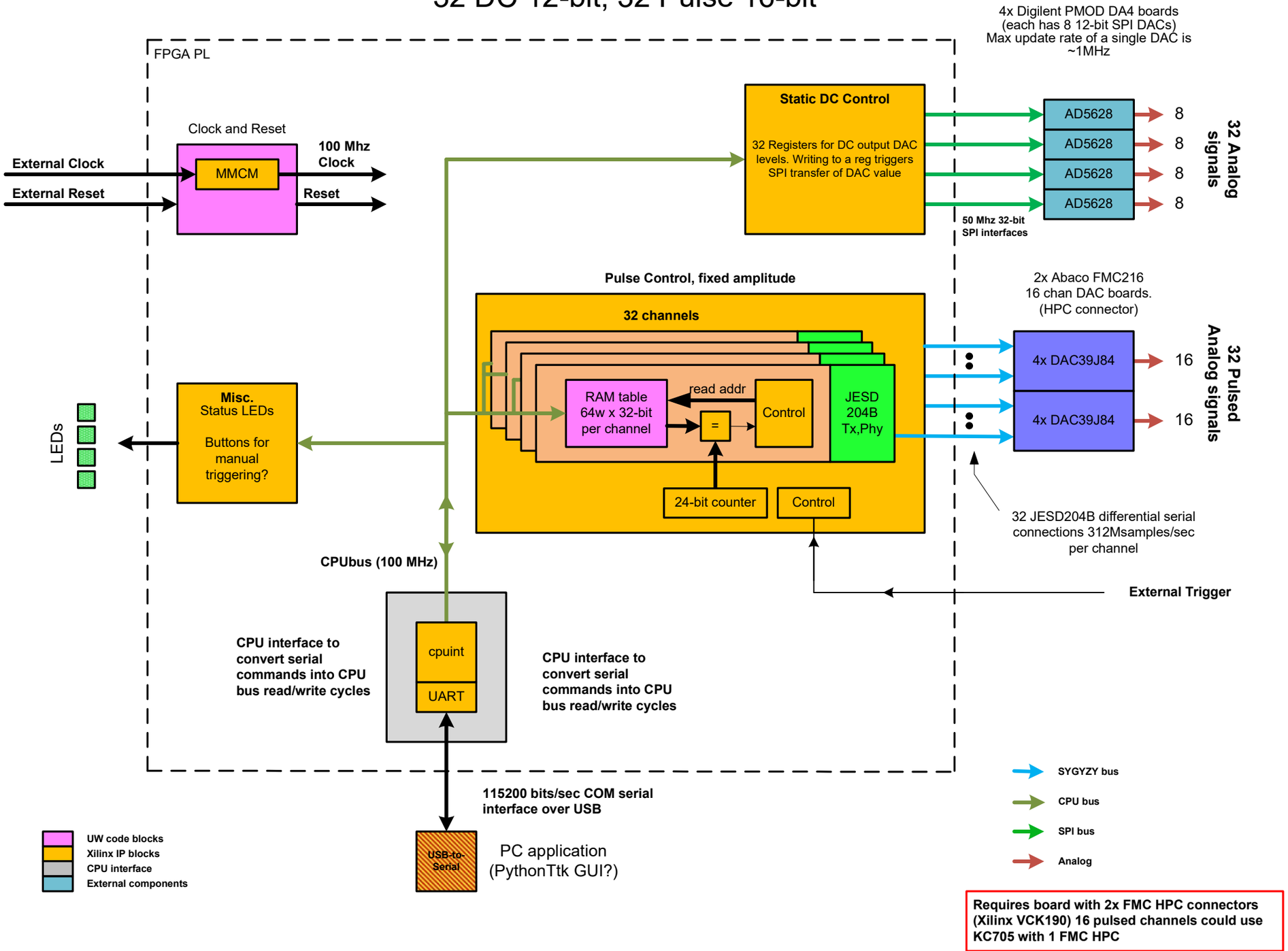
Laser Control FPGA Block Diagram
32 DC 12-bit, 32 Pulse 0-1 (3.3V)



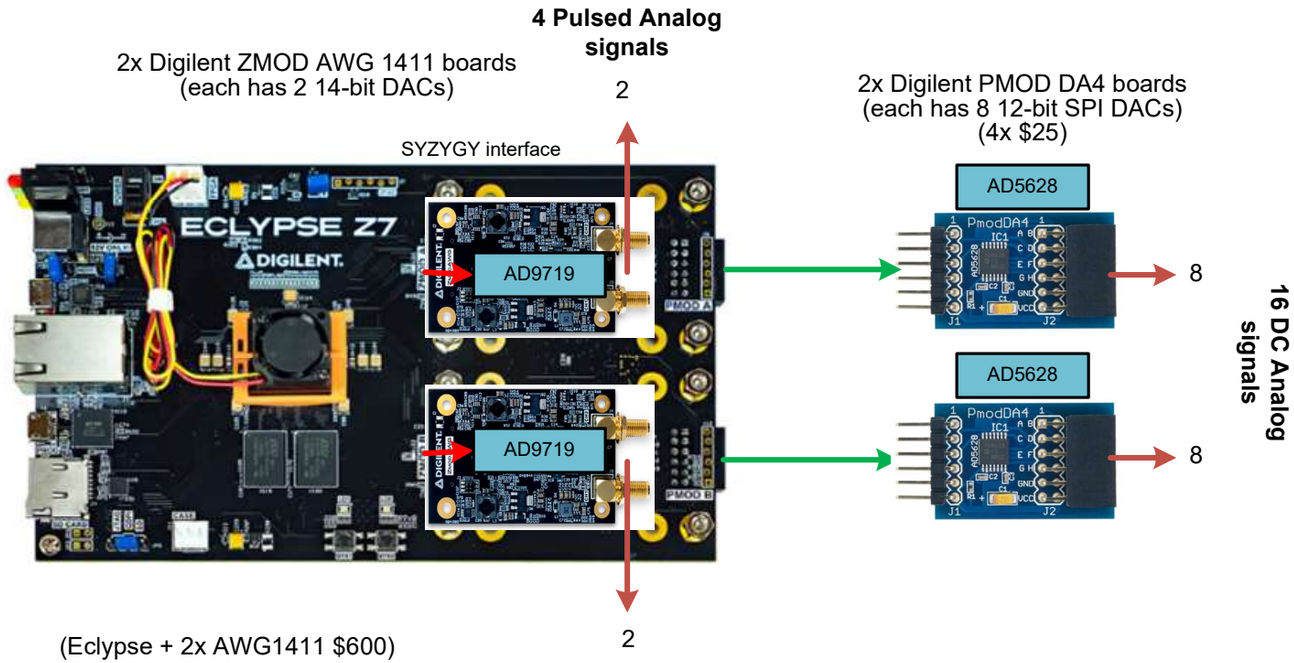
Laser Control FPGA Block Diagram
32 DC 12-bit, 4 Pulse 14-bit



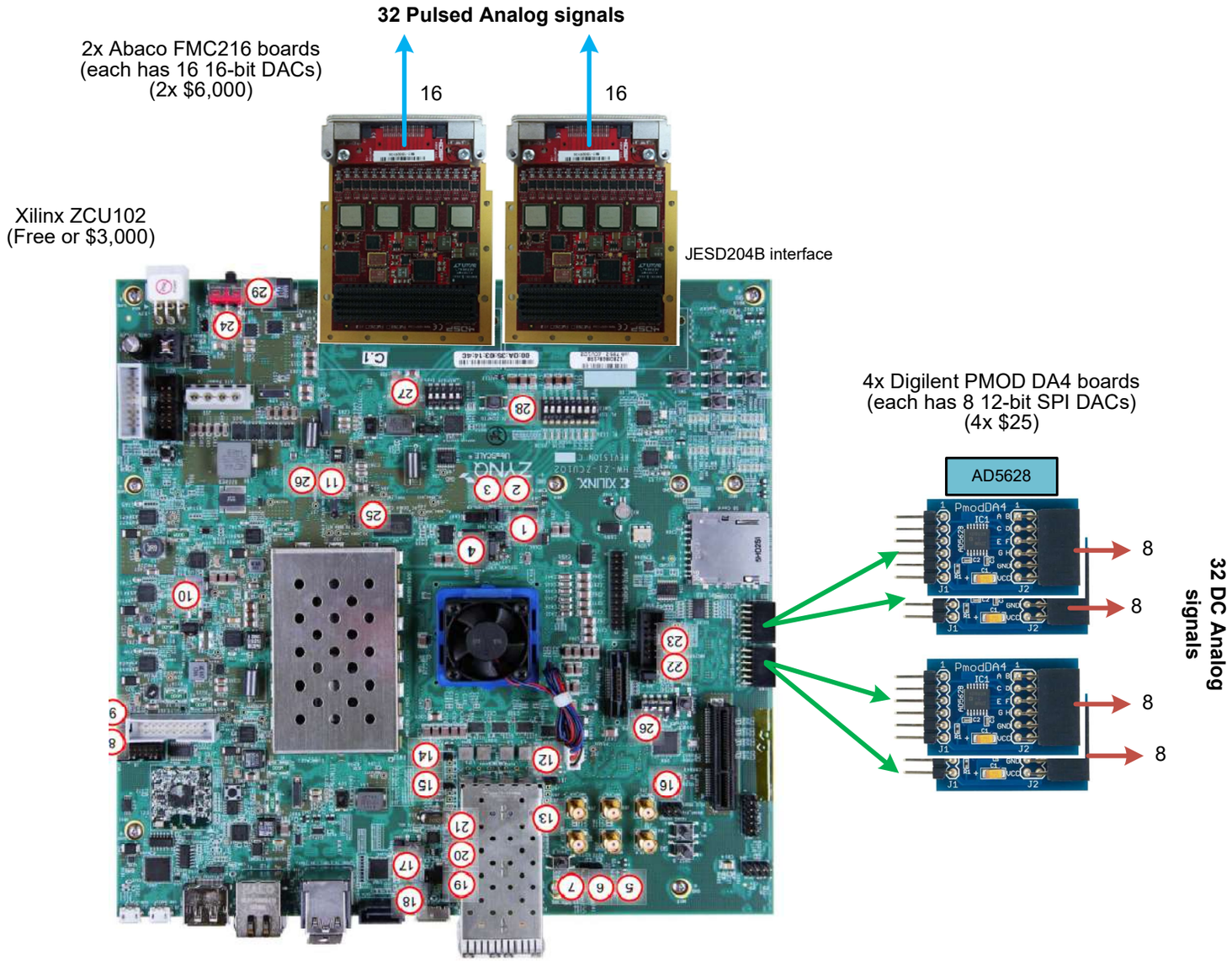
Laser Control FPGA Block Diagram
32 DC 12-bit, 32 Pulse 16-bit



Eclypse Z7 + 2x AWG1411 + 2x PMOD DA4
16 DC 12-bit, 4 Pulse 14-bit analog



Xilinx ZCU102 + 2x Abaco FMC216 + 4x PMOD DA4
32 DC 12-bit, 32 Pulse 16-bit analog



Xilinx KC705 + 1x Abaco FMC216 + 4x PMOD DA4
32 DC 12-bit, 16 Pulse 16-bit analog