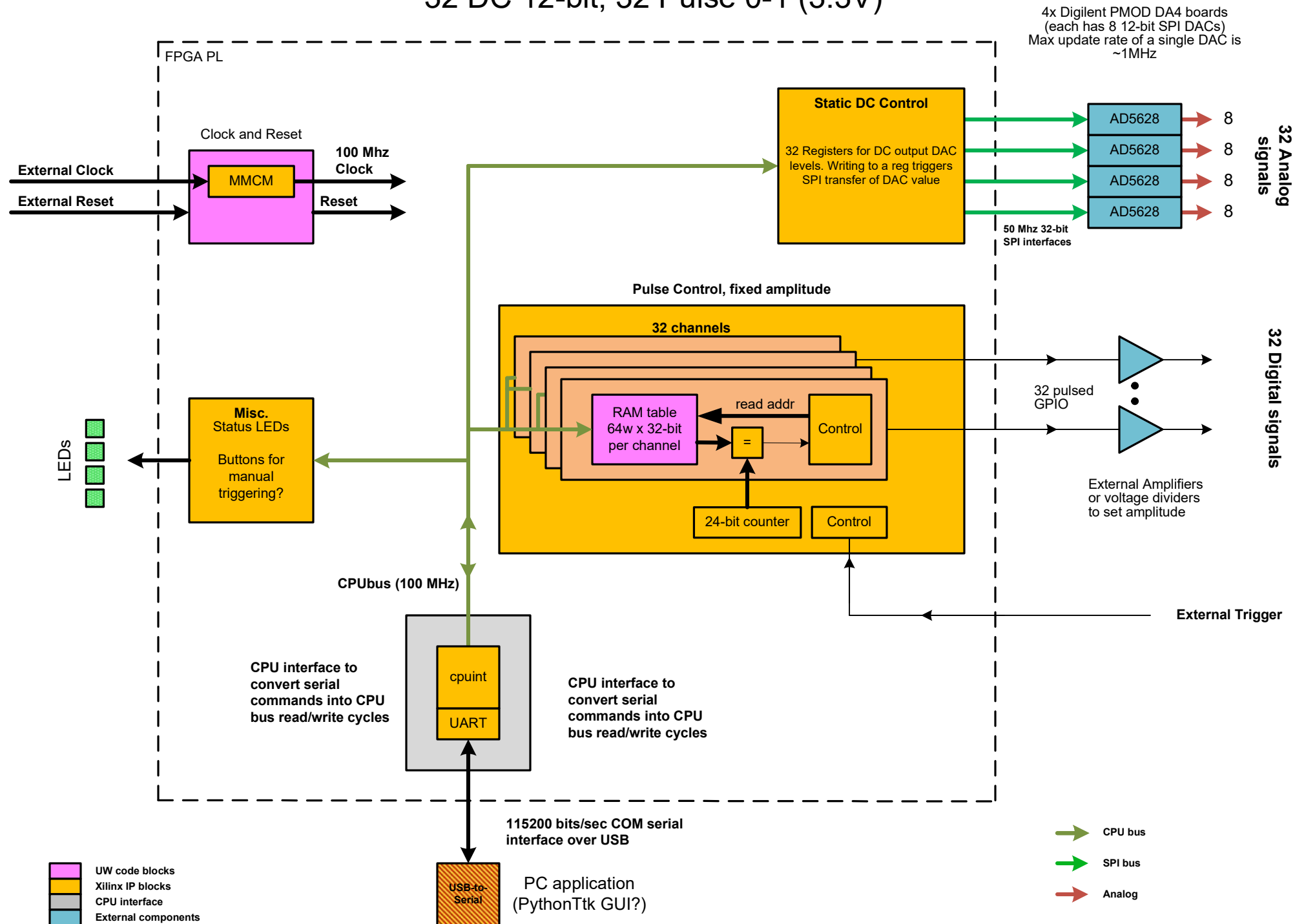


Laser Control FPGA Block Diagram

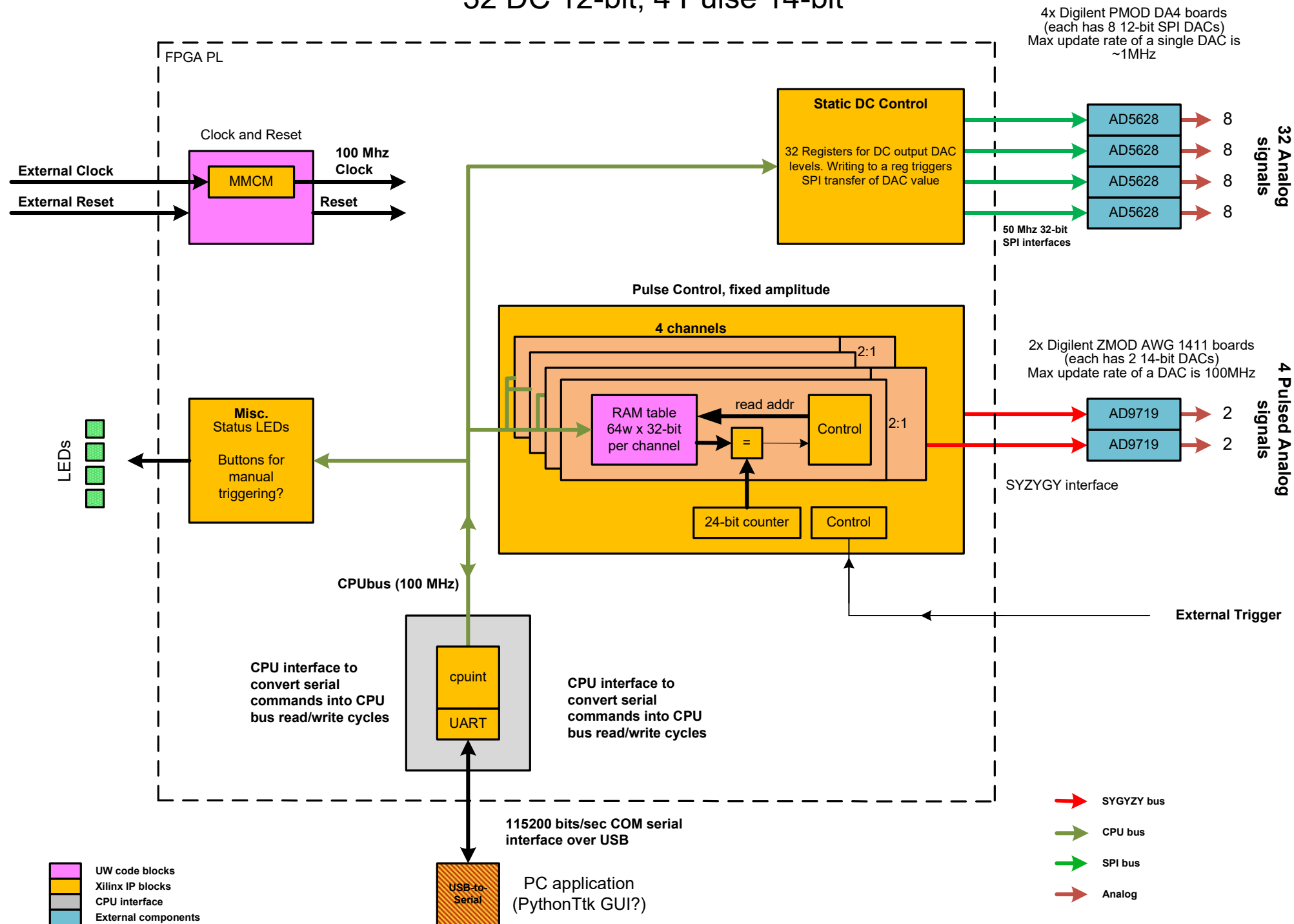
32 DC 12-bit, 32 Pulse 0-1 (3.3V)



Requires board with 4x PMOD connectors (Zybo) and 32+ GPIO

Laser Control FPGA Block Diagram

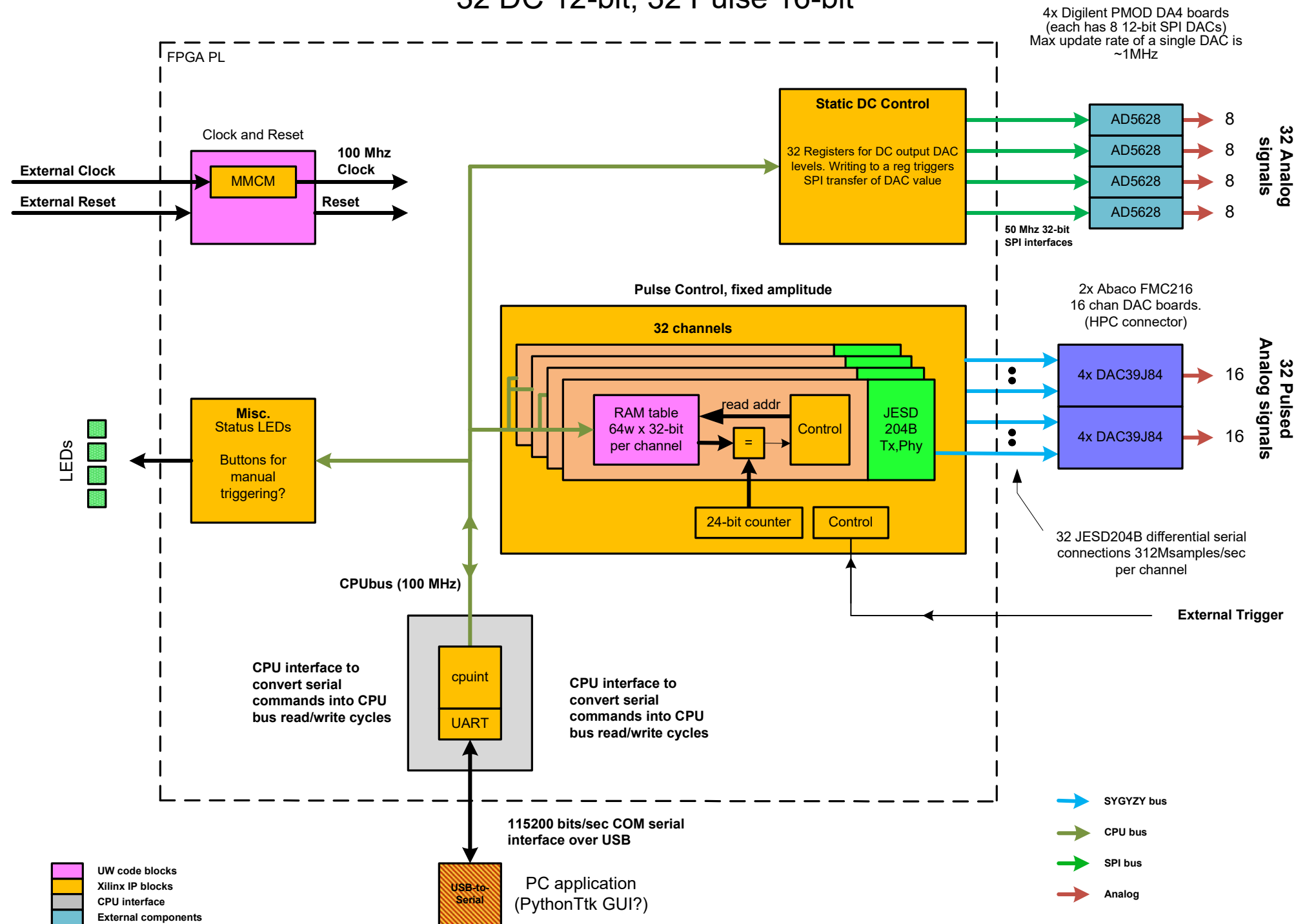
32 DC 12-bit, 4 Pulse 14-bit



Requires board with 2x SYZYGY connectors
(Digilent Eclipse Z7 or PYNQ-ZU) 2 pulsed channels could use Digilent USB104 Artix7

Laser Control FPGA Block Diagram

32 DC 12-bit, 32 Pulse 16-bit



Requires board with 2x FMC HPC connectors (Xilinx VCK190) 16 pulsed channels could use KC705 with 1 FMC HPC