

Last Updated: Oct 29, 2017

— Power
— Analog Signal
— Digital Signal
- - - - - Board Outline

Project: **BMS_Current_Sense.PrjPcb**

Title: **BMS Current Sense Block Diagram**

Project Lead: **Taiping Li**

Size: **Letter**

Date: **2018-02-23**

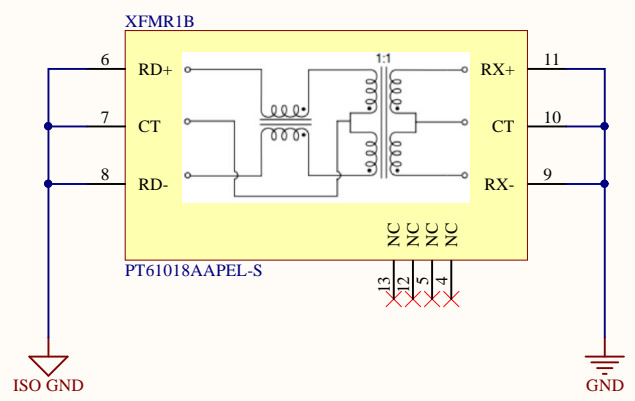
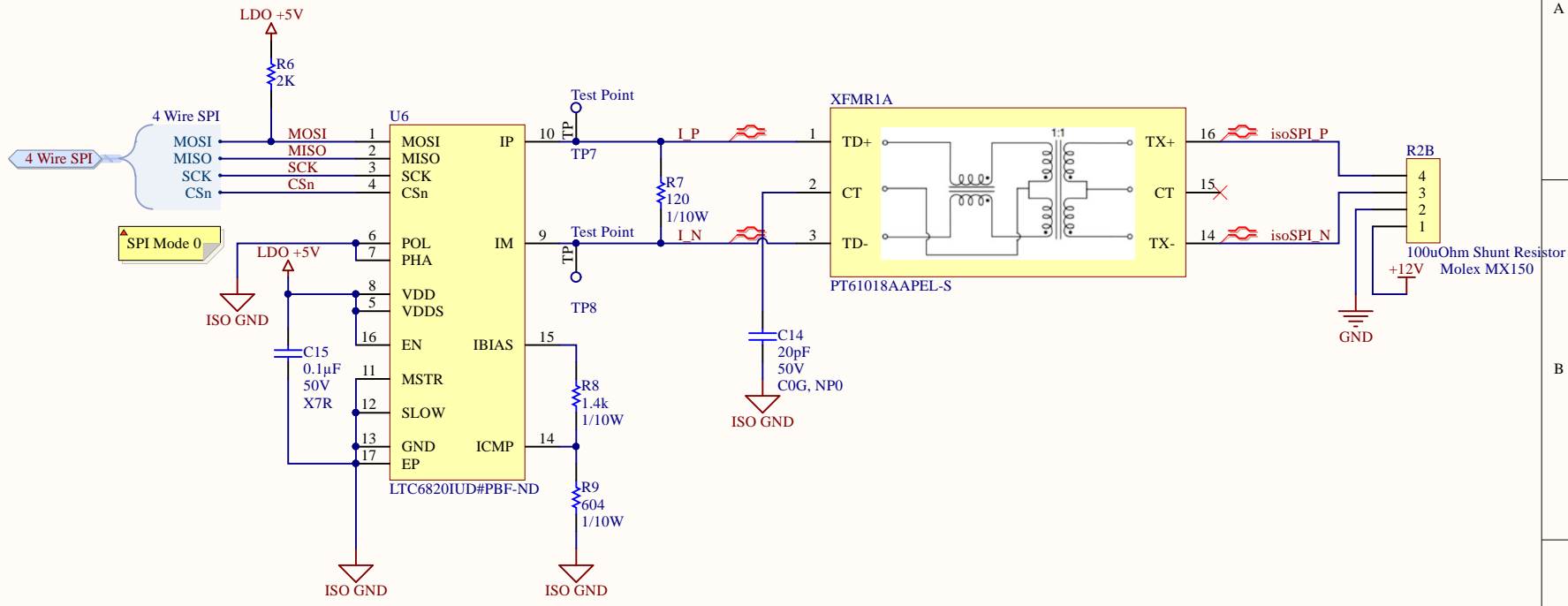
Revision: **2.2**


Sheet 1 of 3



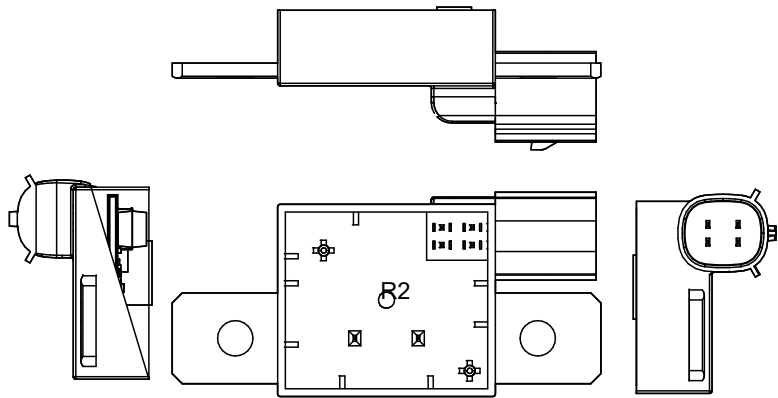
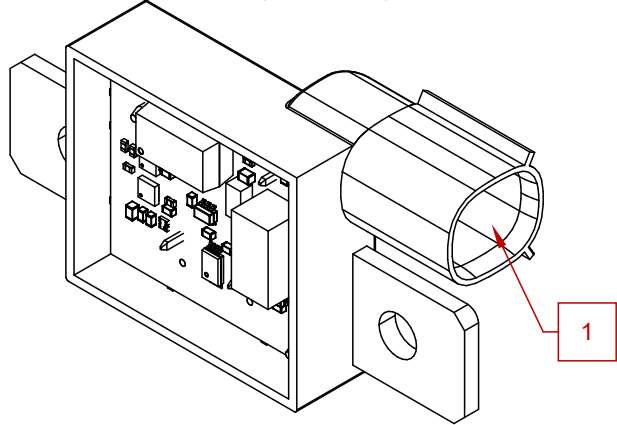
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Project: BMS_Current_Sense.PrjPcb		
Title: BMS Current Sense isoSPI Interface		
Project Lead: Taiping Li		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9
Size: Letter	Revision: 2.2	
Date: 2018-02-23	Sheet 3 of 3	
		Website: www.uwmidsun.com

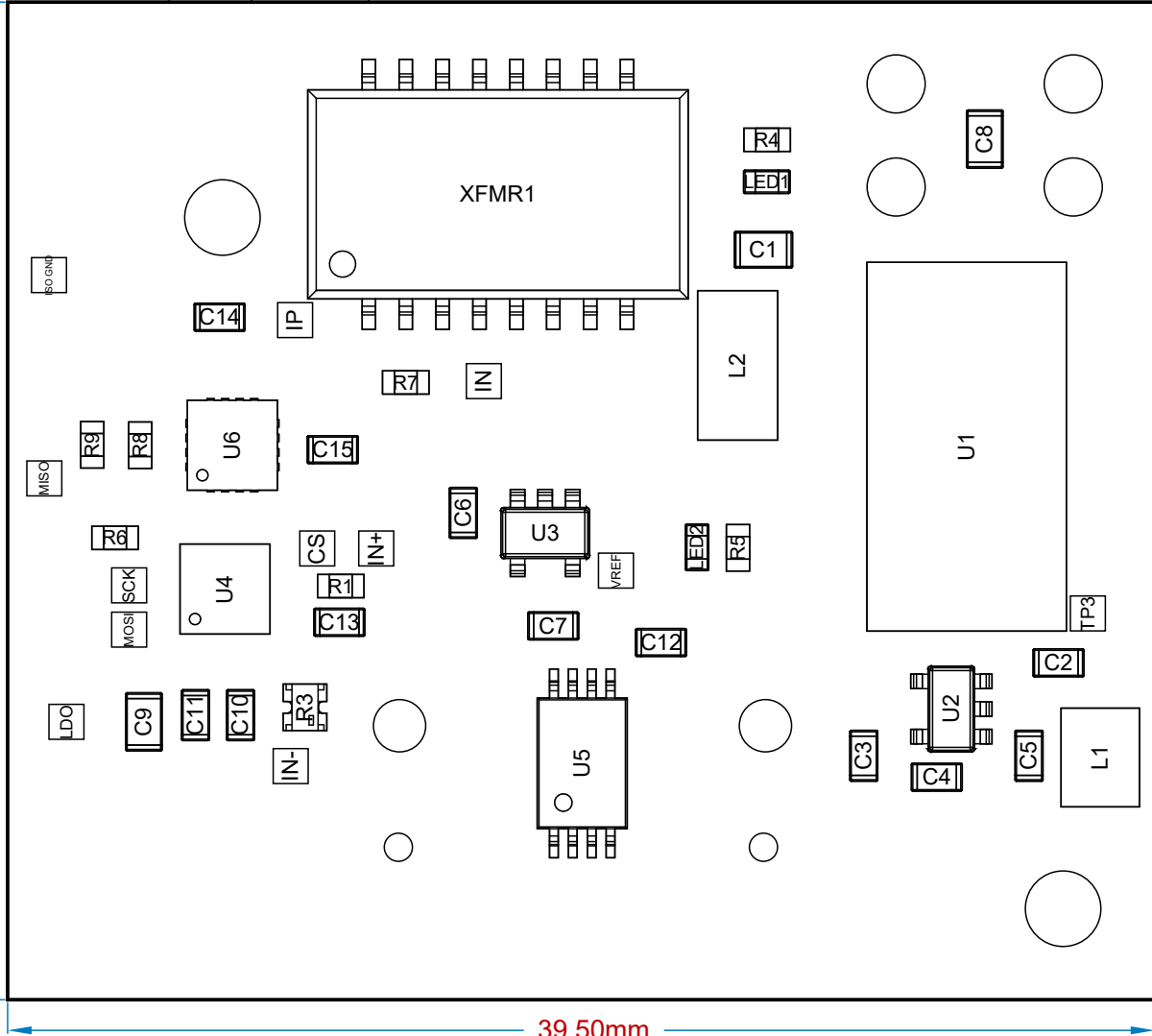
View from Top side (Scale 1:1)



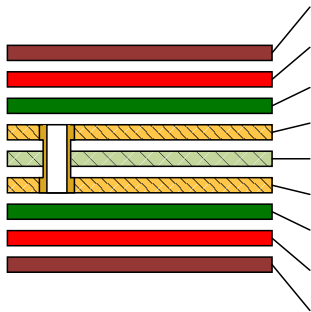
Notes:

- 1 Mating: Molex 0334824001
2. Layer stack based on data from Oshpark

View from Top side (Scale 4:1)

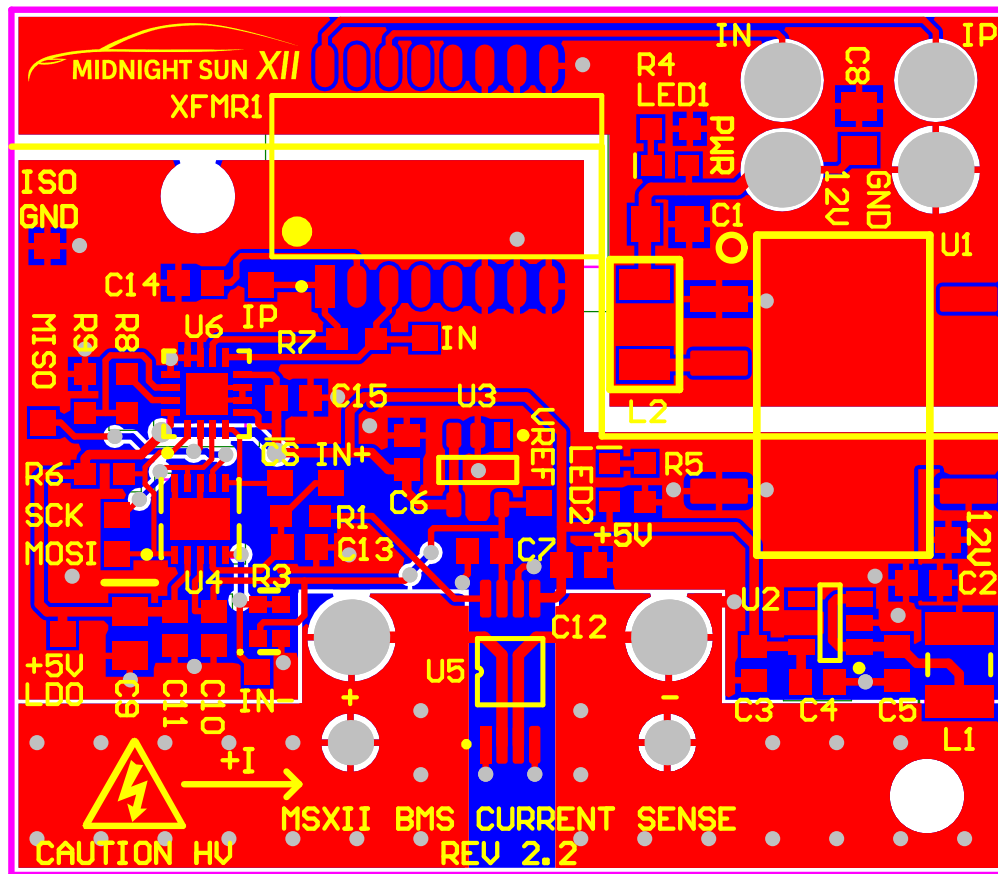


Layer Stack Legend



Total thickness: 1.60mm

Top Paste				Paste Mask	GTP
Top Overlay				Legend	GTO
Top Solder	0.00mm	Solder Resist		Solder Mask	GTS
Top Layer	0.04mm			Signal	GTL
	1.52mm	FR-4		Dielectric	
Bottom Layer	0.04mm			Signal	GBL
Bottom Solder	0.00mm	Solder Resist		Solder Mask	GBS
Bottom Overlay				Legend	GBO
Bottom Paste				Paste Mask	GBP



MIDNIGHT SUN XII
XFMR1

ISO
GND

R4
LED1

PMR

C1
O

C8

12V

GND

IP

C14

U6

IP

R7

IN

C15

U3

VREF

LED2

+5V

L2

MISO

R9

R8

R6

SCK

MOSI

CS

IN+

C6

R1

C13

U4

R3

IN-

+5V
LDO

C9

C11

C10

U5

C12

U2

C3

C4

C5

C2

L1



+I

MSXII

BMS

CURRENT

SENSE

REV 2.2