

Preview - temp_summary.pdf

Resource Usage Report for logic_shifter

Parameter Value: 2

Module	port_count	cell_count	wire_bits	port_bits	hierarchy_depth	wire_count	full_adder	\$_ANDNOT_	\$_MUX_	\$_NOT_	\$_NAND_	\$_ORNOT_	\$_NOR_
logic_shifter	4	6	17	6	0	12	1	2	4	0	0	0	0

Parameter Value: 3

Module	port_count	cell_count	wire_bits	port_bits	hierarchy_depth	wire_count	full_adder	\$_ANDNOT_	\$_MUX_	\$_NOT_	\$_NAND_	\$_ORNOT_	\$_NOR_
logic_shifter	4	21	44	9	0	28	1	2	4	2	1	2	1

Parameter Value: 4

Module	port_count	cell_count	wire_bits	port_bits	hierarchy_depth	wire_count	full_adder	\$_ANDNOT_	\$_MUX_	\$_NOT_	\$_NAND_	\$_ORNOT_	\$_NOR_
logic_shifter	4	36	71	12	0	44	1	2	4	4	2	4	2

Parameter Value: 5

Module	port_count	cell_count	wire_bits	port_bits	hierarchy_depth	wire_count	full_adder	\$_ANDNOT_	\$_MUX_	\$_NOT_	\$_NAND_	\$_ORNOT_	\$_NOR_
logic_shifter	4	51	98	15	0	60	1	2	4	6	3	6	3

Parameter Value: 6

Module	port_count	cell_count	wire_bits	port_bits	hierarchy_depth	wire_count	full_adder	\$_ANDNOT_	\$_MUX_	\$_NOT_	\$_NAND_	\$_ORNOT_	\$_NOR_
logic_shifter	4	66	125	18	0	76	1	2	4	8	4	8	4

Parameter Value: 7

Module	port_count	cell_count	wire_bits	port_bits	hierarchy_depth	wire_count	full_adder	\$_ANDNOT_	\$_MUX_	\$_NOT_	\$_NAND_	\$_ORNOT_	\$_NOR_
logic_shifter	4	81	152	21	0	92	1	2	4	10	5	10	5

Parameter Value: 8

Module	port_count	cell_count	wire_bits	port_bits	hierarchy_depth	wire_count	full_adder	\$_ANDNOT_	\$_MUX_	\$_NOT_	\$_NAND_	\$_ORNOT_	\$_NOR_
logic_shifter	4	96	179	24	0	108	1	2	4	12	6	12	6