Resource Usage Report for conditional_shifter

Table of Contents

Parameter Value: 2

_bits	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$pa
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	
04	35	169	1	9	6	93	1	
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	

Parameter Value: 3

_bits	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$pa
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	
04	35	169	1	9	6	93	1	
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	

Parameter Value: 4

_bits	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$pa
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	
04	35	169	1	9	6	93	1	
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	

Parameter Value: 5

_bits	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$pa
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	
04	35	169	1	9	6	93	1	
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	

Parameter Value: 6

_bits	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$pa
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	
04	35	169	1	9	6	93	1	
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	

Parameter Value: 7

_bits	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$pa
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	
04	35	169	1	9	6	93	1	
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	

Parameter Value: 8

_bits	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$pa
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	
04	35	169	1	9	6	93	1	
0	475	1025	0	463	4	31	0	
0	480	983	0	467	4	62	0	