Resource Usage Report for full_adder

Table of Contents

Parameter Value: None

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	$_AND_	half_adder	$_OR_	$_XOR_	$_AND_	$_OR_	$_XOR_
full_adder	8	1	5	5	3	8	2	2	1	2	2	1	2
full_adder -> half_adder	4	0	4	4	2	4	1	0	0	1	1	0	1
half_adder	4	0	4	4	2	4	1	0	0	1	1	0	1