

**Preview - temp\_summary.pdf**

**Resource Usage Report for ripple\_carry\_subtractor**

Parameter Value: 1

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	half_subtractor	\$_OR_	full_subtractor	\$_XOR_	\$_ANDNOT_	\$_XOR_	\$_OR_	\$_ANDNOT_
full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2
full_subtractor -> half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
ripple_carry_subtractor	6	2	5	8	2	11	0	2	2	4	4	4	2	4
ripple_carry_subtractor -> full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2

Parameter Value: 2

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	half_subtractor	\$_OR_	full_subtractor	\$_XOR_	\$_ANDNOT_	\$_XOR_	\$_OR_	\$_ANDNOT_
full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2
full_subtractor -> half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
ripple_carry_subtractor	6	2	5	8	2	11	0	2	2	4	4	4	2	4
ripple_carry_subtractor -> full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2

Parameter Value: 3

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	half_subtractor	\$_OR_	full_subtractor	\$_XOR_	\$_ANDNOT_	\$_XOR_	\$_OR_	\$_ANDNOT_
full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2
full_subtractor -> half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
ripple_carry_subtractor	6	2	5	8	2	11	0	2	2	4	4	4	2	4
ripple_carry_subtractor -> full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2

Parameter Value: 4

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	half_subtractor	\$_OR_	full_subtractor	\$_XOR_	\$_ANDNOT_	\$_XOR_	\$_OR_	\$_ANDNOT_
full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2
full_subtractor -> half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
ripple_carry_subtractor	6	2	5	8	2	11	0	2	2	4	4	4	2	4
ripple_carry_subtractor -> full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2

Parameter Value: 5

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	half_subtractor	\$_OR_	full_subtractor	\$_XOR_	\$_ANDNOT_	\$_XOR_	\$_OR_	\$_ANDNOT_
full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2
full_subtractor -> half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
ripple_carry_subtractor	6	2	5	8	2	11	0	2	2	4	4	4	2	4
ripple_carry_subtractor -> full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2

Parameter Value: 6

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	half_subtractor	\$_OR_	full_subtractor	\$_XOR_	\$_ANDNOT_	\$_XOR_	\$_OR_	\$_ANDNOT_
full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2
full_subtractor -> half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
ripple_carry_subtractor	6	2	5	8	2	11	0	2	2	4	4	4	2	4
ripple_carry_subtractor -> full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2

Parameter Value: 7

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	half_subtractor	\$_OR_	full_subtractor	\$_XOR_	\$_ANDNOT_	\$_XOR_	\$_OR_	\$_ANDNOT_
full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2
full_subtractor -> half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
ripple_carry_subtractor	6	2	5	8	2	11	0	2	2	4	4	4	2	4
ripple_carry_subtractor -> full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2

Parameter Value: 8

Module	wire_count	hierarchy_depth	port_count	port_bits	cell_count	wire_bits	half_subtractor	\$_OR_	full_subtractor	\$_XOR_	\$_ANDNOT_	\$_XOR_	\$_OR_	\$_ANDNOT_
full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2
full_subtractor -> half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
half_subtractor	4	0	4	4	2	4	0	0	0	1	1	1	0	1
ripple_carry_subtractor	6	2	5	8	2	11	0	2	2	4	4	4	2	4
ripple_carry_subtractor -> full_subtractor	8	1	5	5	3	8	2	1	0	2	2	2	1	2