Preview - temp_summary.pdf

Resource Usage Report for conditional_shifter

Parameter Value: 2

its	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$para
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	
	35	169	1	9	6	93	1	
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	

Parameter Value: 3

its	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$para
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	
	35	169	1	9	6	93	1	
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	

Parameter Value: 4

its	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$para
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	
	35	169	1	9	6	93	1	
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	

Parameter Value: 5

its	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$para
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	
	35	169	1	9	6	93	1	
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	

Parameter Value: 6

its	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$para
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	
	35	169	1	9	6	93	1	
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	

Parameter Value: 7

its	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$para
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	
	35	169	1	9	6	93	1	
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	

Parameter Value: 8

4

its	cell_count	wire_bits	hierarchy_depth	wire_count	port_count	$_ANDNOT_	$paramod\logic_shifter\N=s32'00000000000000000000000000100000	$para
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	
	35	169	1	9	6	93	1	
	475	1025	0	463	4	31	0	
	480	983	0	467	4	62	0	