

# GATE

December 18, 2023

1. In the circuit shown below,  $Y$  is a 2-bit ( $Y_1Y_0$ ) output of the combinational logic. What is the maximum value of  $Y$  for any given digital inputs,  $A_1A_0$  and  $B_1B_0$  ?

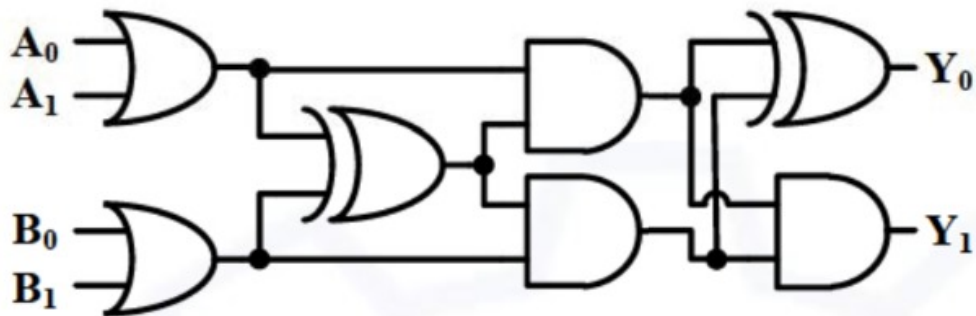


Figure 1:

- (A) 01
- (B) 10
- (C) 00
- (D) 11