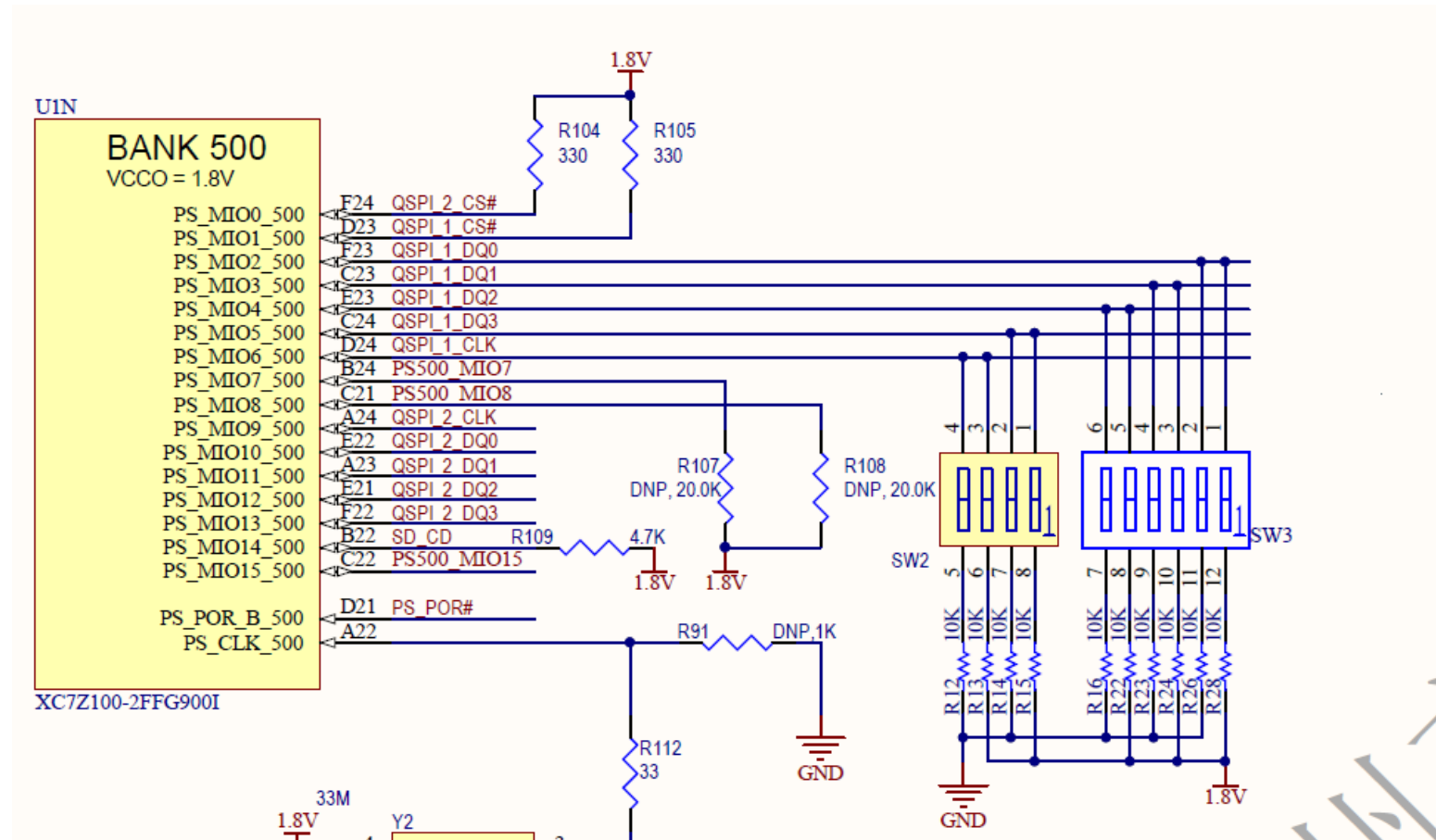ZYNQ7100 具有多种启动方式：本单板使用这三种, JTAG 模式,QSPI 模式,SD 模式

1. 官方设置:三种模式的设置依靠对以下管脚的上下来设置来实现

Table 6-4: **Boot Mode MIO Strapping Pins**

Pin-signal / Mode	MIO[8]	MIO[7]	MIO[6]	MIO[5]	MIO[4]	MIO[3]	MIO[2]
	VMODE[1]	VMODE[0]	BOOT_MODE[4]	BOOT_MODE[0]	BOOT_MODE[2]	BOOT_MODE[1]	BOOT_MODE[3]
Boot Devices							
JTAG Boot Mode; cascaded is most common[1]				0	0	0	JTAG Chain Routing[2] 0: Cascade mode 1: Independent mode
NOR Boot[3]				0	0	1	
NAND				0	1	0	
Quad-SPI[3]				1	0	0	
SD Card				1	1	0	
Mode for all 3 PLLs							
PLL Enabled			0	Hardware waits for PLL to lock, then executes BootROM.			
PLL Bypassed			1	Allows for a wide PS_CLK frequency range.			
MIO Bank Voltage[4]							
	Bank 1	Bank 0	Voltage Bank 0 includes MIO pins 0 thru 15. Voltage Bank 1 includes MIO pins 16 thru 53.				
2.5 V, 3.3 V	0	0					
1.8 V	1	1					

2. 在扣板上的原理图按照以下设计:

3. 所以对应到拨码开关的设置如下图所示,一定要注意图片上拨码开关设置,不要弄错了

	SW2				SW3					
	1	2	3	4	1	2	3	4	5	6
JTAG	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON
QSPI	ON	OFF	OFF	ON	OFF	ON	OFF	ON	OFF	ON
SD	ON	OFF	OFF	ON	OFF	ON	OFF	ON	ON	OFF