

Hierarchical Binary Search Tree for Packet Classification

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Abstract—In order to provide value-added services such as policy-based routing and the quality of services in next generation network, the Internet routers need to classify packets into flows for different treatments. Since packet classification should be performed in wire-speed for every packet incoming in several hundred gigabits per second, it becomes a major challenge in the Internet routers. In this letter, we propose a new packet classification scheme based on hierarchical binary search tree. The proposed scheme hierarchically connects binary search trees without empty internal nodes, and hence the proposed architecture significantly improves the search performance as well as greatly reduces the memory requirement compared with trie-based schemes.

Index Terms—Packet classification, hierarchical trie, binary search tree, hierarchical binary search tree.

I. INTRODUCTION

PACKET classification is indispensable for the Internet routers to provide the quality of services. Packet classification is to classify incoming packets into flows based on pre-defined rules so that routers can treat each packet according to the service defined in the class that the input packet belongs to [1]. Classes are defined by rules composed of multiple header fields, mainly destination prefix, source prefix, destination port number, source port number, and protocol type. Each field requires different matching operations such as the exact matching for the protocol type, prefix matching for prefixes, and range matching for port numbers. The difficulty in the packet classification is that it not only involves complicated matching operations but also has to identify the highest priority rule among all matching rules. Moreover, the packet classification should be performed in real-time for every packet incoming in several million packets per second, and hence the search speed is the major concern of the packet classification. This letter proposes a new packet classification scheme providing high throughput and low memory requirement.

Linear search is the simplest solution for packet classification and it consumes the smallest amount of memory. However, for a large rule set, the search performance of the linear search does not satisfy the real-time requirement. There are various approaches of packet classification, and they can be categorized into three groups. The first group is based on independent 1-dimensional searches. Bit-vector algorithm and cross-product algorithm are included in this

TABLE I
AN EXAMPLE RULE SET

Rule No.	Src prefix	Dst prefix	Src port (start,end)	Dst port (start, end)	Protocol
0	1110*	*	53, 53	443, 443	17
1	111*	101*	53, 53	25, 25	6
2	*	10*	53, 53	25, 25	17
3	101*	11*	67, 67	5632, 5632	6
4	01*	0011*	1024, 65535	1024, 65535	6
5	101*	11*	53, 53	25, 25	4
6	11*	0100*	0, 65535	5632, 5632	6
7	101*	1011*	0, 65535	5632, 5632	6
8	*	10*	53, 53	25, 25	6
9	11*	11*	0, 15576	2783, 2783	4
10	010*	00*	53, 53	443,443	17
11	11*	00*	53, 53	25, 25	6
12	101*	0*	0, 65535	5632, 5632	6
13	01*	1*	53, 53	443, 443	17
14	01*	0*	0, 65535	5632, 5632	6

group [2], [3]. The second group is based on the heuristic characteristics of classifiers, and the tuple-space search, hierarchical intelligent cutting, and hyper-cutting algorithms are included in this group [4]. The third group is based on trie for fields represented by prefixes, and the hierarchical trie, set-pruning trie, and grid-of-trie algorithms are belonged to this group [5]. Area-based quad-tree (AQT) [6] and priority-based quad-tree [7] algorithms are the 2-dimensional variation of this group. Some of the trie-based algorithms follow the hierarchical approach of the packet classification. The hierarchical approach is known to be slow because of back-tracking problem. However, we claim that, if there is not many prefix nesting relationship so that the back-tracking is not frequently occurred, the hierarchical approach achieves high speed search performance since search space is significantly reduced every time a field search is completed. The hierarchical approach of the packet classification is to recursively perform search in each field. Using the source prefix field, all matching candidate rules are identified, and for those candidates, rules are further filtered using the destination prefix field, and so on. The issue is what kind of data structures should be used for each field.

Hierarchical-trie (H-trie) [1], [4] scheme uses binary tries for the fields represented by prefixes for packet classification. Search firstly follows the source prefix trie, and the destination prefix trie is examined only when the source prefix is matched. Since packet classification is to search for the matched rule with the highest priority, even though a match is found, search has to go back to the source prefix trie and repeat until the entire source trie is examined. Set-pruning trie removes the back-tracking by copying all ancestor rules into leaves [4], and grid-of-trie removes the back-tracking by pre-computing switch pointers in each node [5]. Table I shows an example rule set which is generated using the class-bench databases [8].

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TABLE II

PROPOSED TREE TABLE FOR THE RULE SET IN TABLE I

Prefix	Length	Left	Right	RulePtr
*	0	1	2	2
01*	2	3	-	14
101*	3	4	5	7
010*	3	-	-	10
1000*	4	-	-	0
11*	2	-	6	6
111*	3	-	-	1

TABLE III

PROPOSED RULE TABLE FOR THE RULE SET IN TABLE I

Rule	Prefix	Length	SrcPort	DstPort	Protocol	Left	Right	Linked
0	*	0	53, 53	443, 443	17	-	-	-
1	101*	3	53, 53	25, 25	6	-	-	-
2	10*	2	53, 53	25, 25	17	-	-	8
3	11*	2	67, 67	5632, 5632	6	-	-	5
4	0011*	4	1024, 65535	1024, 65535	6	-	-	-
5	11*	2	53, 53	25, 25	4	-	-	-
6	0100*	4	0, 65535	5632, 5632	6	11	9	-
7	1011*	4	0, 65535	5632, 5632	6	12	3	-
8	10*	2	53, 53	25, 25	6	-	-	-
9	11*	2	0, 15576	2783, 2783	4	-	-	-
10	00*	2	53, 53	443, 443	17	-	-	-
11	00*	2	53, 53	25, 25	6	-	-	-
12	0*	1	0, 65535	5632, 5632	6	-	-	-
13	1*	1	53, 53	443, 443	17	-	-	-
14	0*	1	0, 65535	5632, 5632	6	4	13	-

the input is not matched with the current entry since rules are linked in the order of priority. If the current entry has valid child pointers, search follows a child pointer. If there are no more pointers to proceed, search goes back to the tree table. As already mentioned, the back-tracking is an intrinsic problem of the hierarchical approach, and hence the rule table is visited whenever a match occurs in the tree table. However, the number of visiting is limited by the number of source prefix nesting. As will be shown in the simulation result in the next section, the maximum number of prefix nesting is 3 or 4 in packet classification table, and hence the rule table is visited 3 or 4 times at maximum.

III. PERFORMANCE EVALUATION

Simulations have been performed using rule sets provided by class-bench [8]. Three different types of rule sets, access control list (ACL), firewall (FW), and IP chain (IPC), are generated, and the sizes of rule sets are between 50 and 5000 rules. In Table IV, we have shown performance comparison between the proposed HBST and the H-trie in terms of the average number of memory accesses (T_{avg}), the maximum number of memory accesses (T_{max}), and the required memory sizes in kbyte (M) according to the number of rules (N). The number of prefix nesting is also shown. As shown in the average and the maximum number of memory accesses, the proposed scheme shows 3 to 8 times faster search speed than the H-trie. In the required memory size, the proposed scheme requires 2 to 9 times less memory than the H-trie.

Table V shows the performance comparison with other schemes for rule sets with 5000 rules. The memory requirement of the proposed scheme is close to the linear search

TABLE IV

PERFORMANCE COMPARISON FOR ACL, IPC, AND FW TYPE RULE SETS

	N	Prefix Nesting	T_{avg}		T_{max}		M (kB)	
			H-trie	HBST	H-trie	HBST	H-trie	HBST
ACL50	49	4	83.59	10.99	108	18	11.33	1.34
ACL100	100	4	87.13	12.75	132	20	18.95	2.53
ACL500	450	4	91.94	20.64	154	55	40.94	9.46
ACL1k	958	4	77.17	17.46	124	41	82.91	20.20
ACL5k	4660	4	84.01	21.16	177	69	410.5	101.9
IPC50	50	4	63.31	10.12	103	13	13.83	1.46
IPC100	100	4	66.63	10.90	104	19	24.53	2.83
IPC500	497	4	72.49	17.44	129	30	92.33	12.63
IPC1k	988	4	71.91	21.11	128	29	121.57	22.28
IPC5k	4468	4	85.64	26.61	192	52	224.70	92.88
FW50	50	4	53.75	10.39	139	19	5.02	1.12
FW100	98	4	50.57	15.58	134	27	9.93	2.34
FW500	425	4	83.43	34.20	201	75	23.37	9.37
FW1k	871	3	52.14	19.80	117	35	39.44	19.26
FW5k	4351	3	69.20	36.75	162	72	119.10	89.88

TABLE V

PERFORMANCE COMPARISON FOR 5K RULE SETS

		Linear Search	H-Trie [4]	BV [2]	AQT [6]	PQT [7]	HBST
ACL5k	T_{max}	4660	177	76	94	113	69
	T_{avg}	2399.0	84.01	64.10	50.11	59.61	21.16
	M	86.47	410.48	2793.2	200.22	149.09	101.94
IPC5k	T_{max}	4468	192	230	415	295	52
	T_{avg}	1957.2	85.64	151.86	344.79	202.07	26.61
	M (kB)	82.90	224.70	2351.5	234.26	142.94	92.88
FW5k	T_{max}	4351	162	1044	1193	999	72
	T_{avg}	2292.3	69.20	738.75	660.12	571.06	36.75
	M (kB)	80.73	119.10	2394.7	479.77	138.98	89.88

which is the minimum bound. In the search speed evaluated by the maximum and the average number of memory accesses, the proposed scheme is the best. H-trie shows the next best performance for IPC type and FW type. While the performance of other schemes heavily depends on the characteristics of classifier types, the performance of the proposed HBST scheme is very consistent. The simulation results also show that the proposed scheme provides very good scalability.

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