

PIMPRI CHINCHWAD EDUCATION TRUST'S
PIMPRI CHINCHWAD COLLEGE OF ENGINEERING
 Sector No. 26, Pradhikaran, Nigdi, Pune - 411044

Department Of Information Technology
 Year and Course T.Y - B.Tech PRN No.: 12201P113

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Sr. No.	Title	Page No.	Date	Signature	Remarks
1)	Implement Merge sort to efficiently organize large scale order based on timestamp (O(n log n) time/space and conquer	-	9-7-25	<i>[Signature]</i> 10/10	②
2)	Implement Quick sort to ensure personalized movie ratings and improve responsiveness (O(n log n) and conquer)	-	23-7-25	<i>[Signature]</i> 10/10	③
3)	Implement Bellman Ford algorithm to maximize utility value within given weight constraint. (Greedy Approach)	-	6-8-25	<i>[Signature]</i> 10/10	④

This is certify that Shri. Vivek Salunke has carried out the above mentioned Design Analysis and algorithm Practicals / Term Work in the Information Technology Department of Pimpri Chinchwad College of Engineering, Pune-44.

Date : 5-11-25

[Signature]
H.O.D. Information Technology
 Pimpri Chinchwad College of Engineering
 Sect. 26, Pradhikaran, Nigdi, Pune-411044
 Subject Incharge
 Head of Department
 Pimpri Chinchwad College of Engineering, Pune - 44

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4)	Smart Traffic management Implement algorithm the optimal ambulance route under dynamic traffic (conditional greedy Approach)	-	20-8-25 1-10-25	<i>[Signature]</i> <i>[Signature]</i>	<i>[Signature]</i> <i>[Signature]</i>
5)	Optimal Delivery Routing Implement DP Algorithm Cost effective delivery paths (Dynamic programming)	-	10-9-25	<i>[Signature]</i> <i>[Signature]</i>	<i>[Signature]</i>
6)	Minimise Route Recourse Parallelized vlc of Kamplack approach to optimization e-Card. Supply Shortest Connected programming	-	24-9-25 24-9-25	<i>[Signature]</i> <i>[Signature]</i>	<i>[Signature]</i>

This is certify that Shri. Vivek Salunkhe has carried out the
above mentioned Design analysis and Algorithm Practicals / Term Work
in the Information Technology Department of Pimpri Chinchwad College of Engineering, Pune-44.





[Signature]
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Date : 5-11-25

Department Of Information Technology

Year and Course T.Y - B.Tech PRN No.: 12201F1113

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71	University Exam Tentative Scheduling- Implement Graph coloring Technique to generate conflict free and efficient timetable (Mark-timetable)	-	-10-25		
81	Delivery Route optimization How apply the branch and bound Algorithm to optimize the cost of delivery routes avoid closed branches and nodes	-	8-10-25		

This is certify that Shri. Vijay Salunke has carried out the Practicals / Term Work of Network analysis and Synthesis in the subject of Engineering Pune-44.

in the Information Technology Department of Information Technology
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e. 11-25
S/N/14

Date: 5-11-25