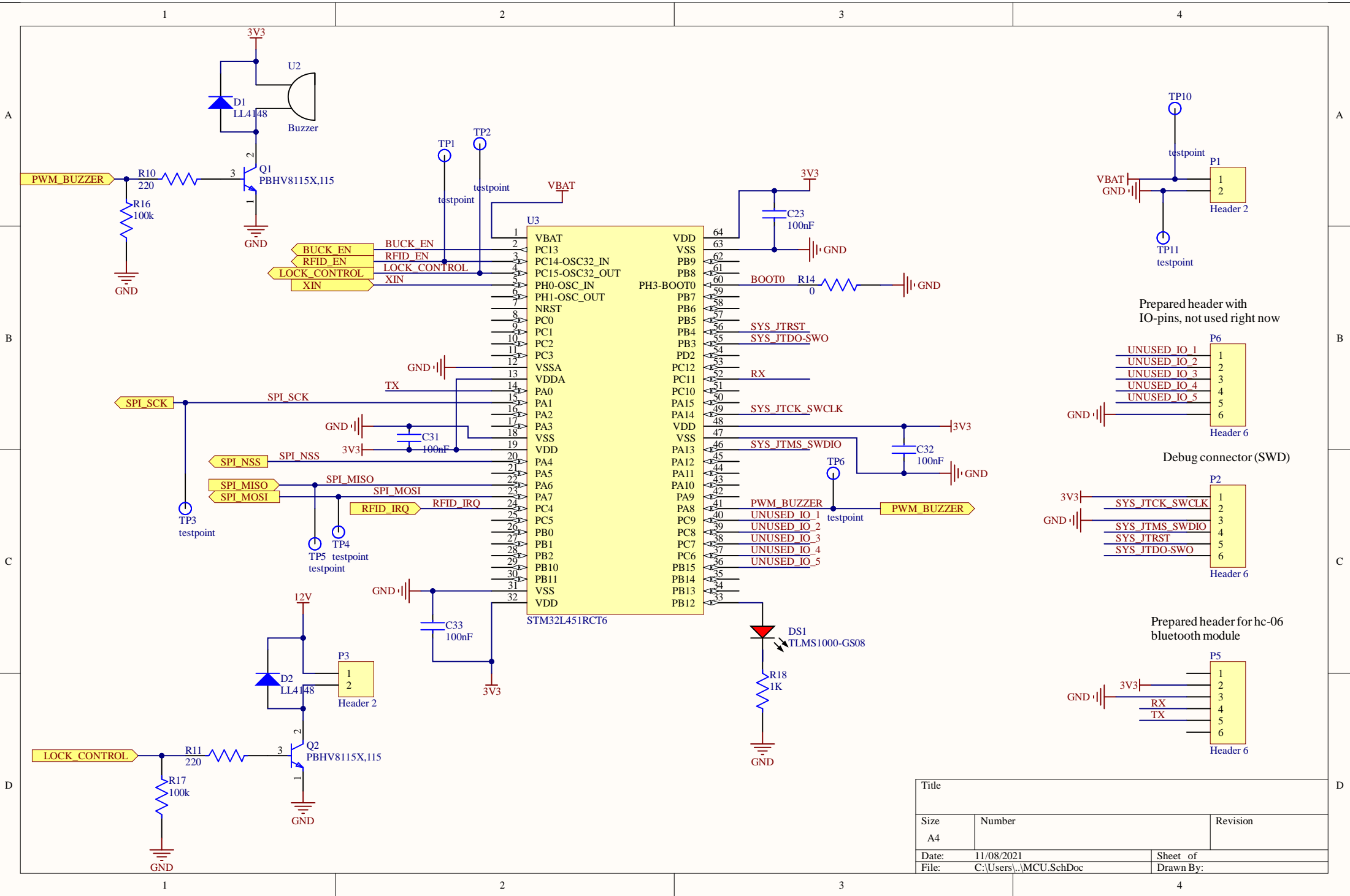


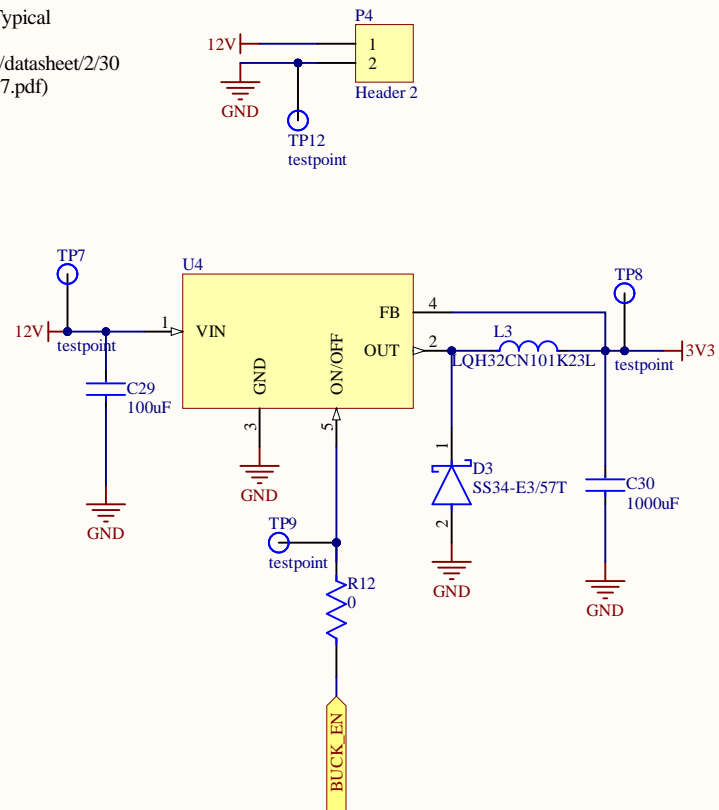
Designed according to Figure 7-2.  
Application Schematic for the TRF796x  
EVM (SPI Mode) in datasheet for  
TRF7960RHBT  
(<https://www.ti.com/lit/ds/symlink/trf7961.pdf>  
f?ts=1636102370290&ref\_url=https%253A%  
252F%252Fwww.google.com%252F)

Title		
Size	Number	Revision
A4		
Date:	11/08/2021	Sheet of
File:	C:\Users\...\RFID.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	11/08/2021	Sheet of
File:	C:\Users\...\MCU.SchDoc	Drawn By:

Designed according to Typical  
application in datasheet  
([https://www.mouser.se/datasheet/2/308/1/LM2576\\_D-2314897.pdf](https://www.mouser.se/datasheet/2/308/1/LM2576_D-2314897.pdf))



Title		
Size	Number	Revision
A4		
Date:	11/08/2021	Sheet of
File:	C:\Users\...\Power.SchDoc	Drawn By:

# Board Stack Report