

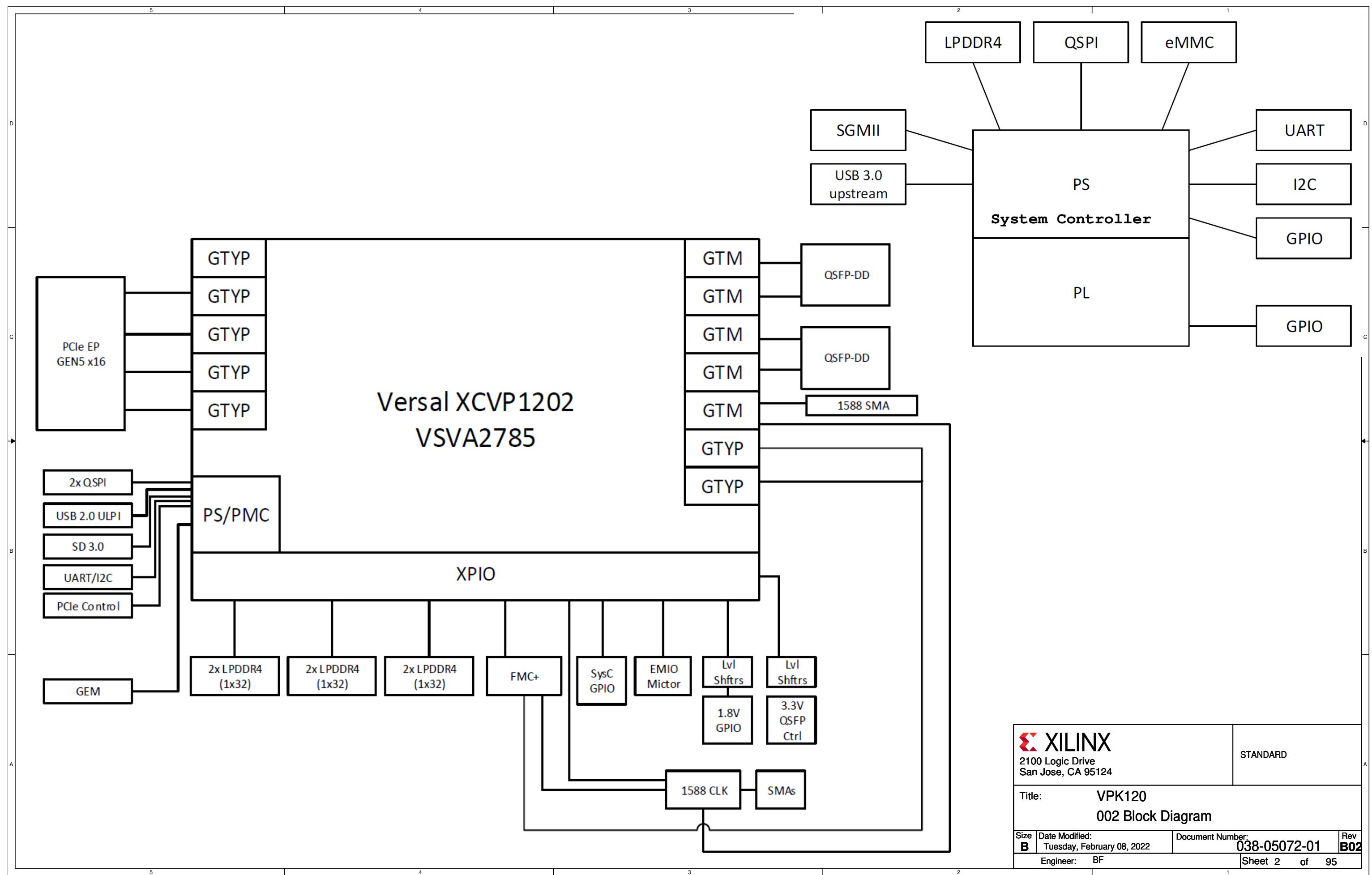
Versal Evaluation Platform

NOTICE: These schematics are made available for users who are working with this specific Xilinx board and should not be considered an optimal board design reference. This board was designed for evaluation purposes with components that are potentially more complex and costly than would be used in an end customer system. Users are encouraged to leverage the Xilinx design guidelines as the optimal starting point for new board designs.

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2100 Logic Drive San Jose, CA 95124		
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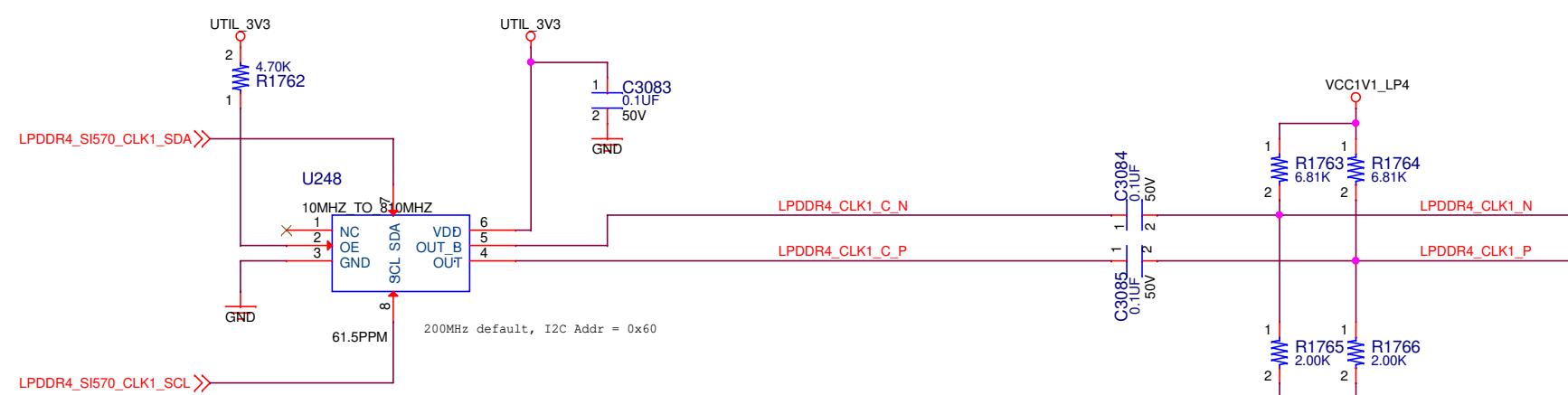
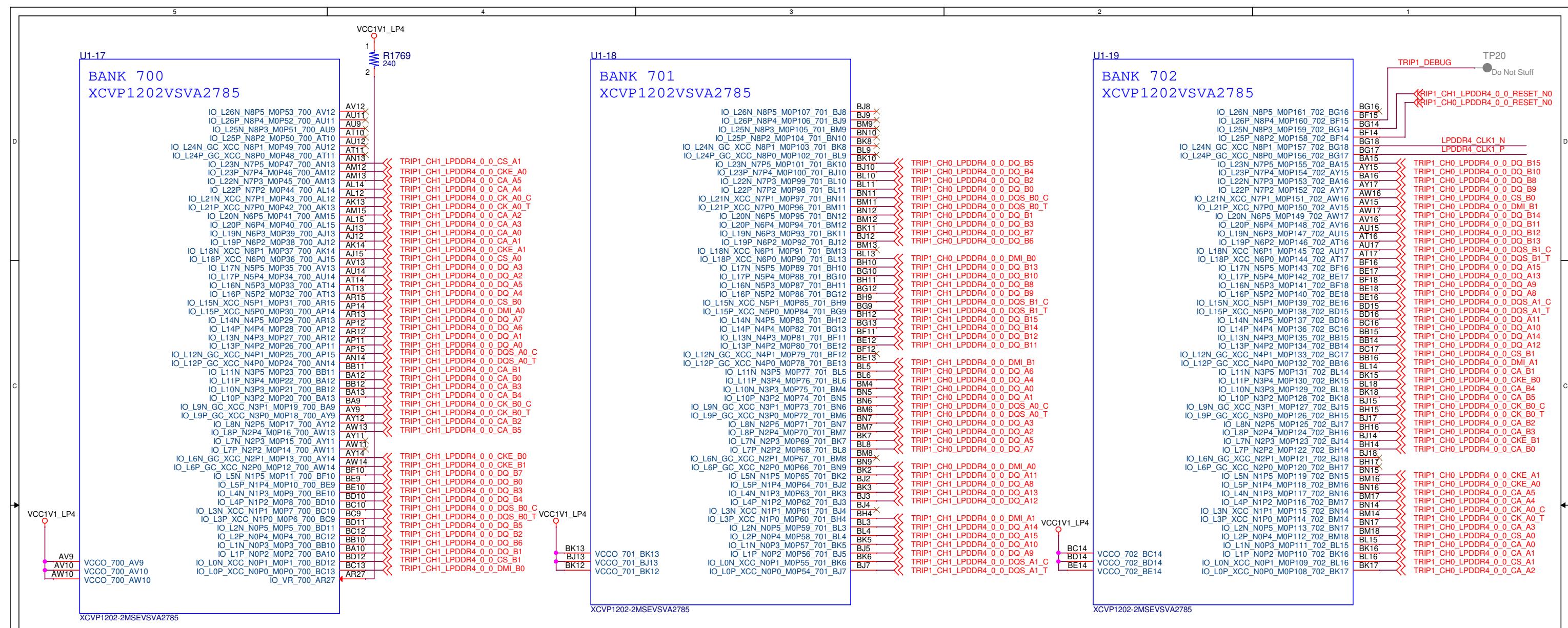
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Title: VPK120
002 Block Diagram

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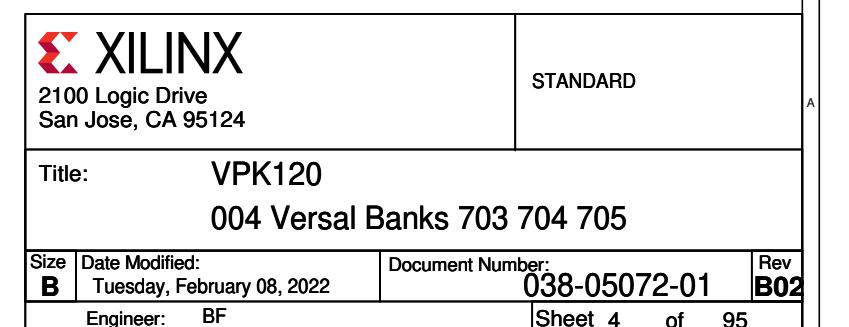
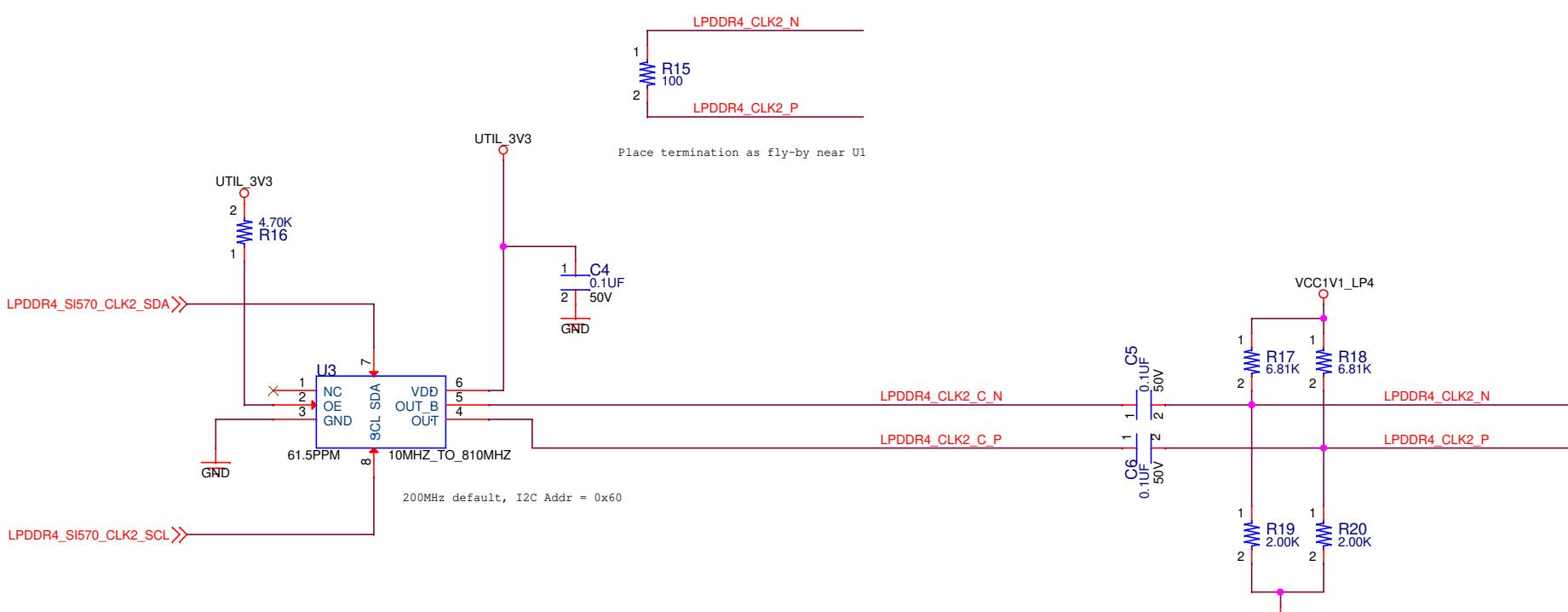
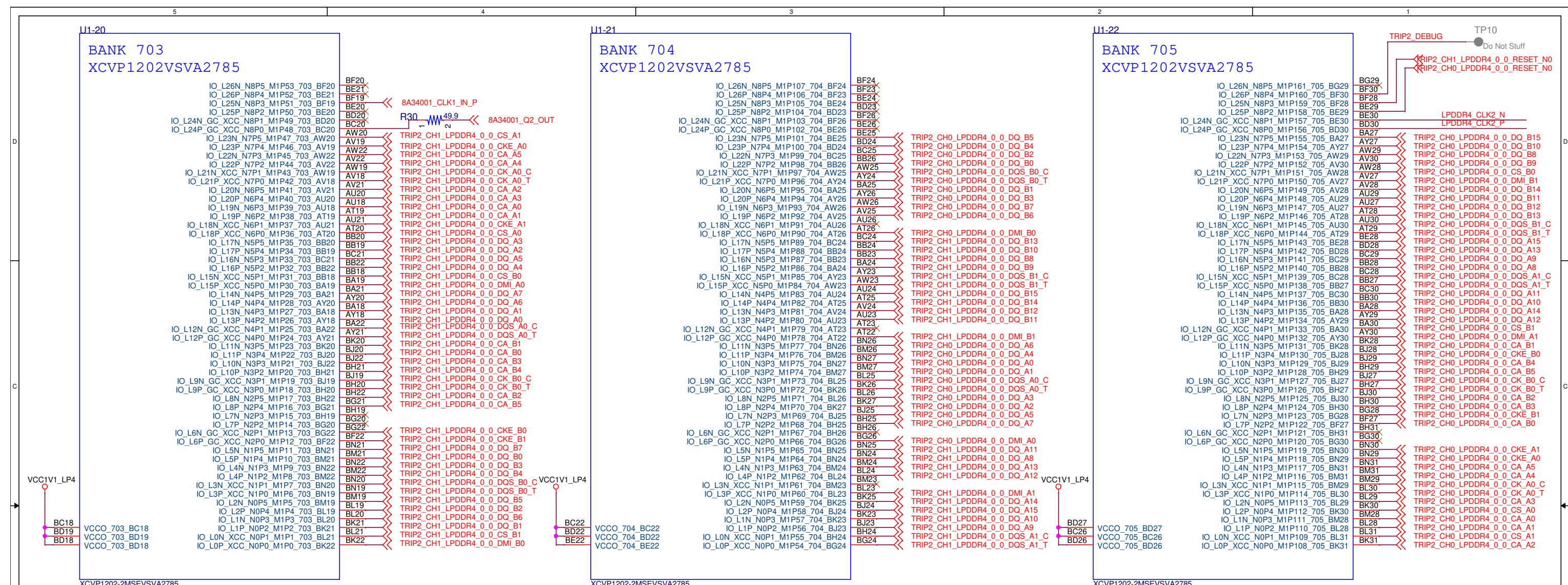


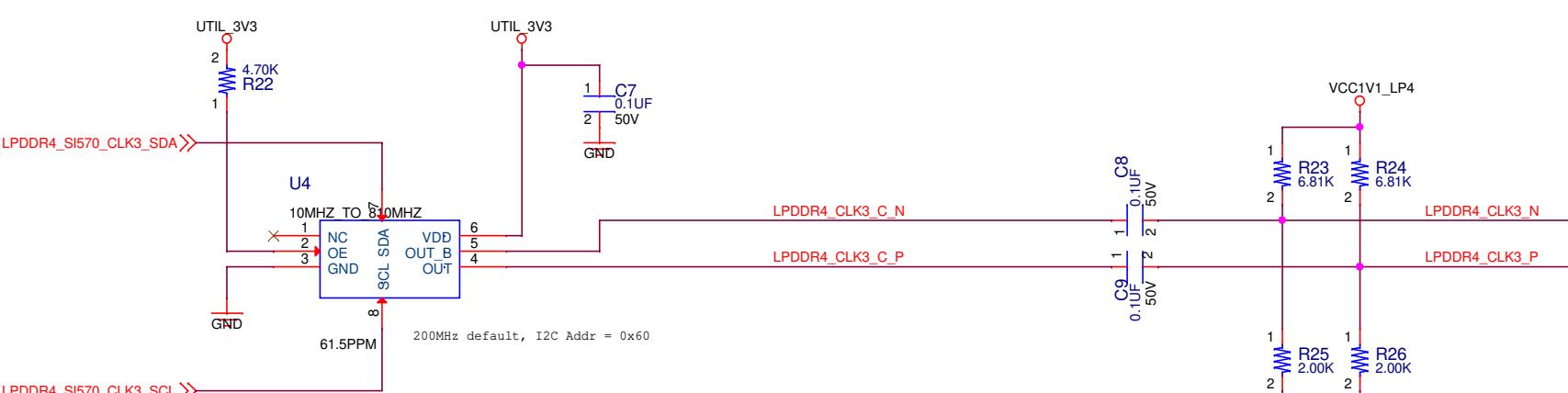
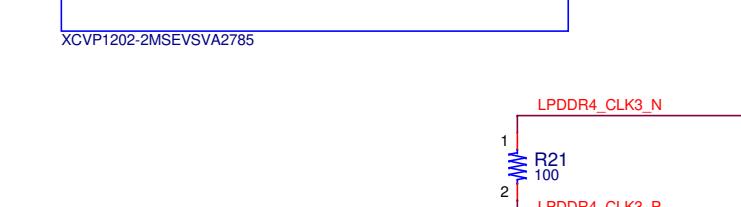
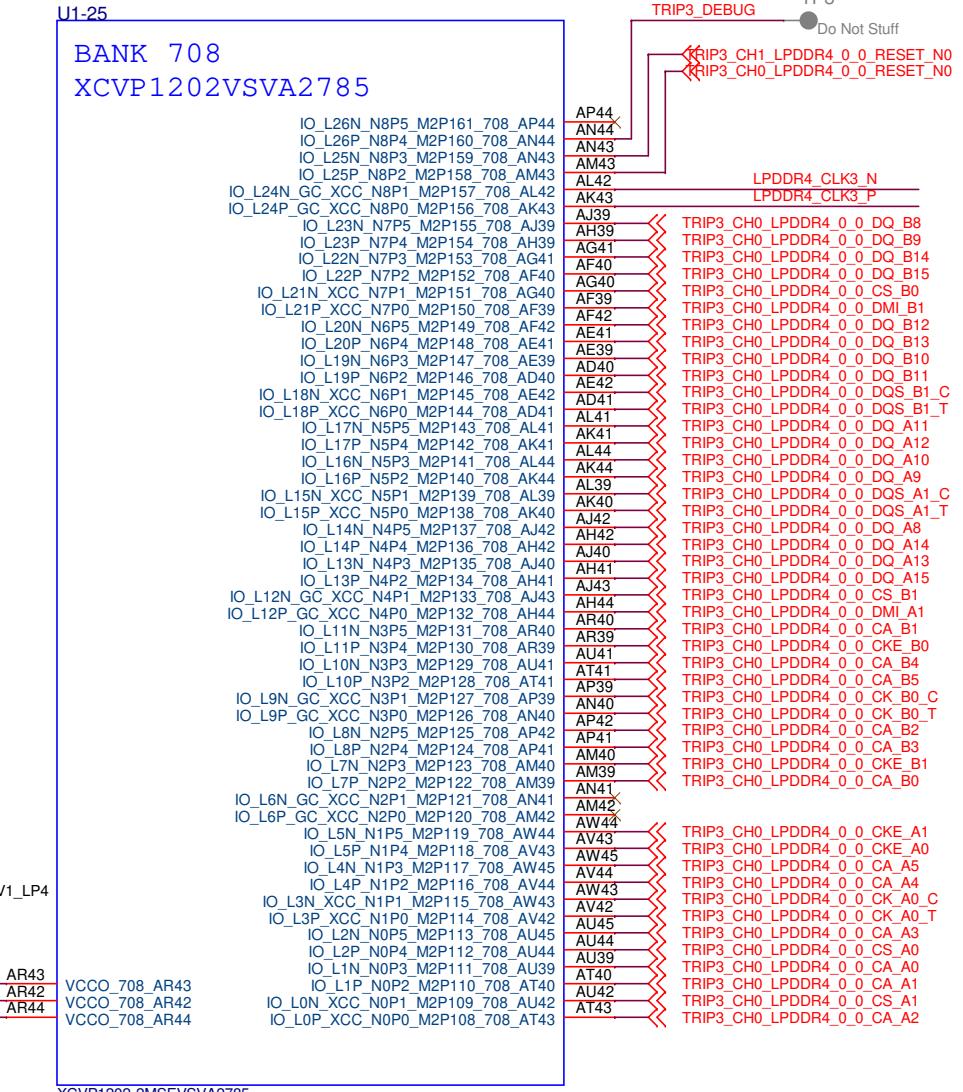
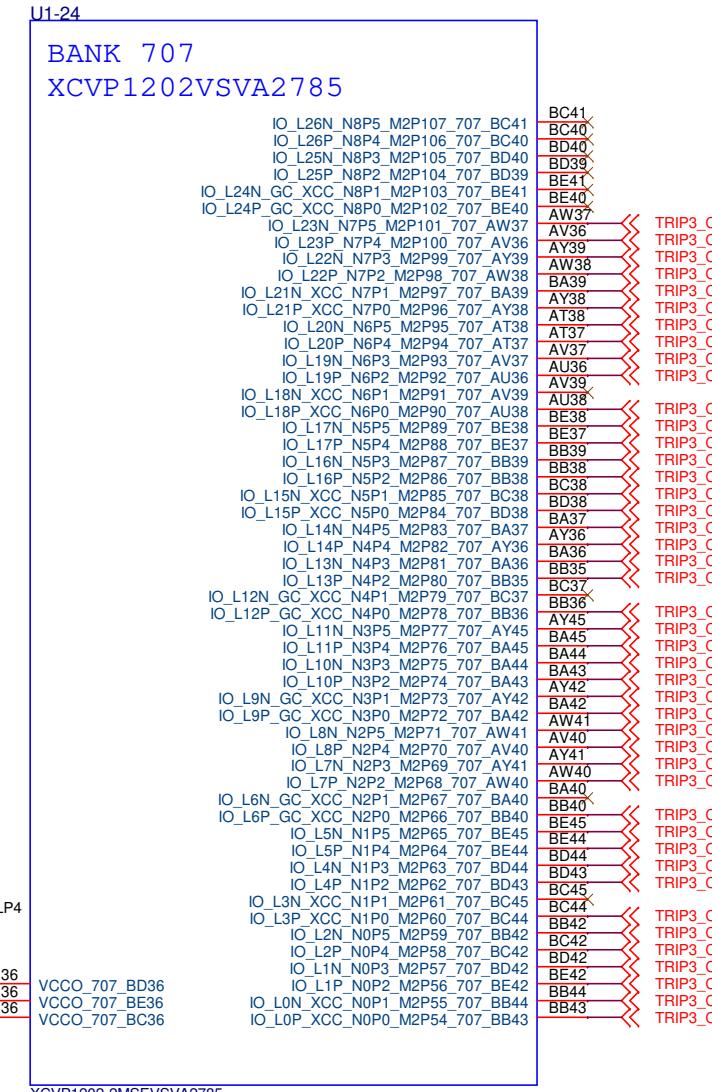
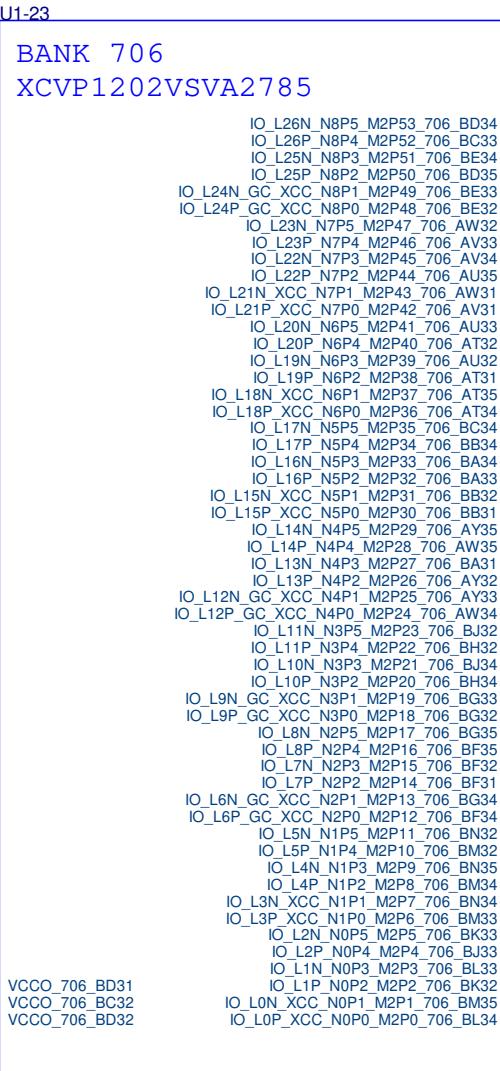
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Title: VPK120
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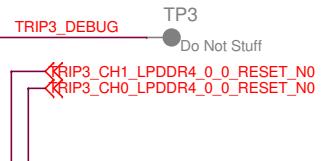


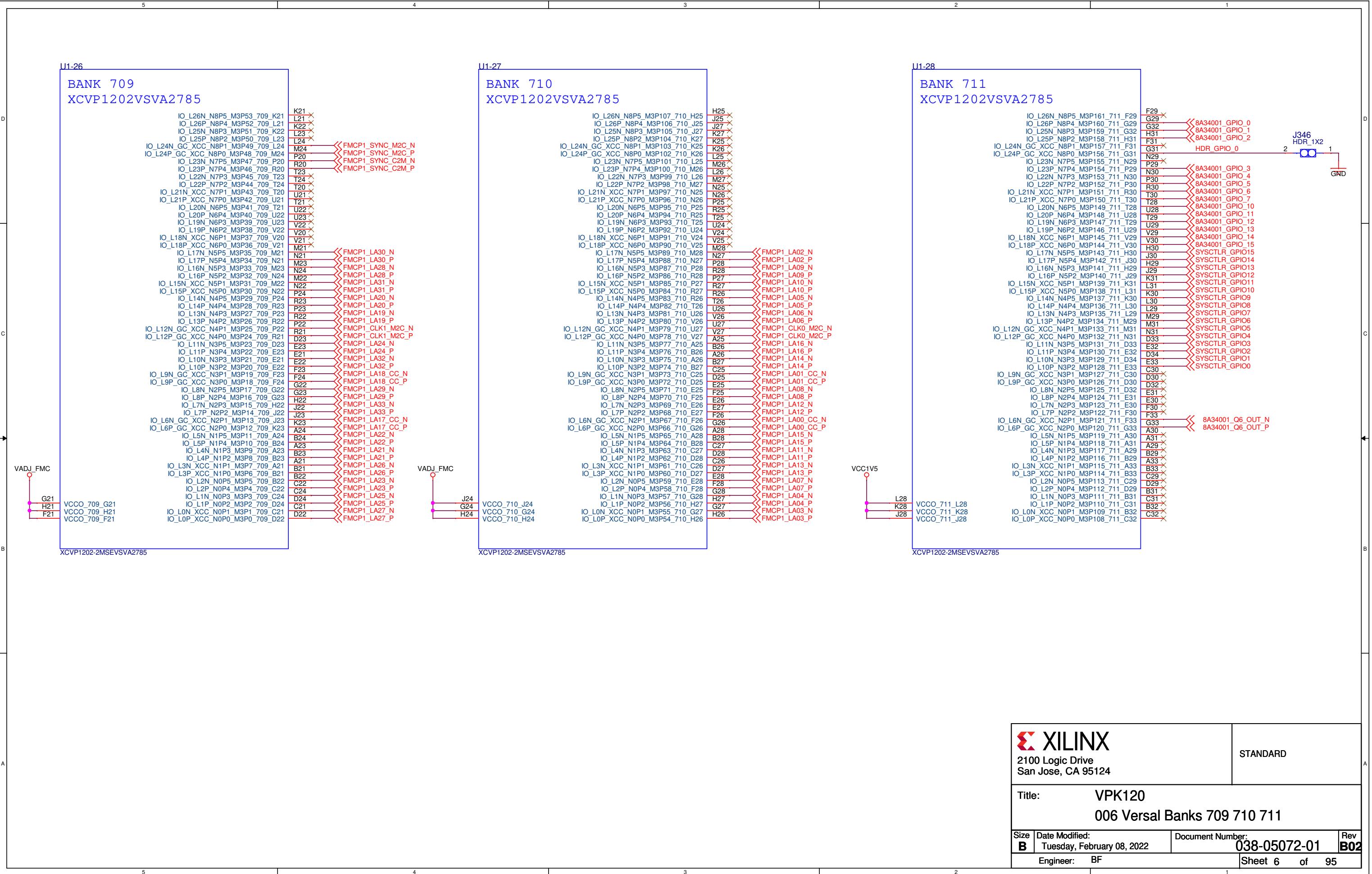
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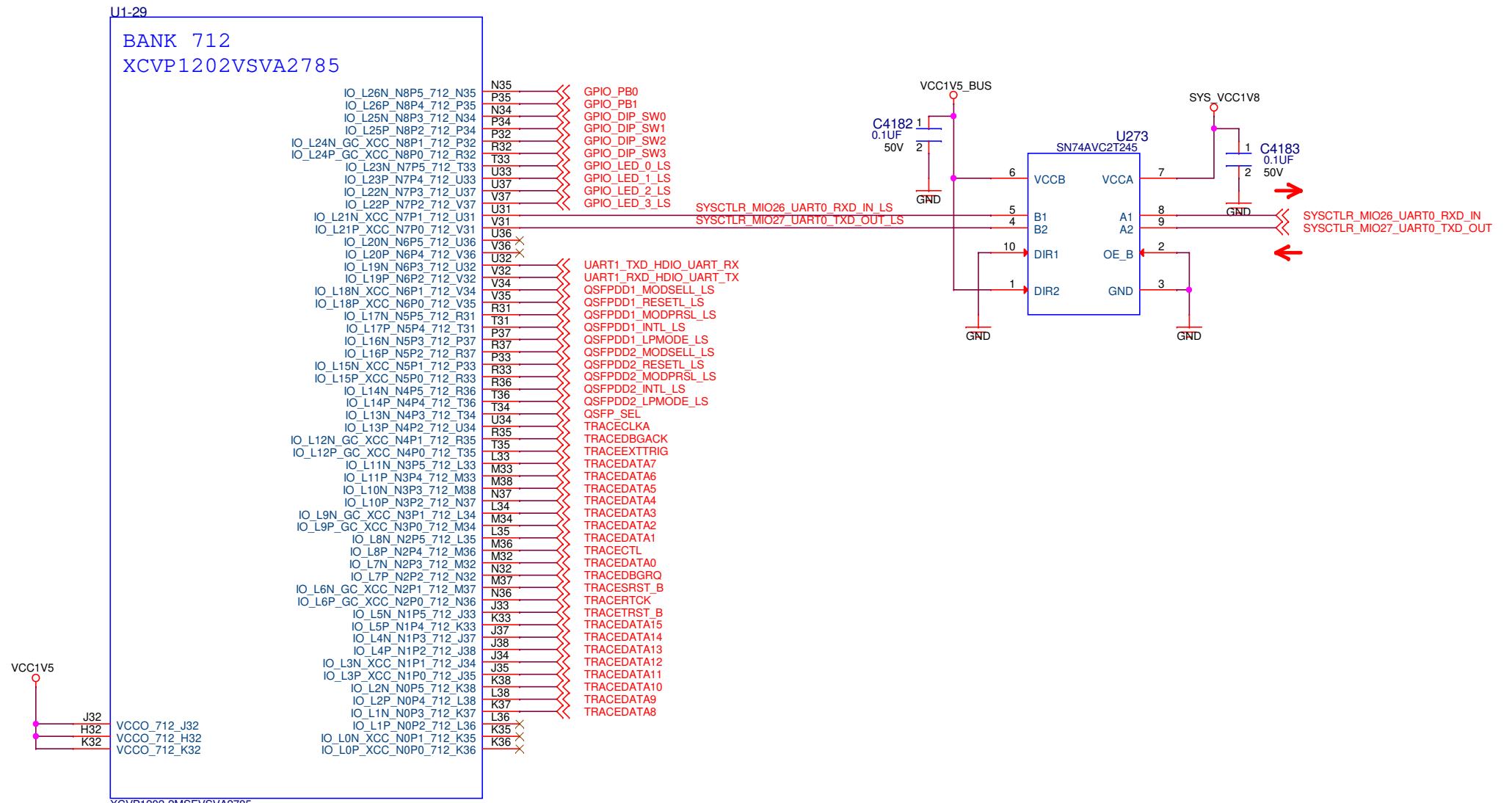
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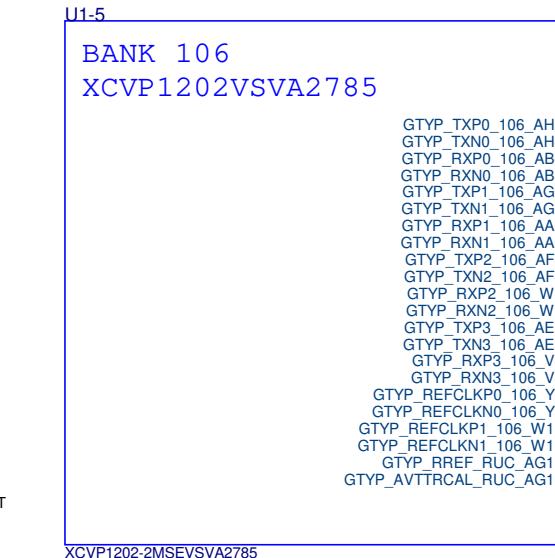
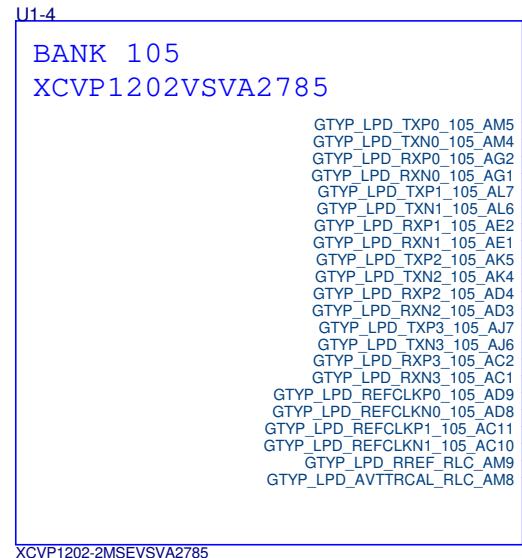


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Title: **VPK120**
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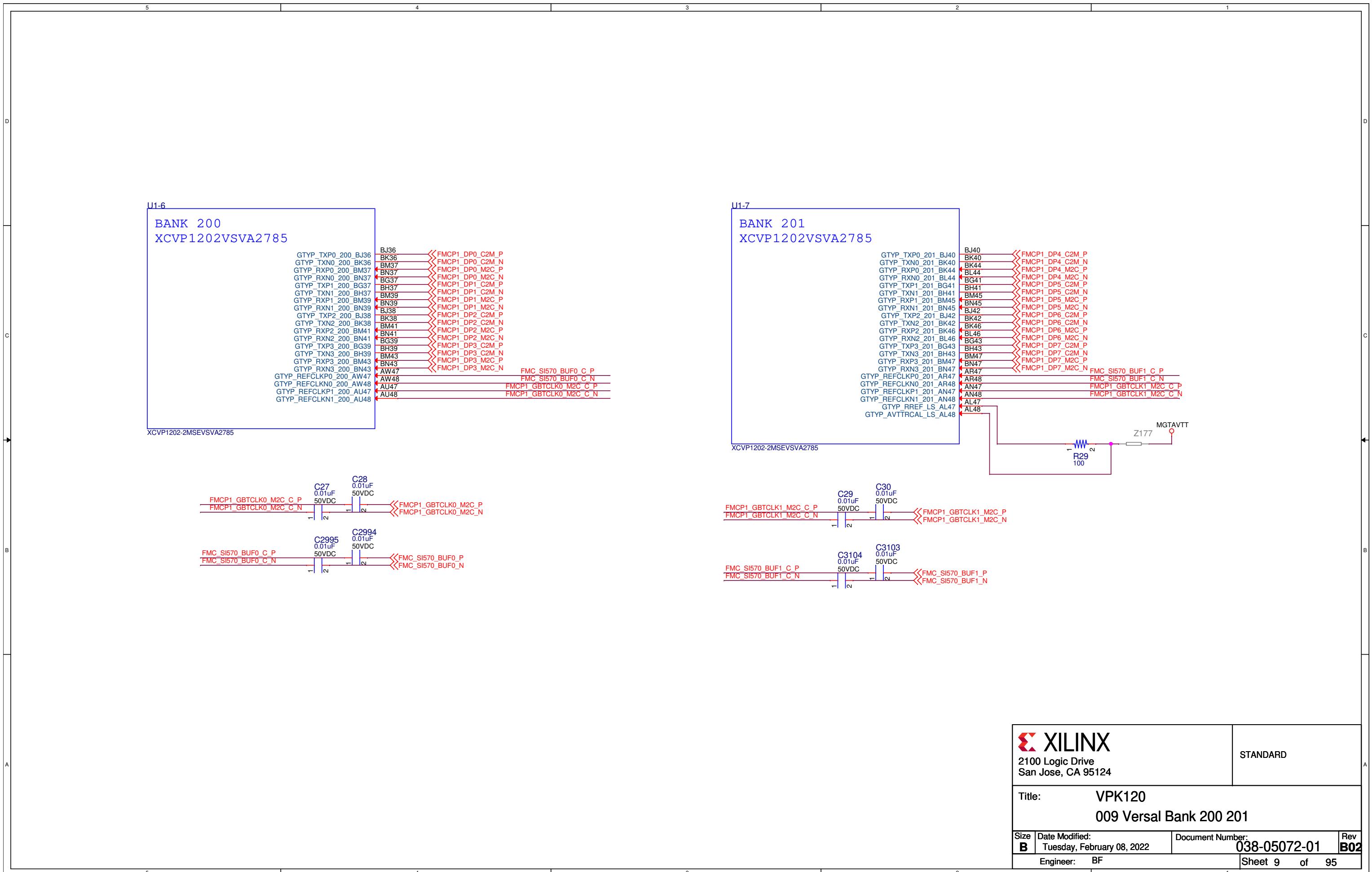
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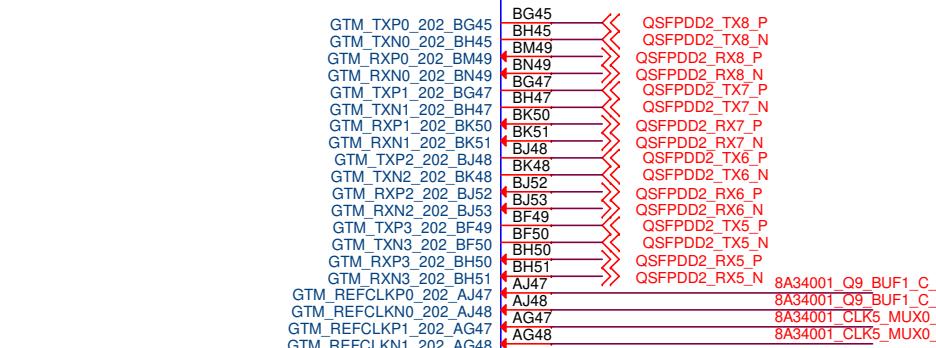
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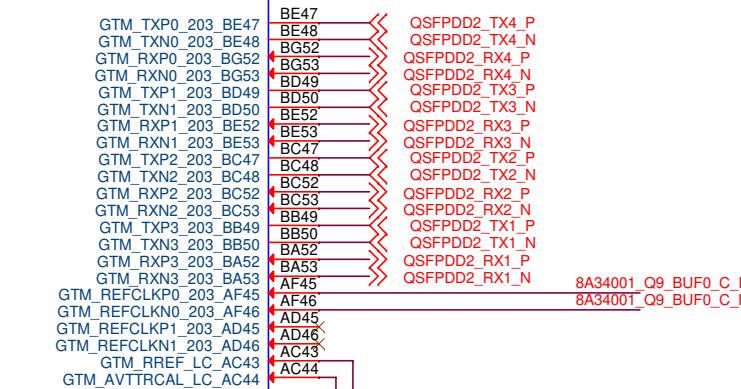
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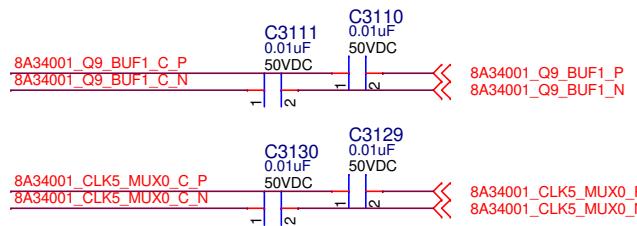
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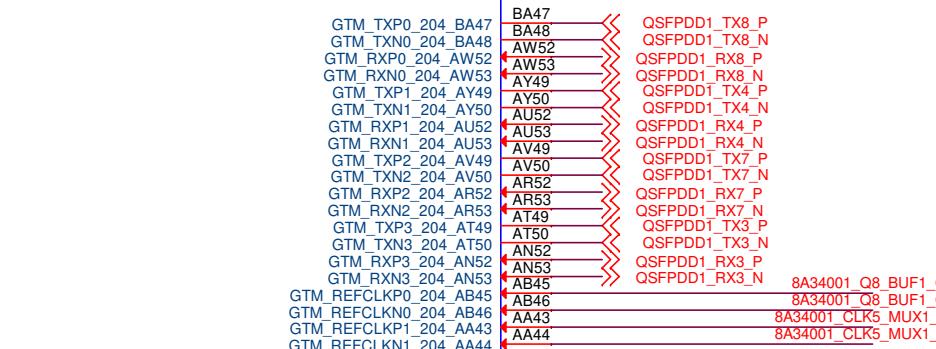


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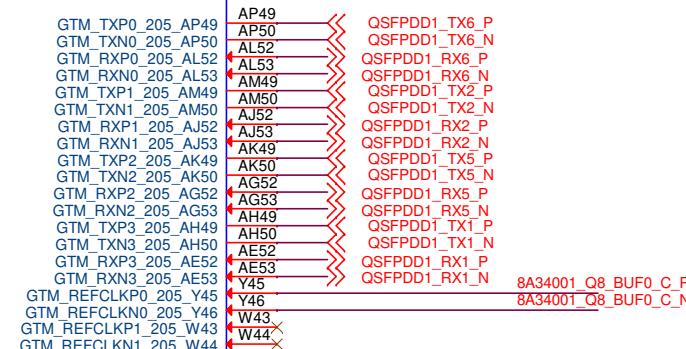
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XCVP1202VSVA2785



XCVP1202-2MSEVSA2785

U1-11

BANK 205
XCVP1202VSVA2785



XCVP1202-2MSEVSA2785



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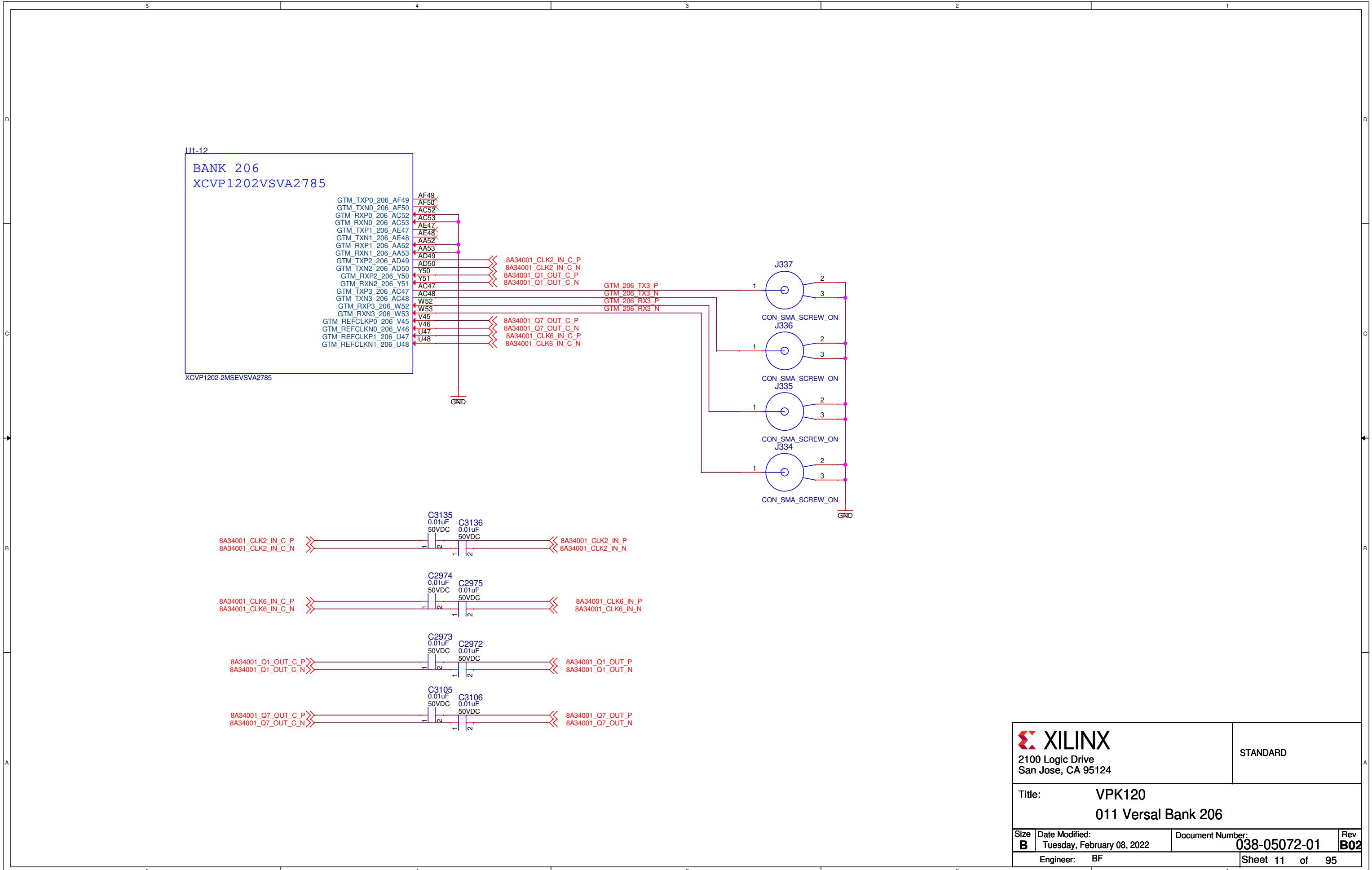
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San Jose, CA 95124

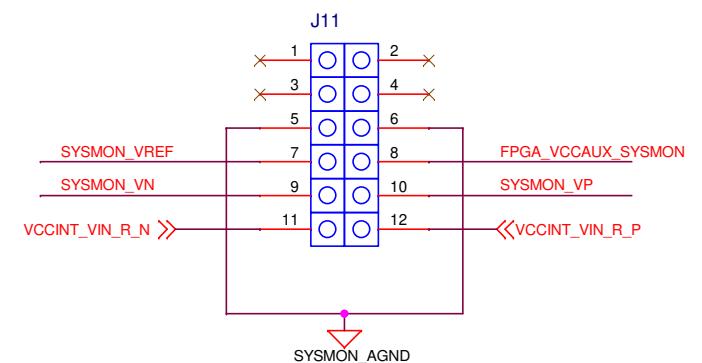
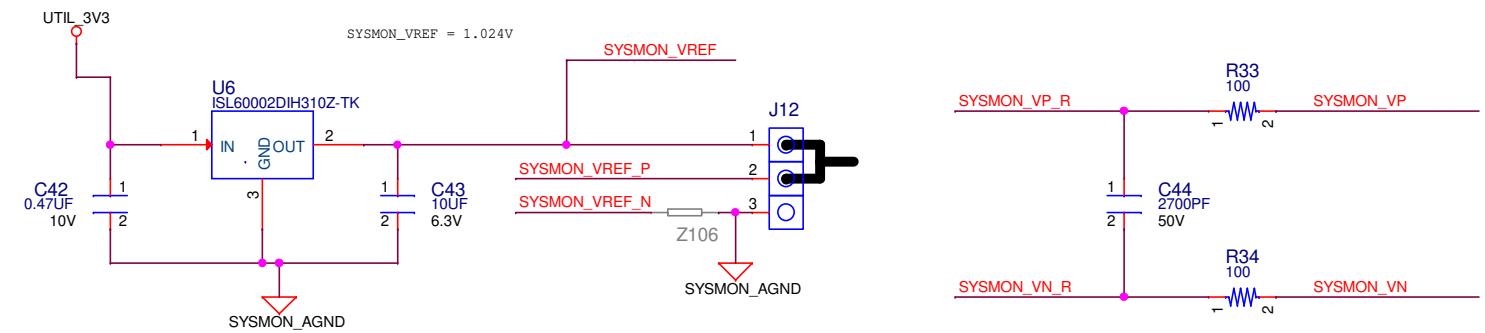
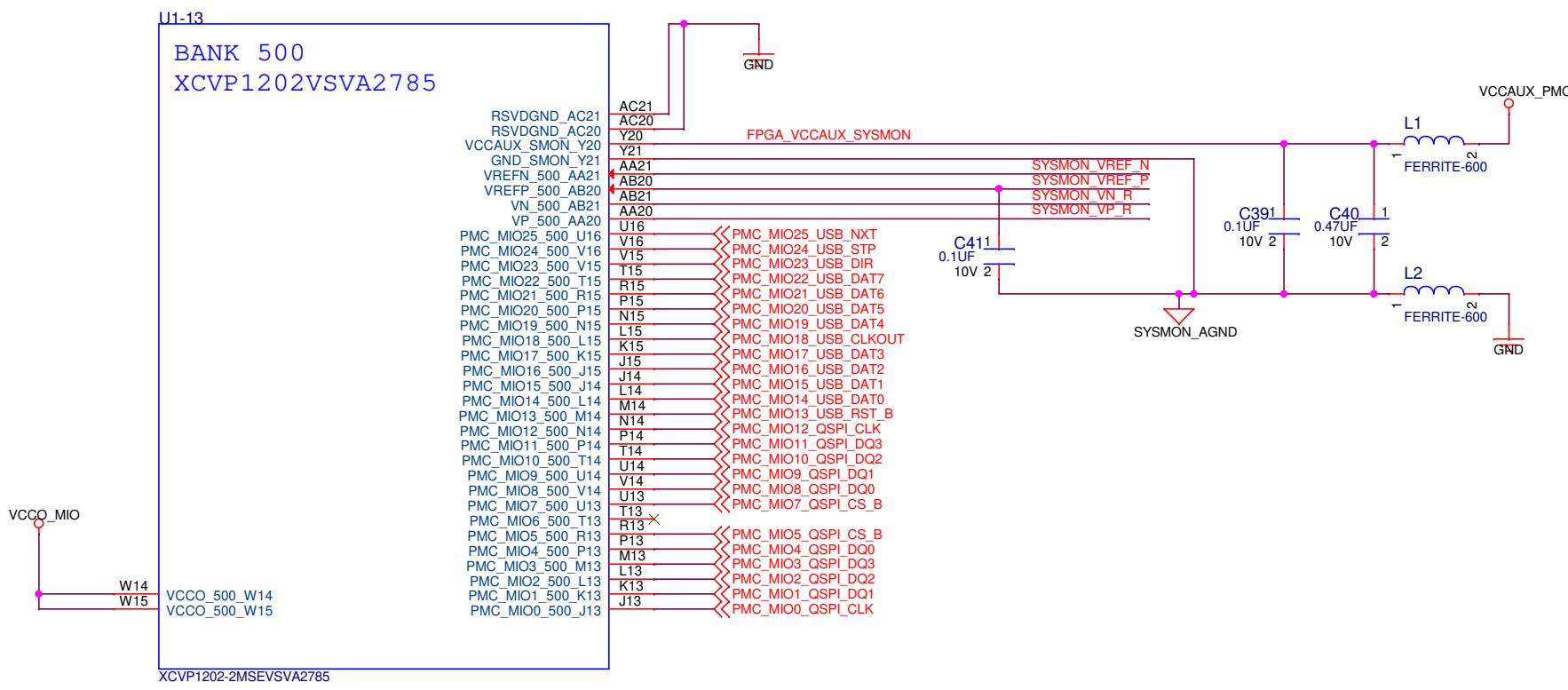
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Title: **VPK120**
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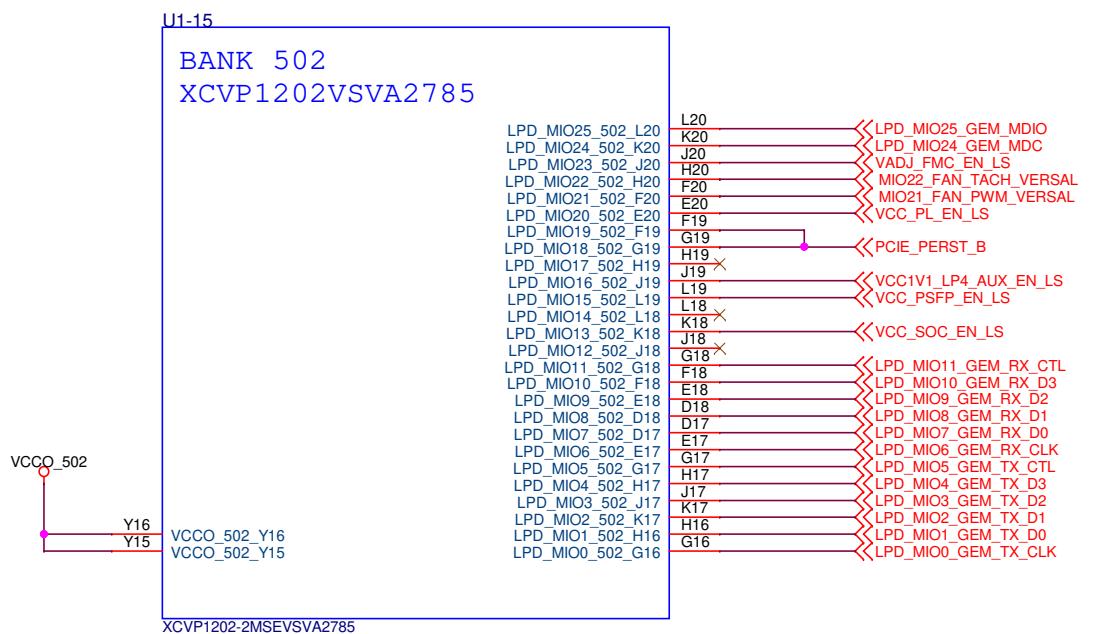
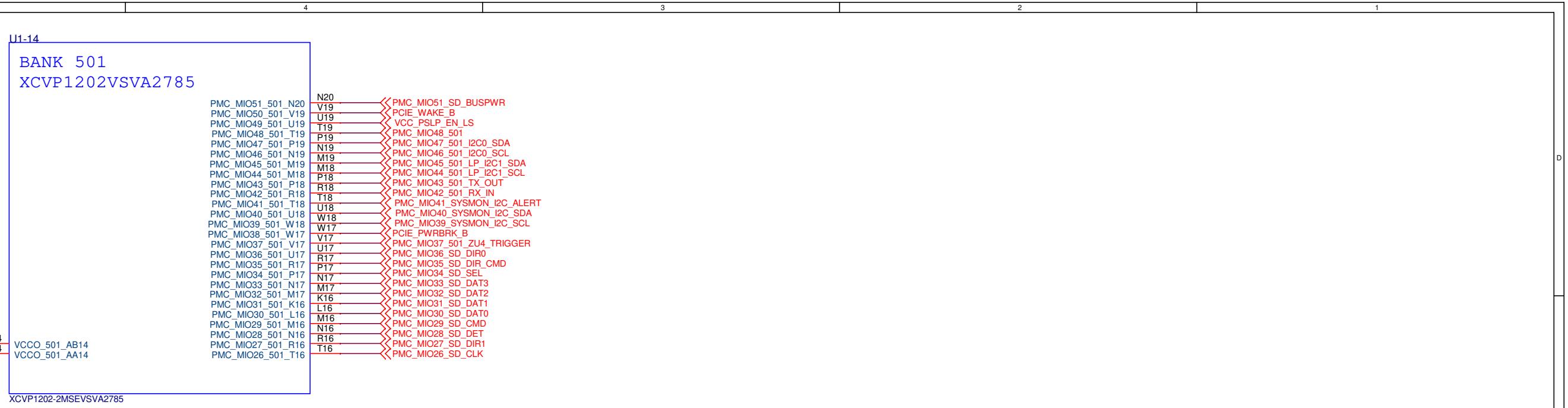
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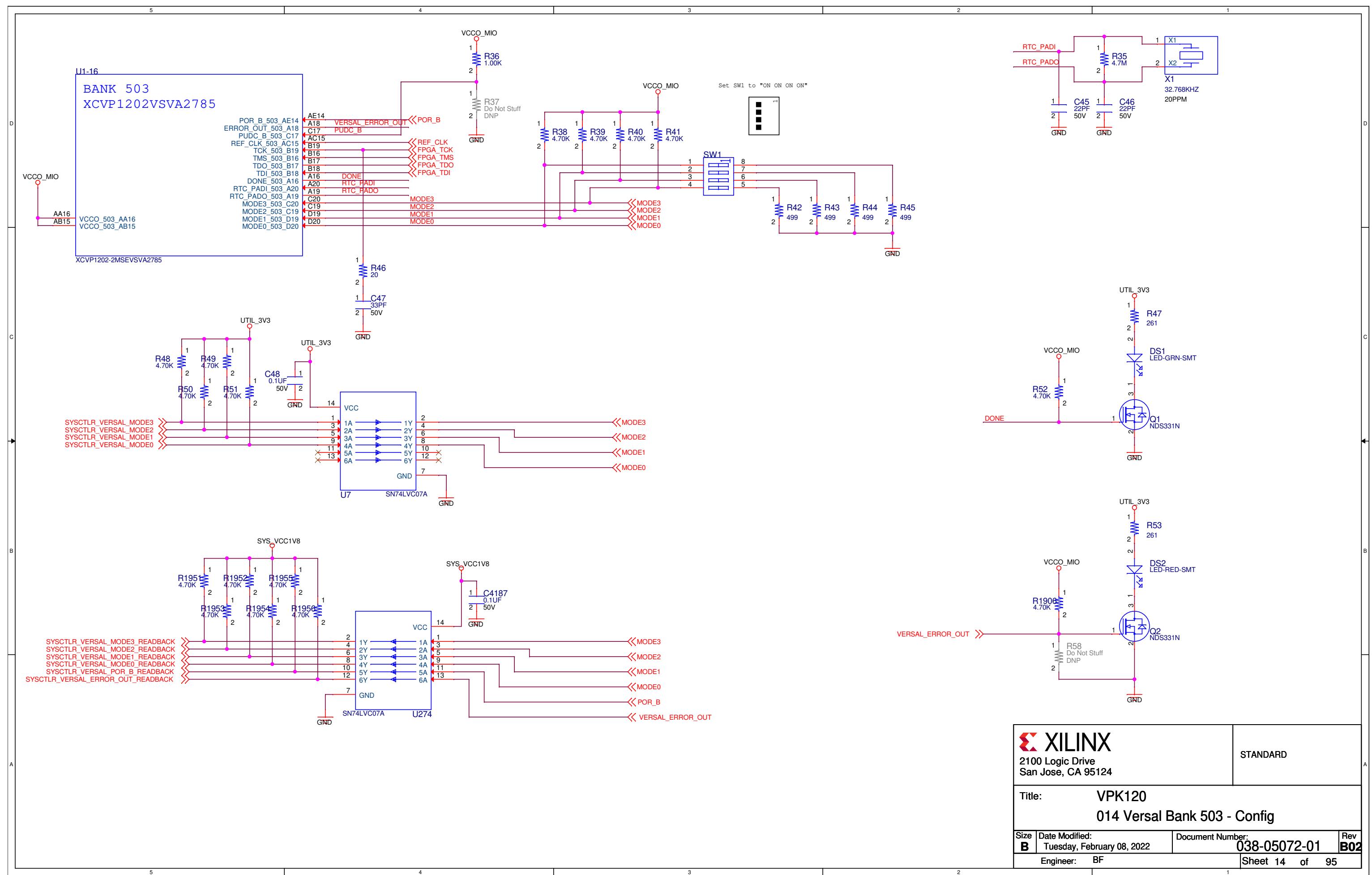
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2100 Logic Drive
San Jose, CA 95124

Title: VPK120
012 Versal Bank 500 MIO-SYSMON

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2100 Logic Drive San Jose, CA 95124		
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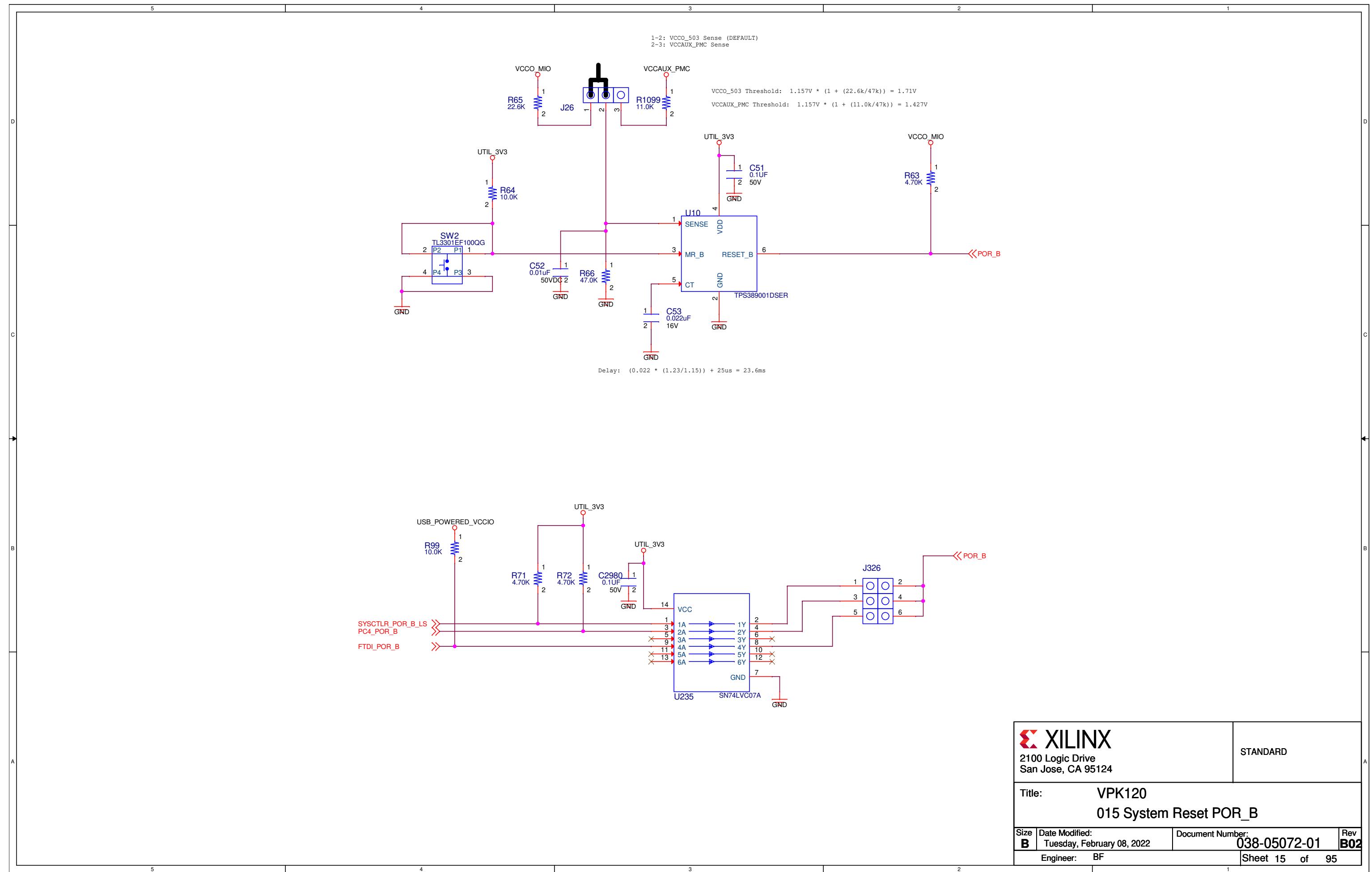
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Title: **VPK120**
014 Versal Bank 503 - Config

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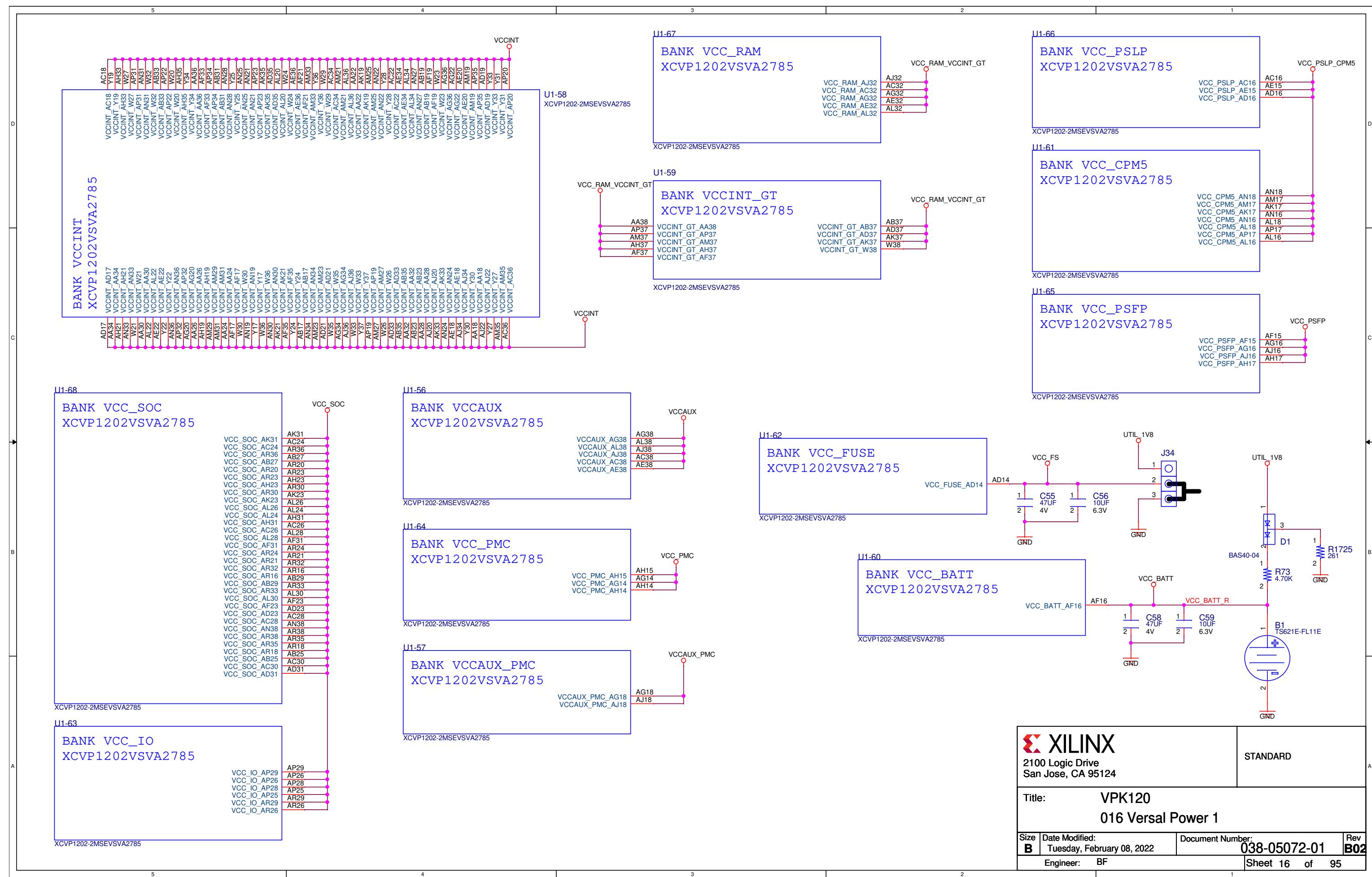


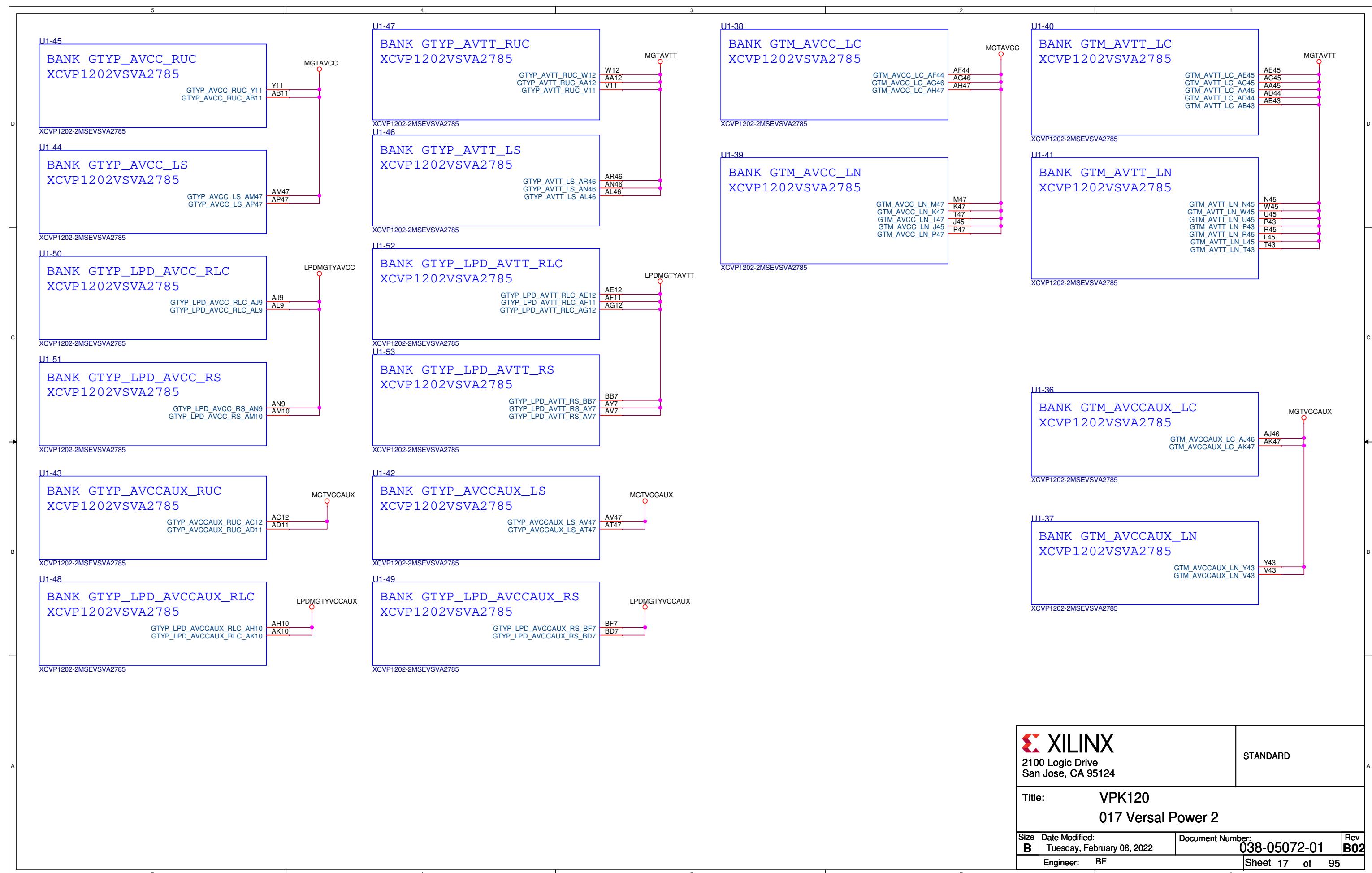
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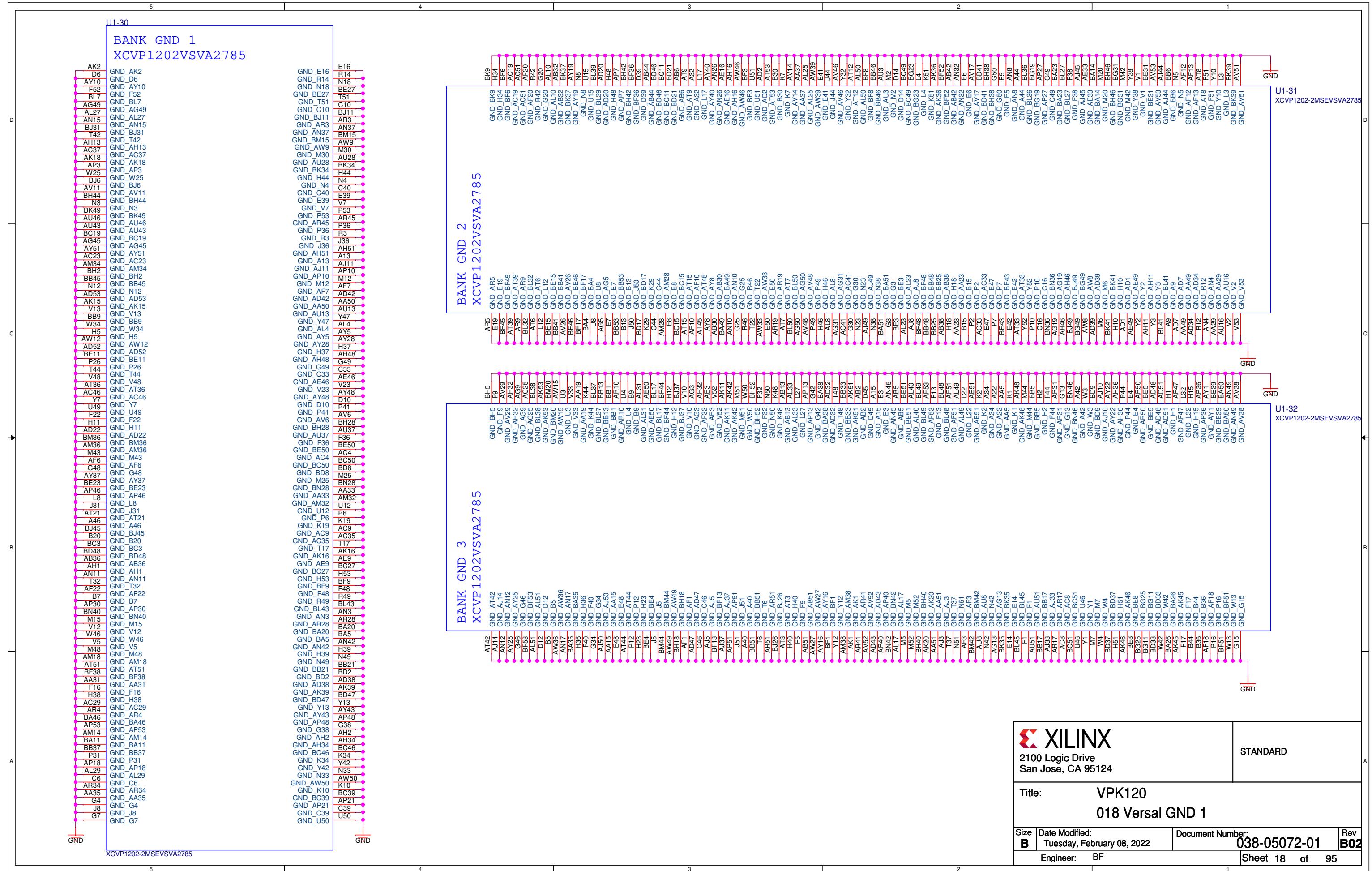
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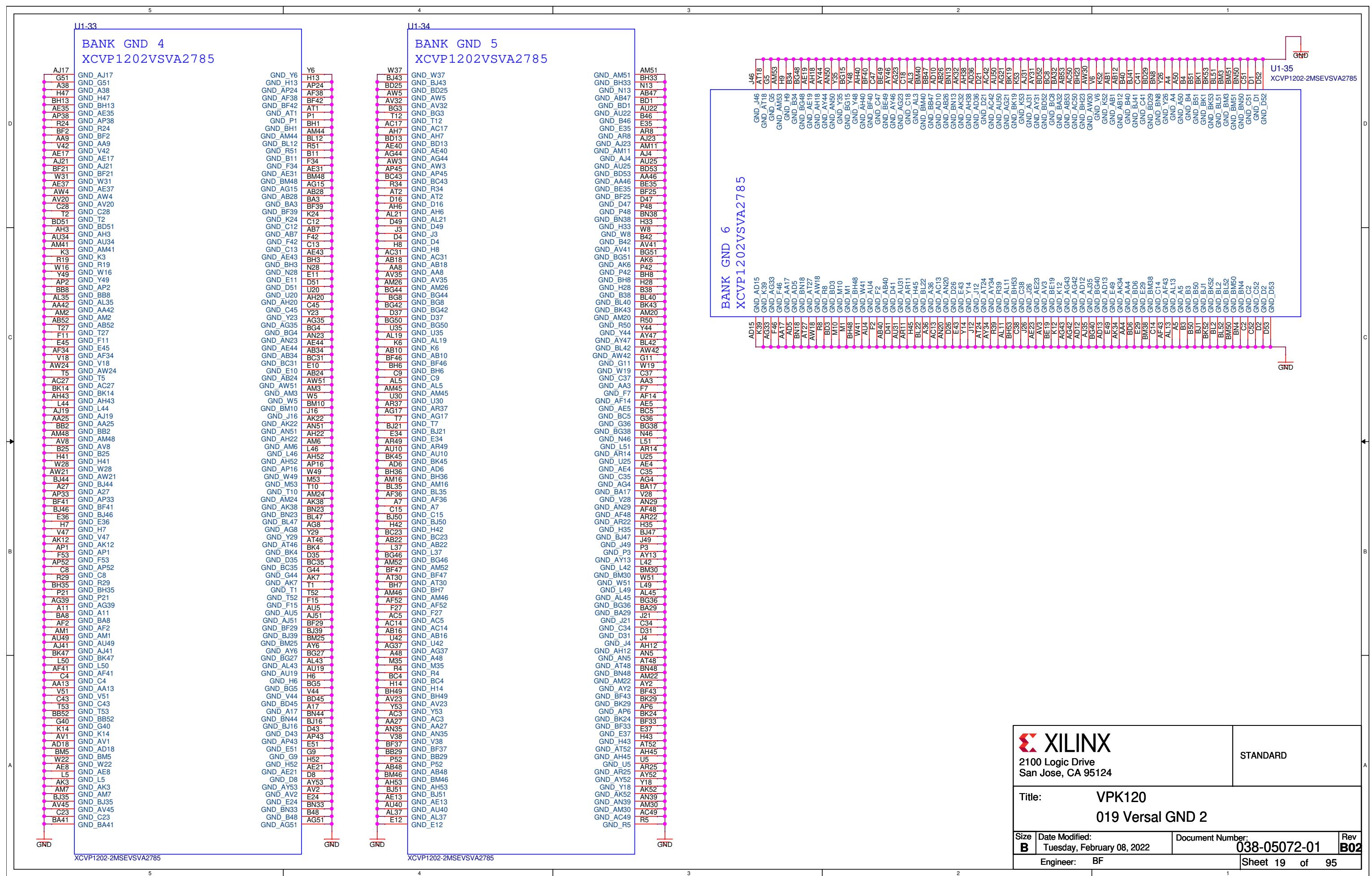
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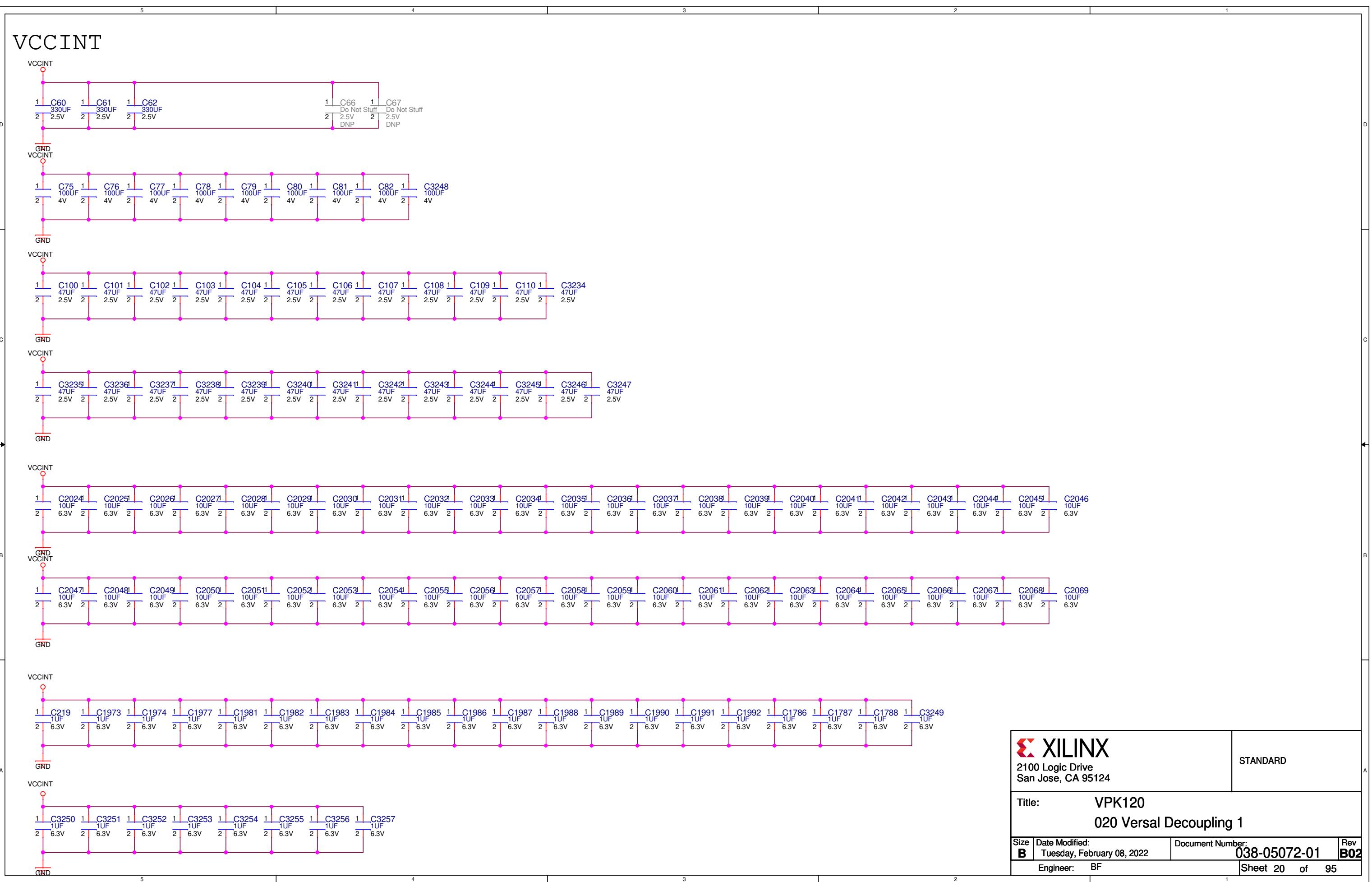


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STANDARD

Title: **VPK120**
019 Versal GND 2

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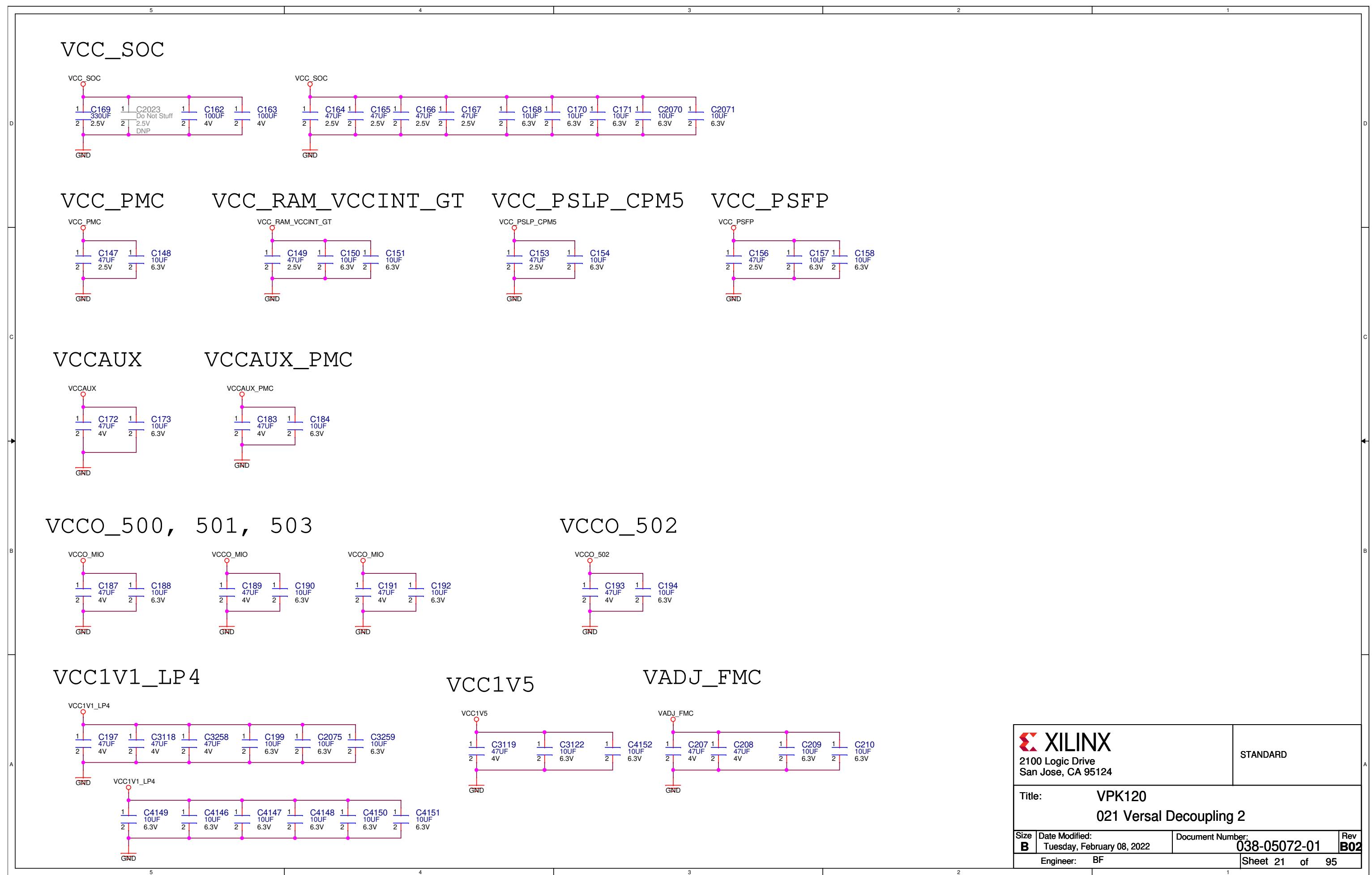
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Size	Date Modified:	Document Number:
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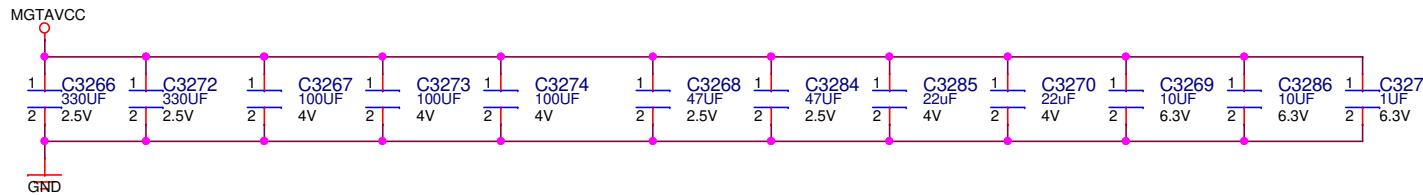
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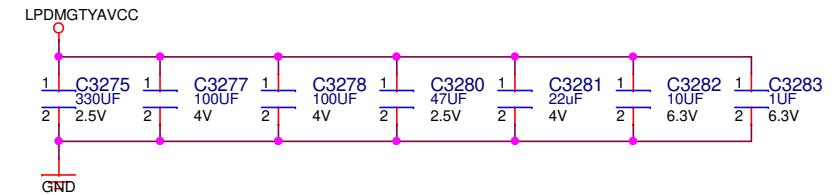


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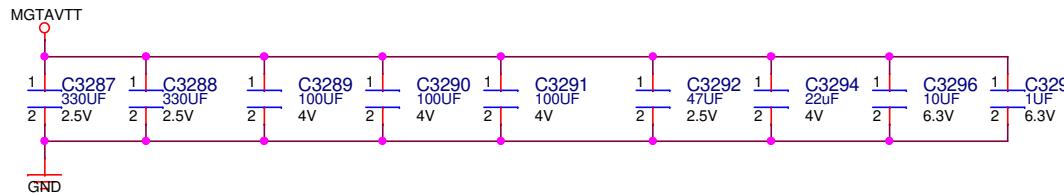
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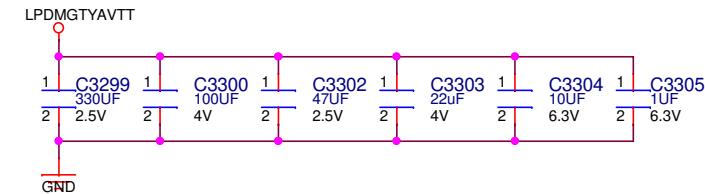
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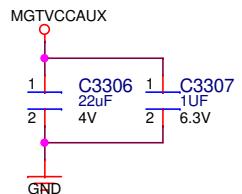
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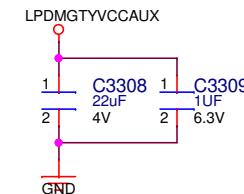
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MGTVCCAUX



LPDMGTYVCCAUX



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STANDARD

Title: VPK120
022 Versal Decoupling 3

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5 4 3 2 1

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BANK NC 1
XCVP1202VSVA2785

B43
W6
D5
G43
AB41
N39
B47
K45
M50
J42
AB9
U41
U7
F14
G41
M41
A45
B10
R6
F47
L9
G14
A41
C42
R40
C48
B39
L41
N52
V41
J9
AB8
N9
B49
R48
D36
D40
V49
D44
N48
W9
D46
F3
R41
V9
D48
P9
E44
D38
N43
K4
R2
F8
G8
J41
T4
E52
J2
U53
P4
U2
W48
AC7
AA11
J47
F10
J6
M4
G12
U1
B37
A39
E38
E40
F45
T11
A6
N2
Y4
Y5
K40
U43
V40
T3
P11
M3
T9
D9
F12
M49
B41
N53
Y40
K8
AB49
E15
P40
N44
K42
L39
NC_B43
NC_W6
NC_D5
NC_G43
NC_AB41
NC_N39
NC_B47
NC_K45
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NC_J42
NC_AB9
NC_U41
NC_U7
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NC_L9
NC_G14
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NC_J9
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NC_B49
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NC_D36
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NC_D44
NC_W9
NC_D46
NC_F3
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NC_Y40
NC_K8
NC_AB49
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NC_P40
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NC_K42
NC_L39

XCVP1202-2MSEVSA2785

U1-55

BANK NC 2
XCVP1202VSVA2785

AA7
A10
N10
F35
L52
AA10
N47
N47
A43
C50
L6
NC_L6
W39
NC_W39
G1
A12
NC_A12
AB39
NC_AB39
M45
B12
NC_B12
R52
N7
NC_N7
B8
NC_B8
N41
NC_N41
J7
NC_J7
B35
NC_B35
L48
NC_L48
M9
NC_M9
A37
NC_A37
V39
NC_V39
U11
NC_U11
U38
NC_U38
K5
NC_K5
L40
NC_L40
R1
NC_R1
E2
NC_E2
C36
NC_C36
J11
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H3
NC_H3
F49
NC_F49
F39
NC_F39
G52
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AA39
NC_AA39
AA48
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P39
NC_P39
R10
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D15
NC_D15
P8
NC_P8
AC40
L1
NC_L1
R7
NC_R7
N11
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U40
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XCVP1202-2MSEVSA2785

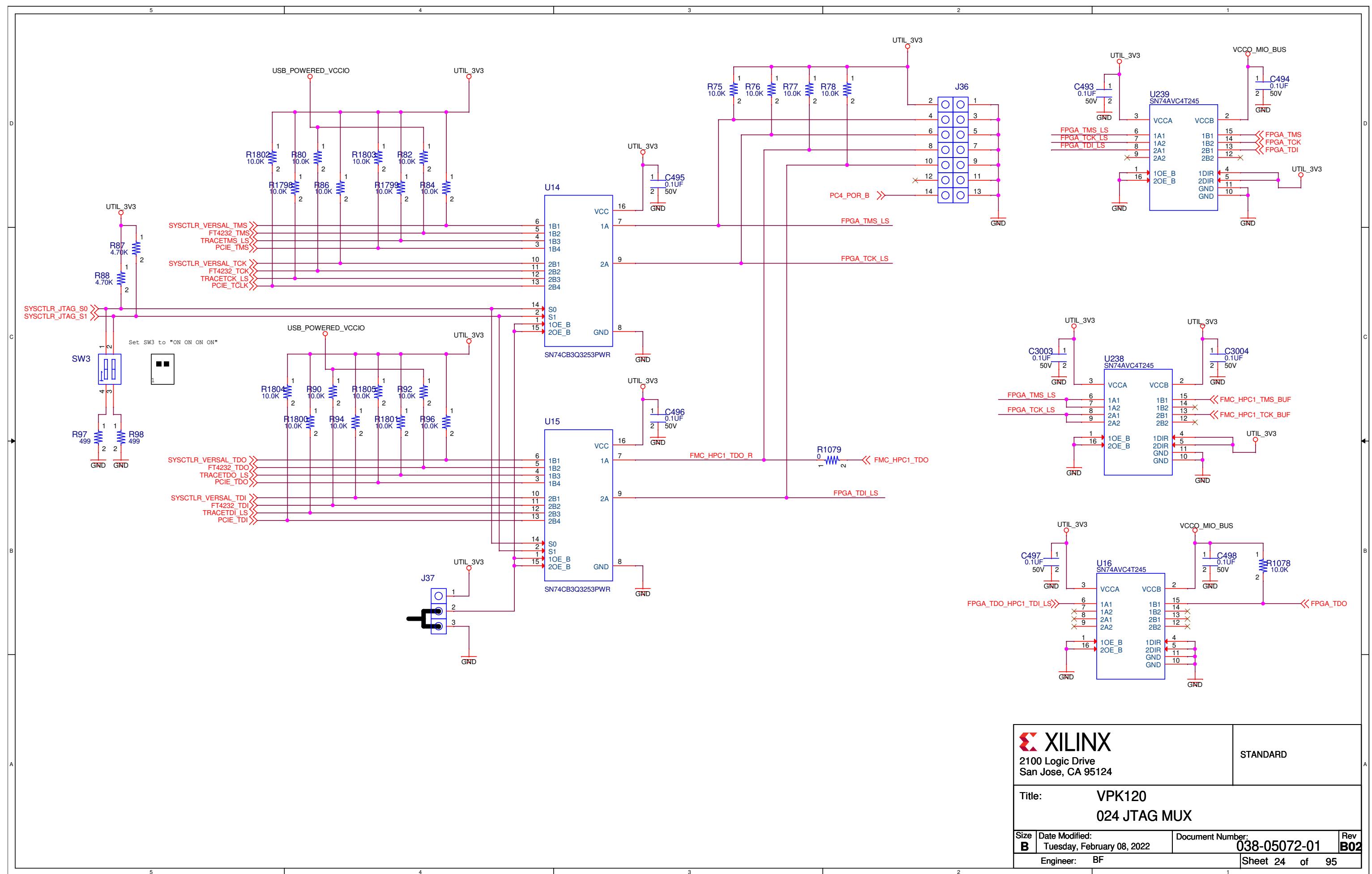


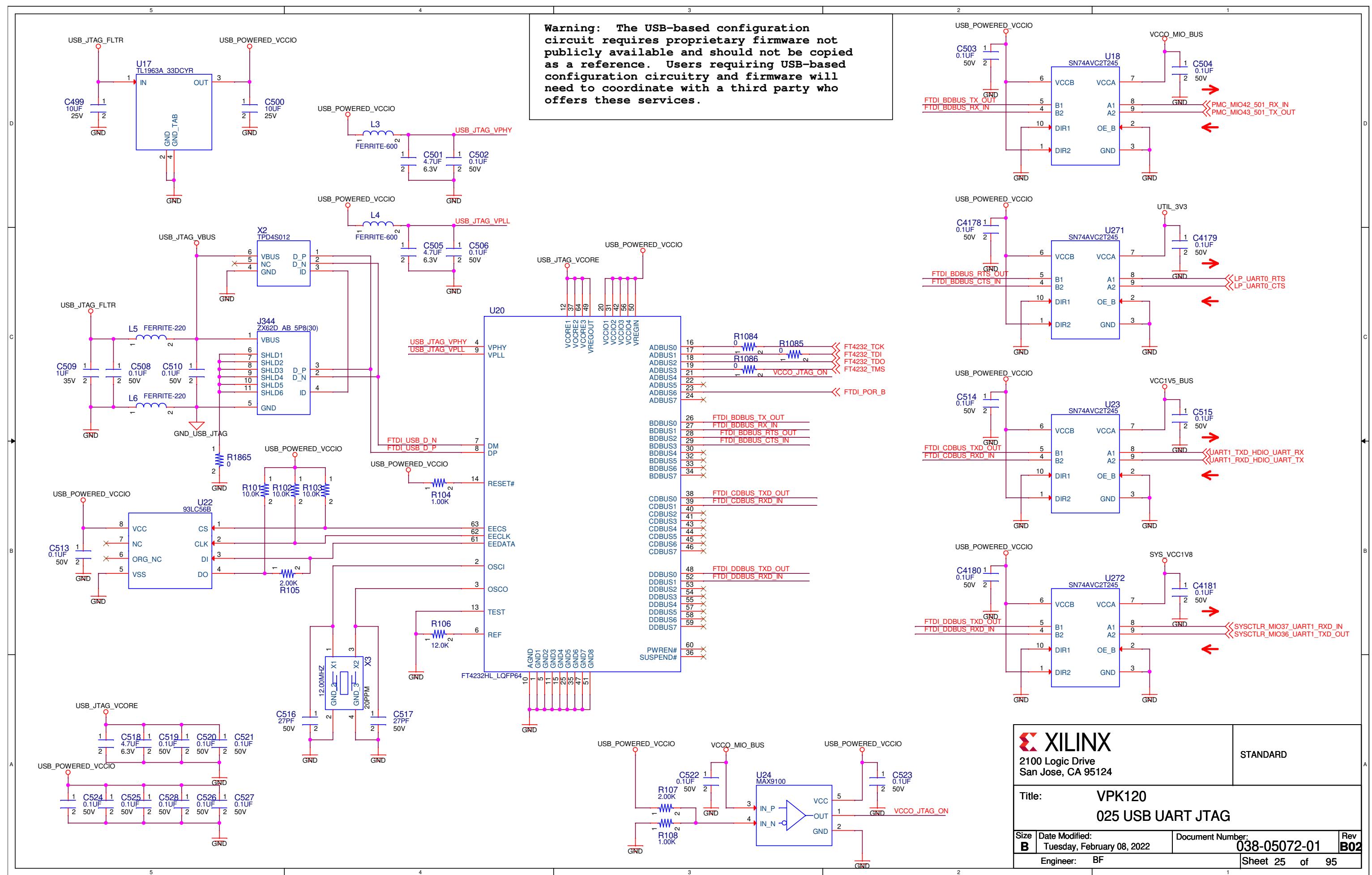
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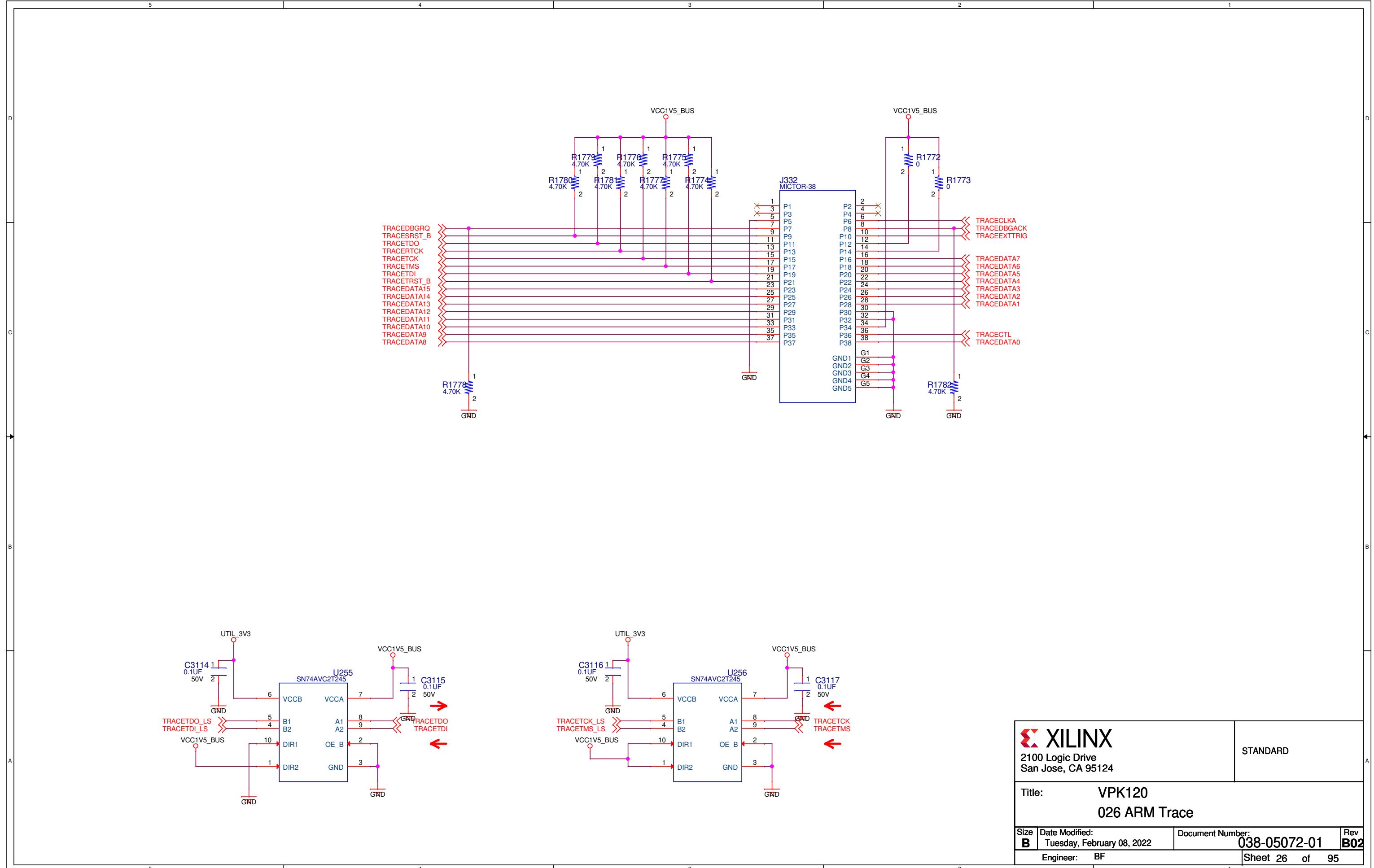
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Title: VPK120
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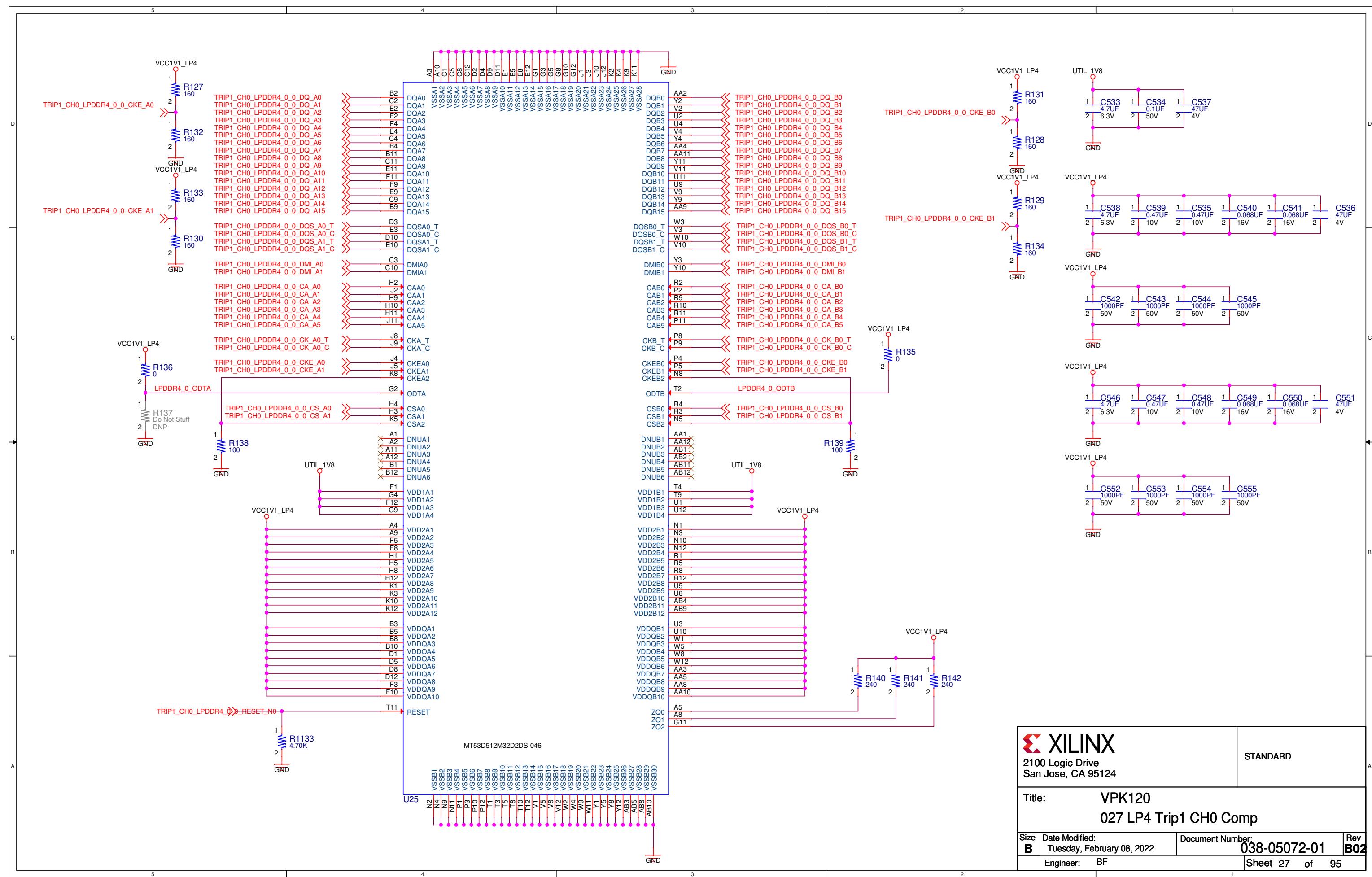
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B	Tuesday, February 08, 2022	038-05072-01	B02
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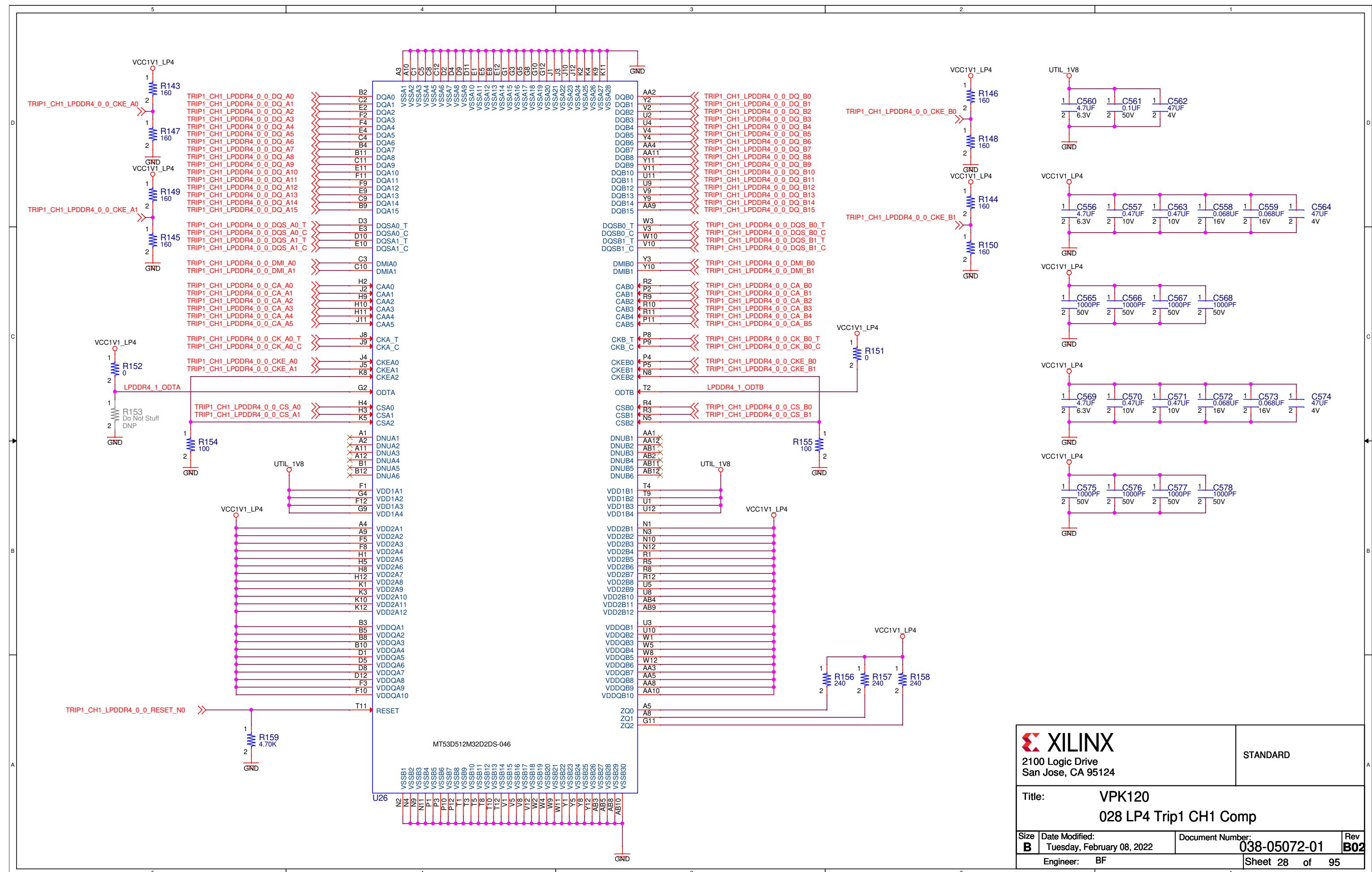
**XILINX**2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: VPK120
026 ARM Trace

Size	Date Modified:	Document Number:	Rev
B	Tuesday, February 08, 2022	038-05072-01	B02
		Engineer: BF	Sheet 26 of 95





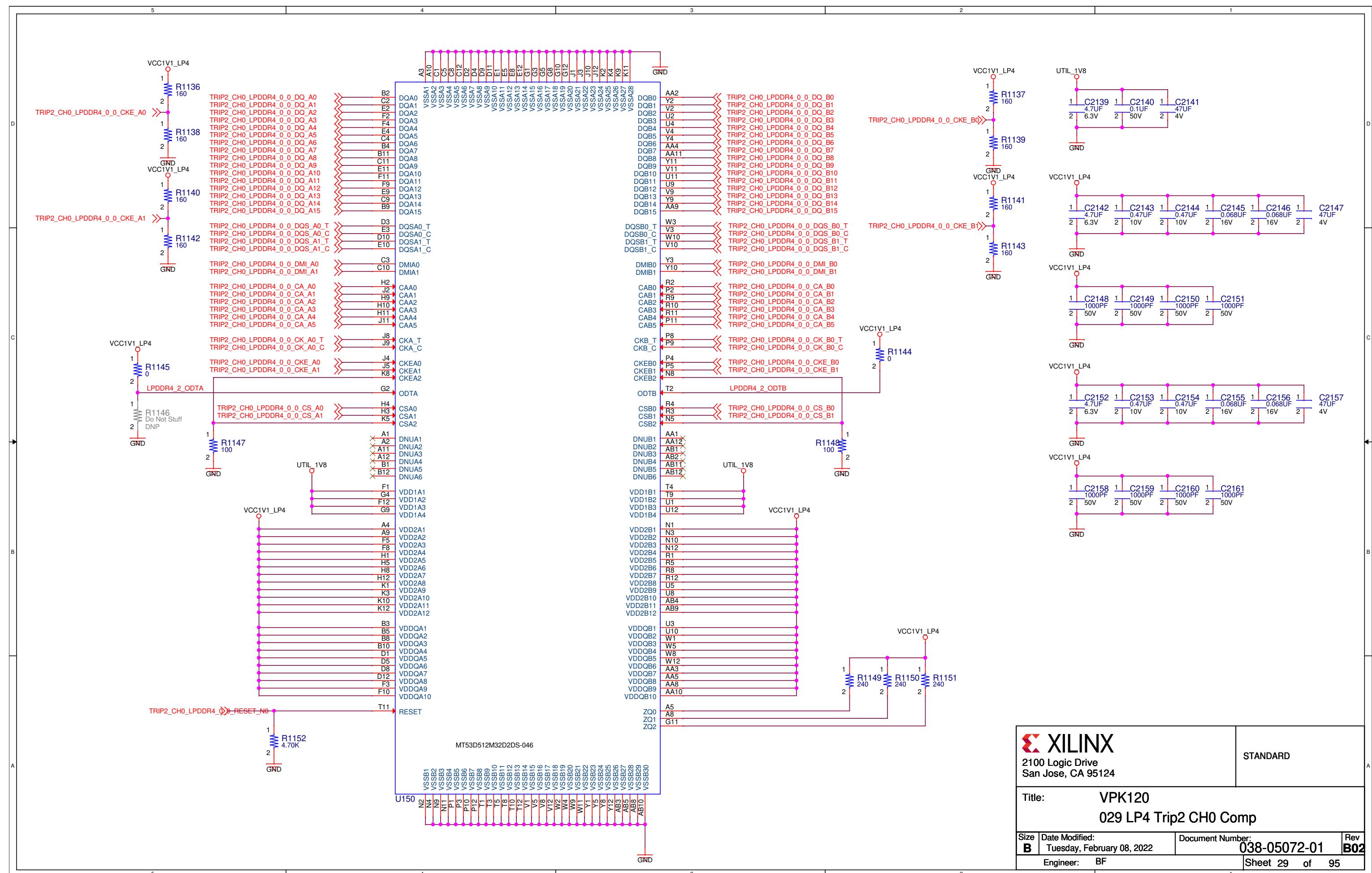
XILINX

2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: **VPK120**
028 LP4 Trip1 CH1 Comp

Size	Date Modified:	Document Number:	Rev
B	Tuesday, February 08, 2022	038-05072-01	B02
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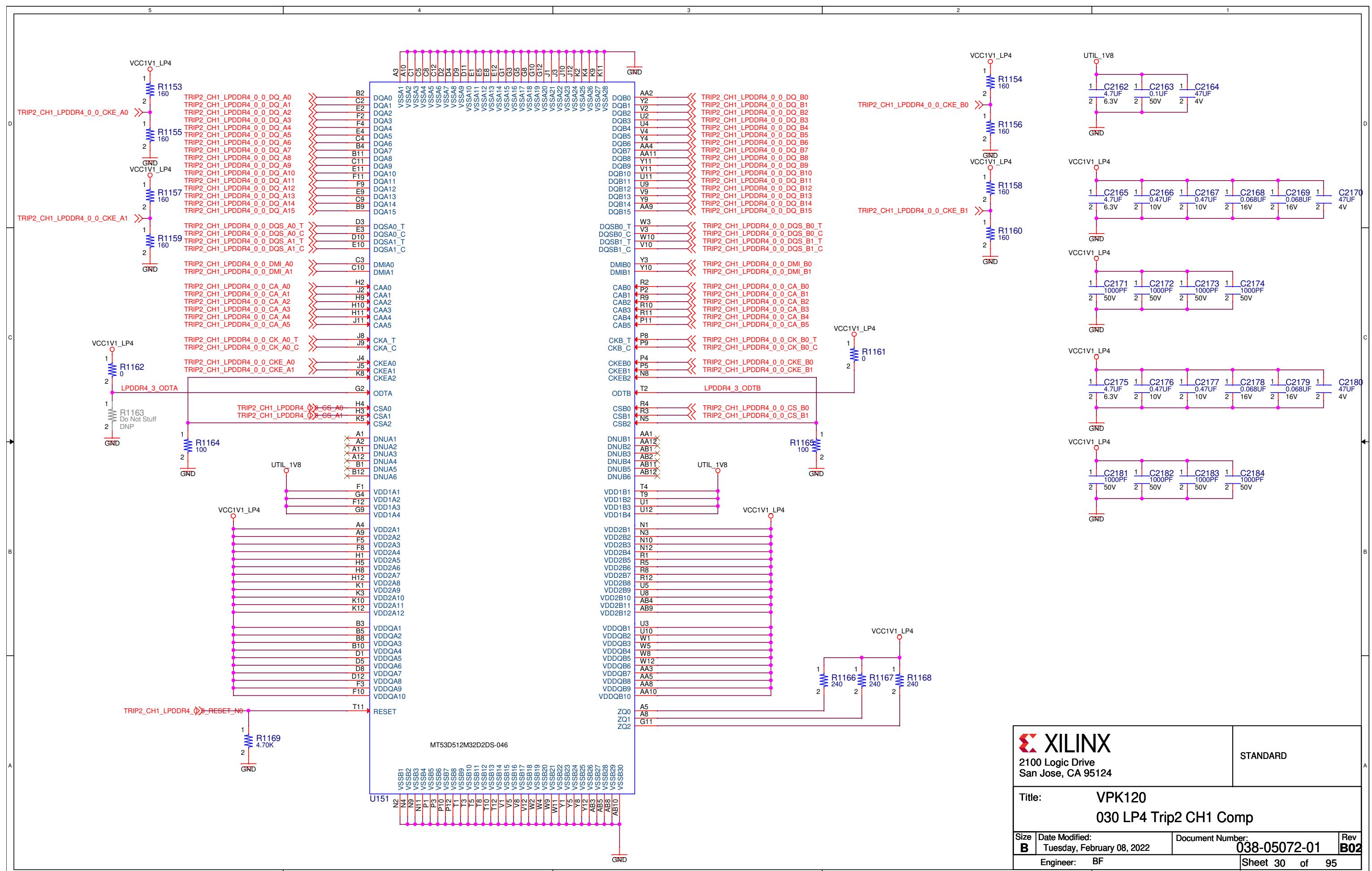
XILINX

2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: **VPK120**
029 LP4 Trip2 CH0 Comp

Size	Date Modified:	Document Number:	Rev
B	Tuesday, February 08, 2022	038-05072-01	B02
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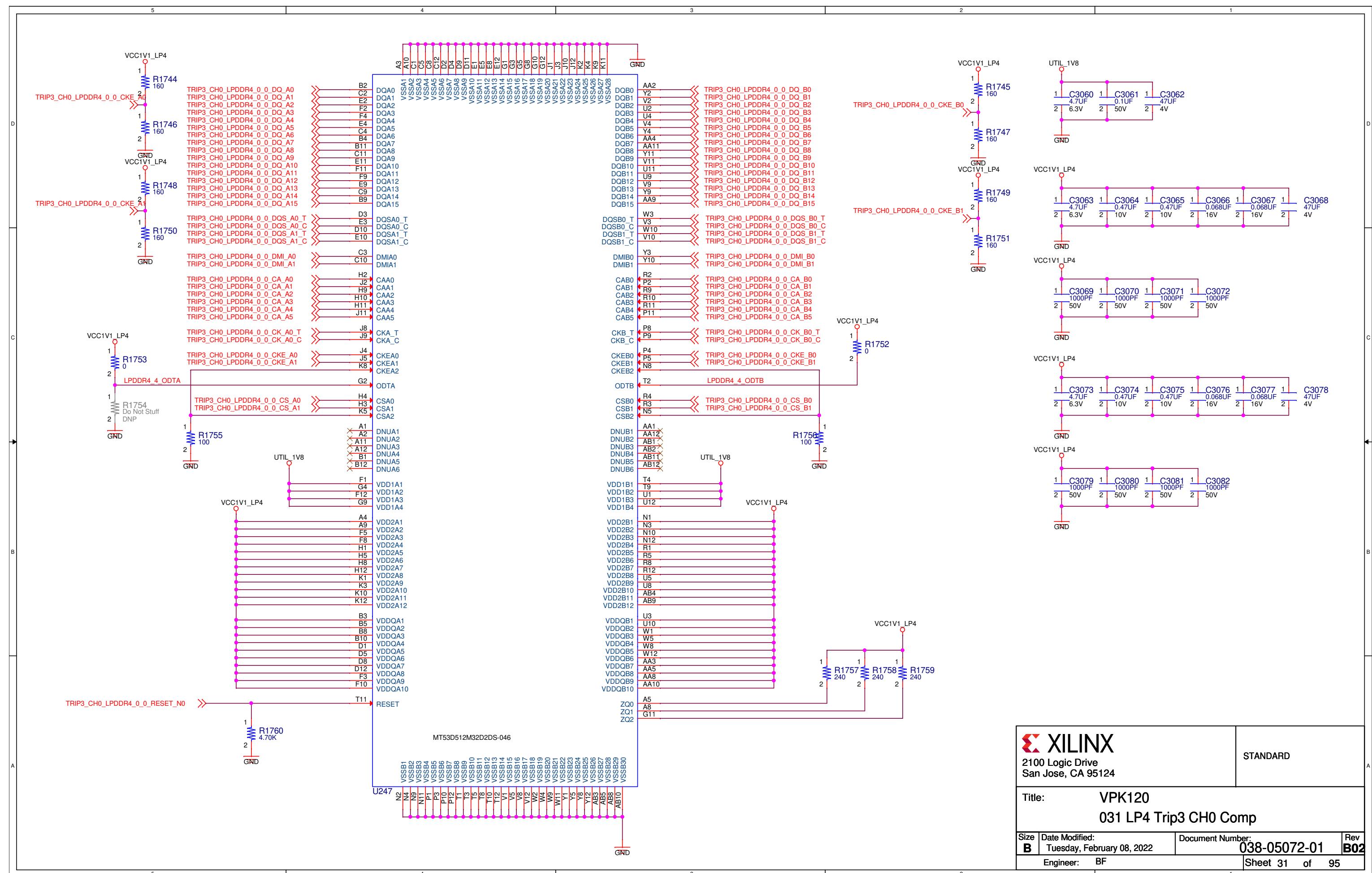
XILINX

2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: **VPK120**
030 LP4 Trip2 CH1 Comp

Size: B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01	Rev: B02
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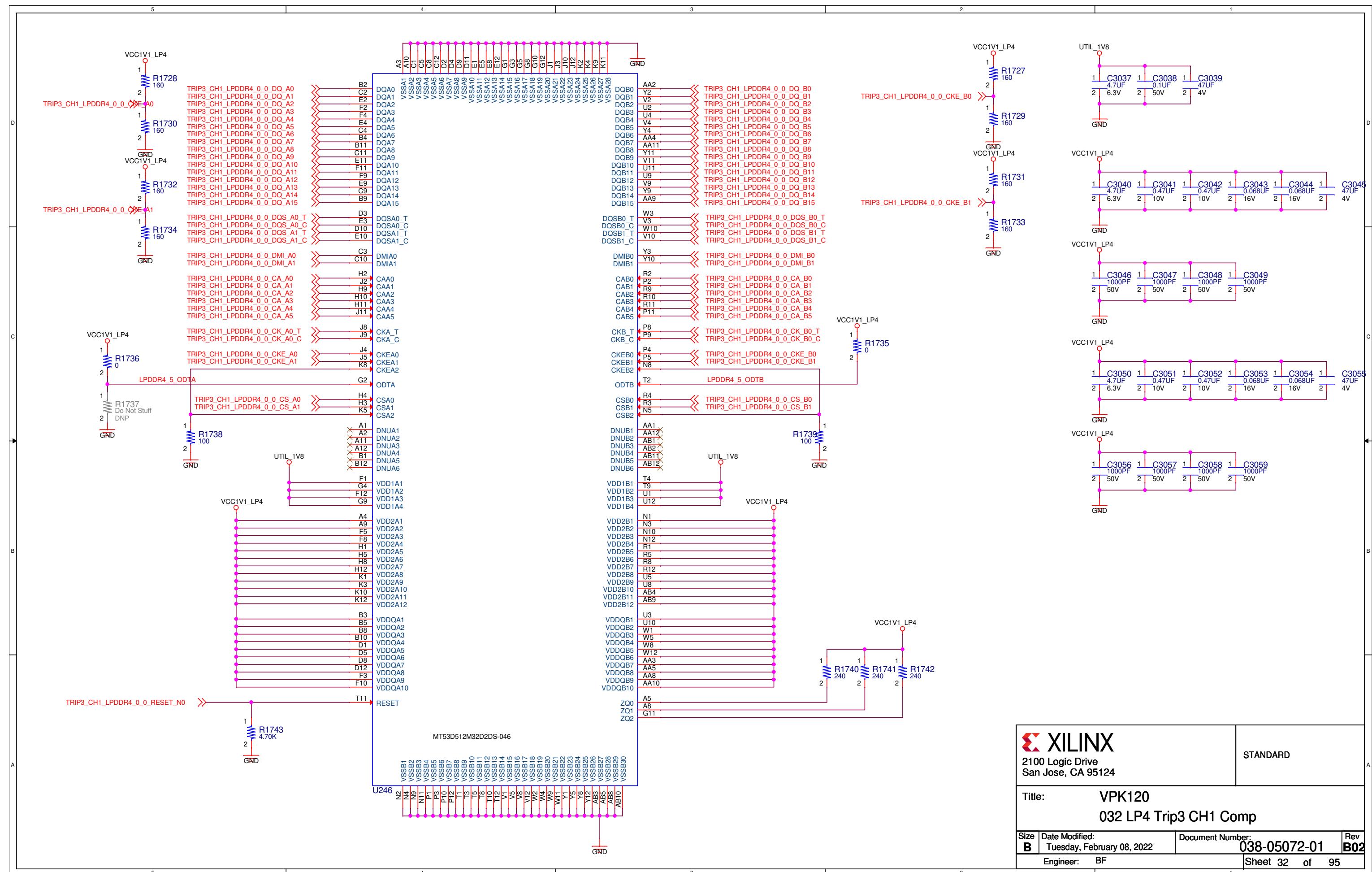
XILINX

2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: **VPK120**
031 LP4 Trip3 CH0 Comp

Size	Date Modified:	Document Number:	Rev
B	Tuesday, February 08, 2022	038-05072-01	B02
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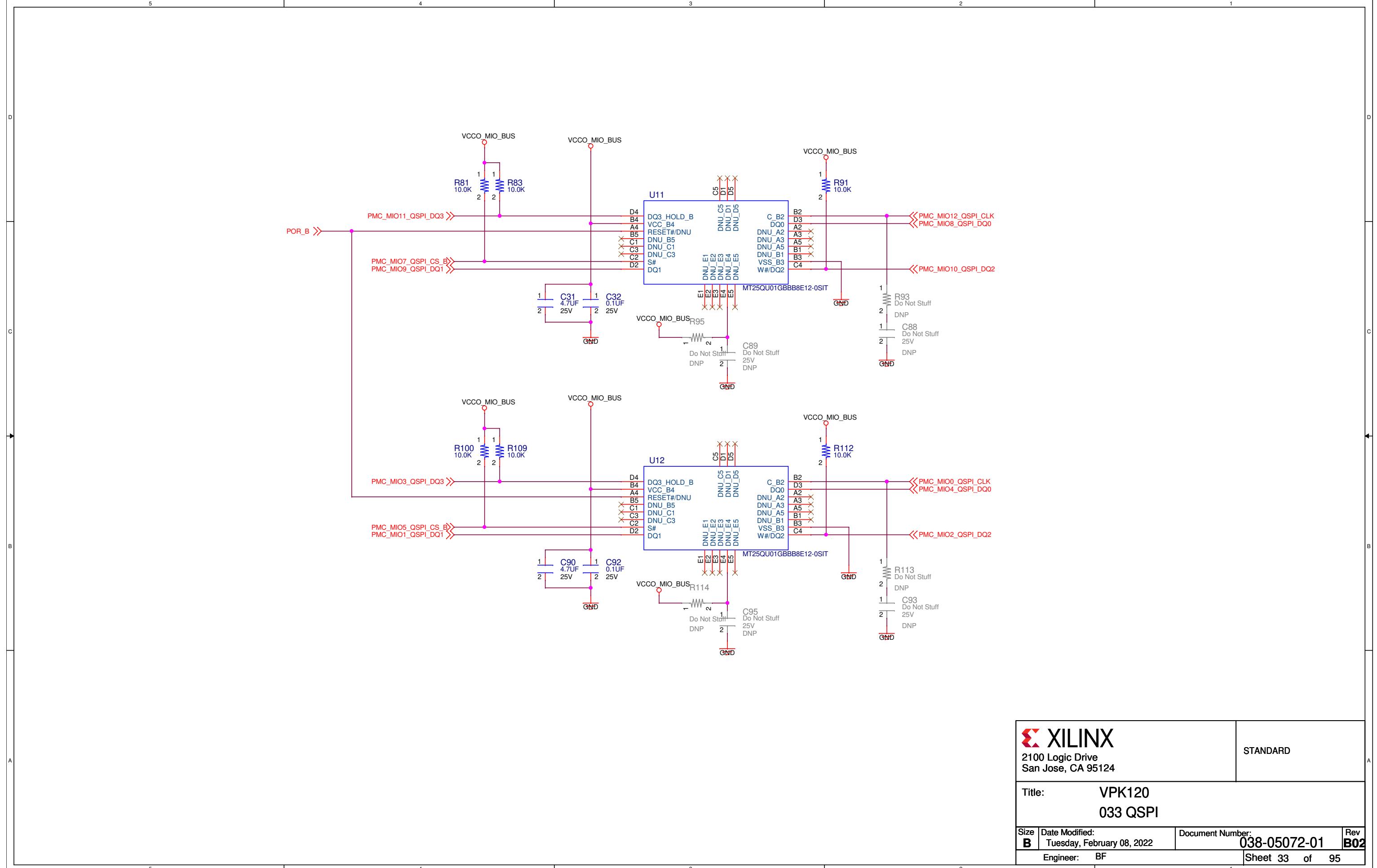
XILINX

2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: **VPK120**
032 LP4 Trip3 CH1 Comp

Size: B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01	Rev: B02
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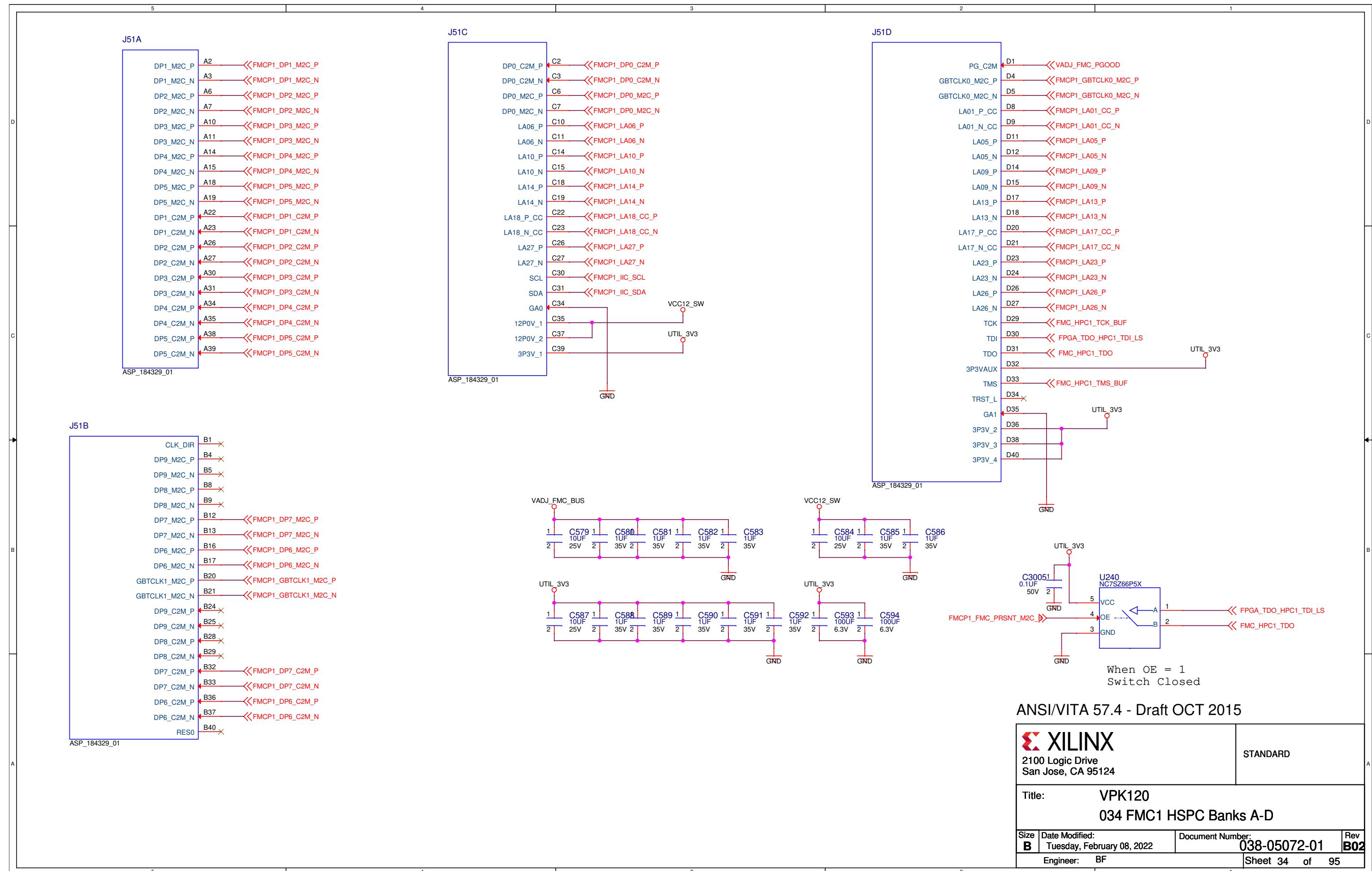


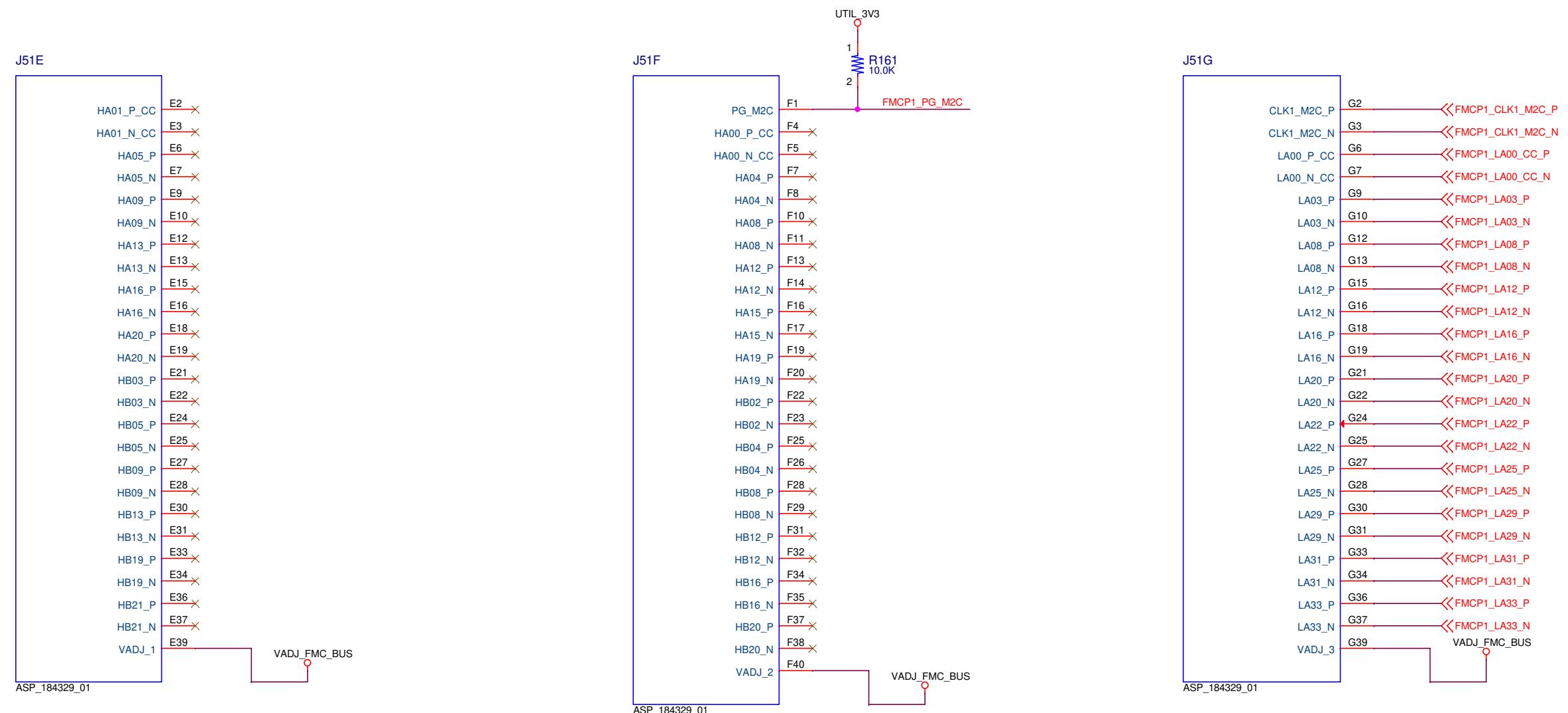
XILINX
2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: VPK120
033 QSPI

Size	Date Modified:	Document Number:	Rev
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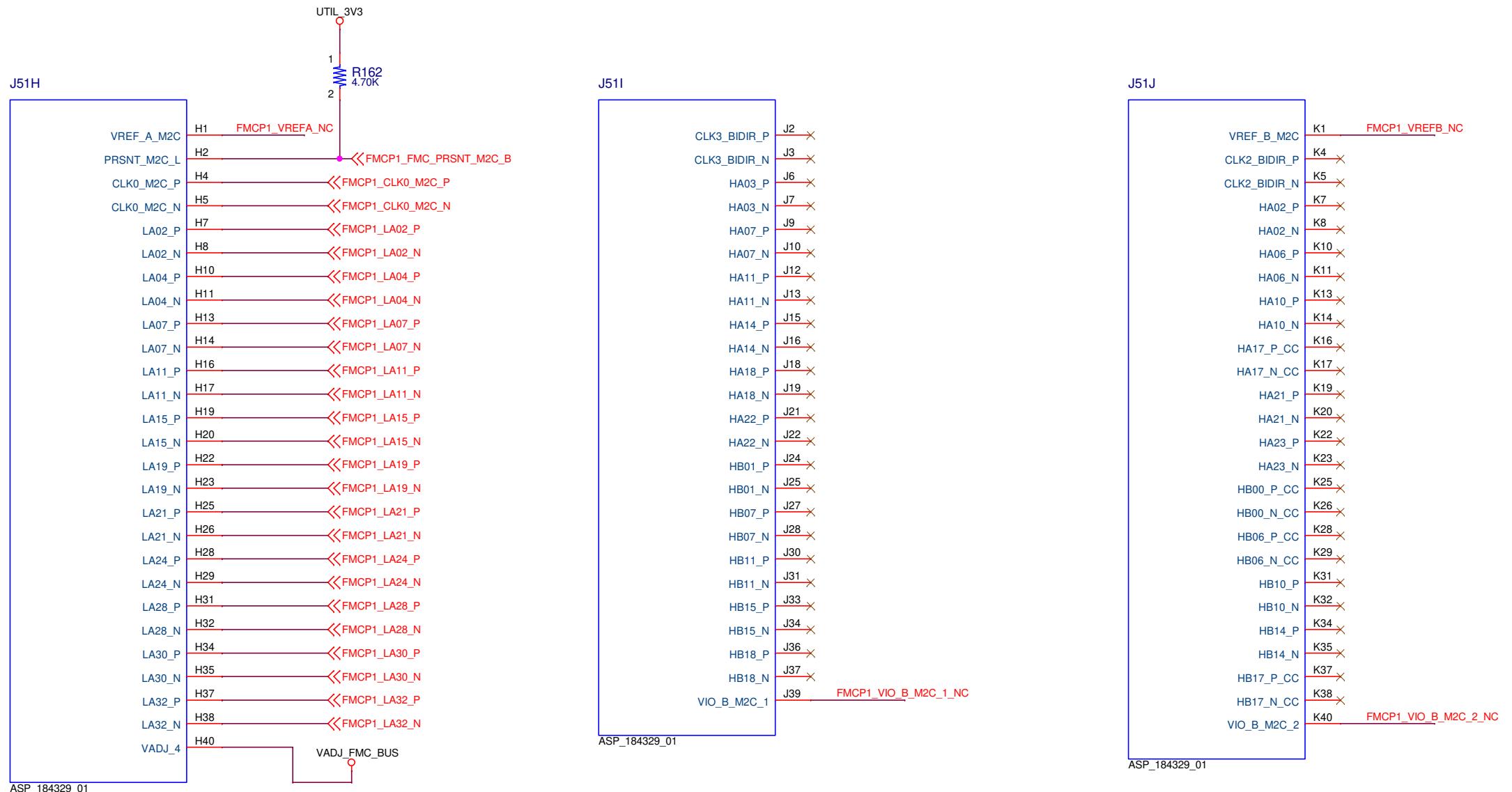


2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: VPK120
035 FMC1 HSPC Banks E-G

Size	Date Modified:	Document Number:	Rev
B	Tuesday, February 08, 2022	038-05072-01	B02
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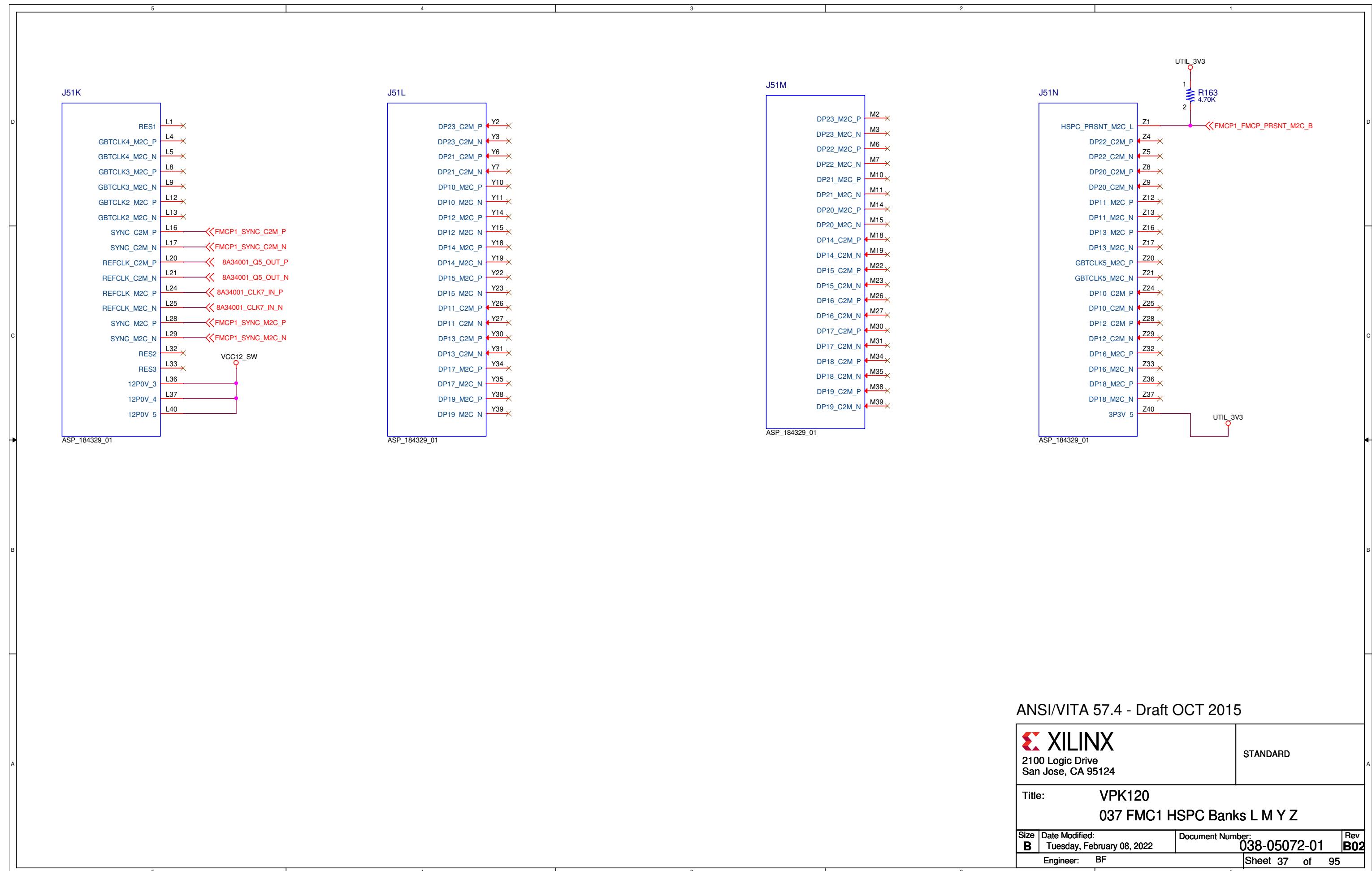


2100 Logic Drive
San Jose, CA 95124

STANDARD

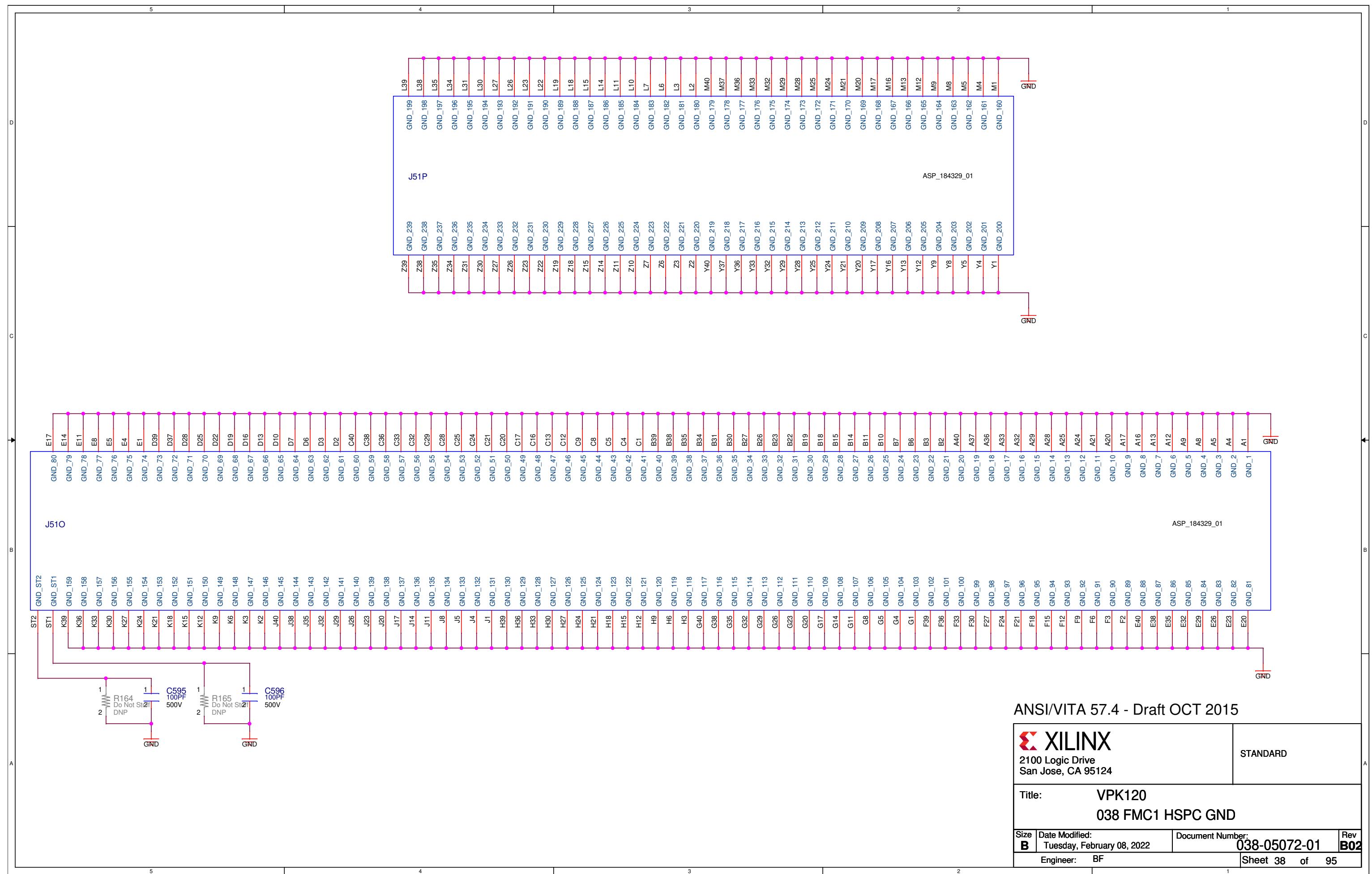
Title: VPK120
036 FMC1 HSPC Banks H-K

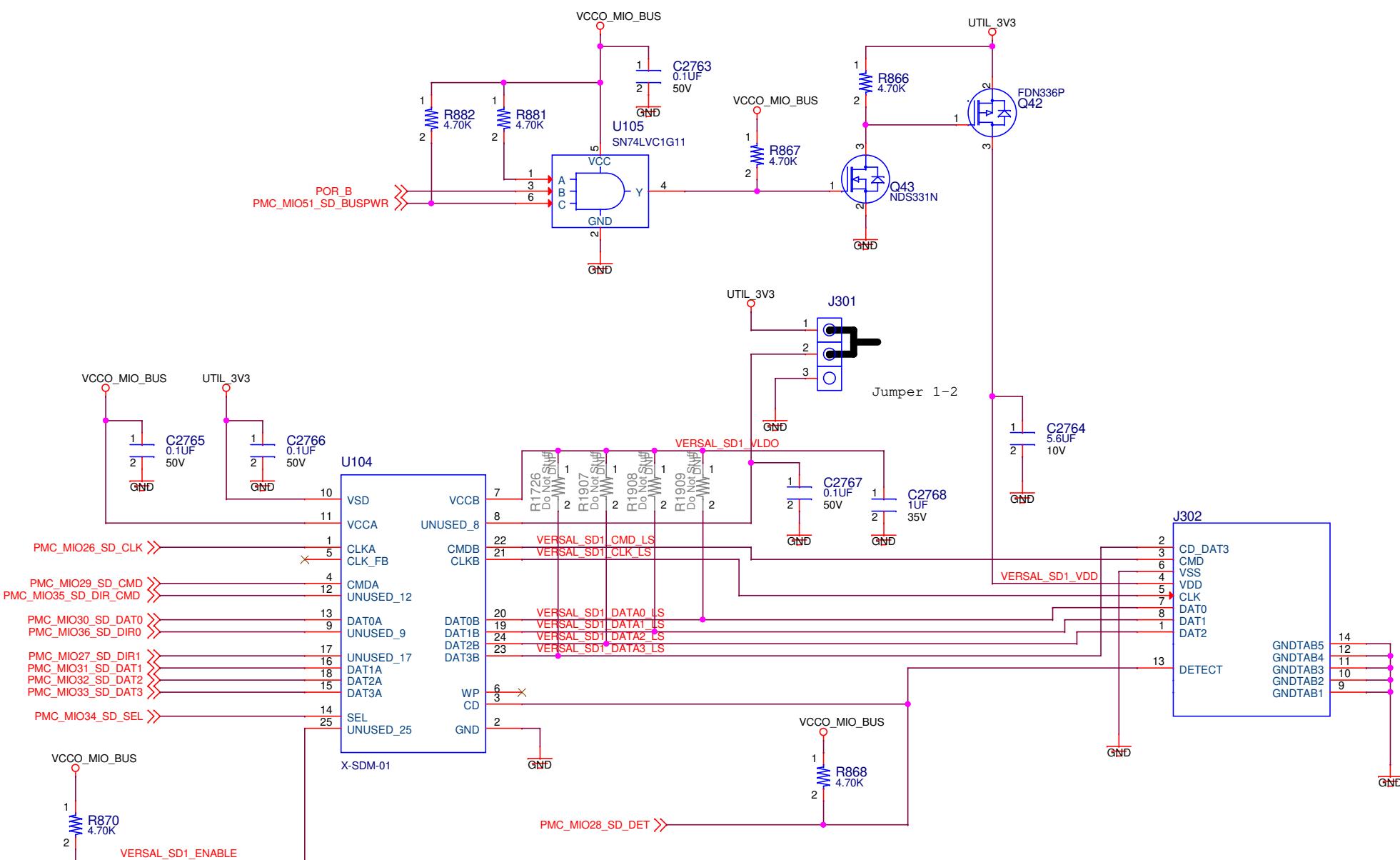
Size	Date Modified:	Document Number:	Rev
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 XILINX 2100 Logic Drive San Jose, CA 95124		STANDARD
Title: VPK120		
037 FMC1 HSPC Banks L M Y Z		
Size: B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01
		Rev: B02
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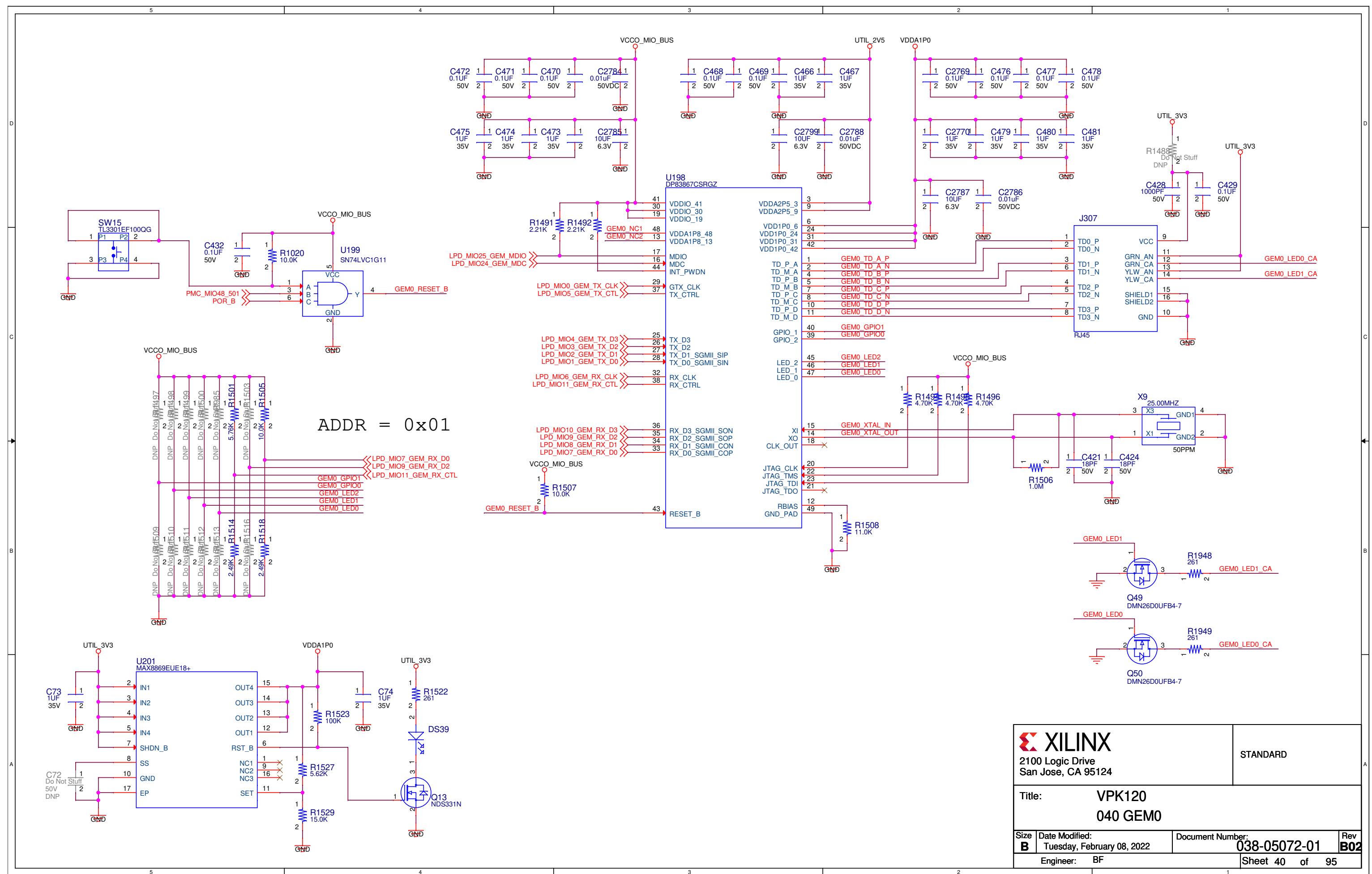
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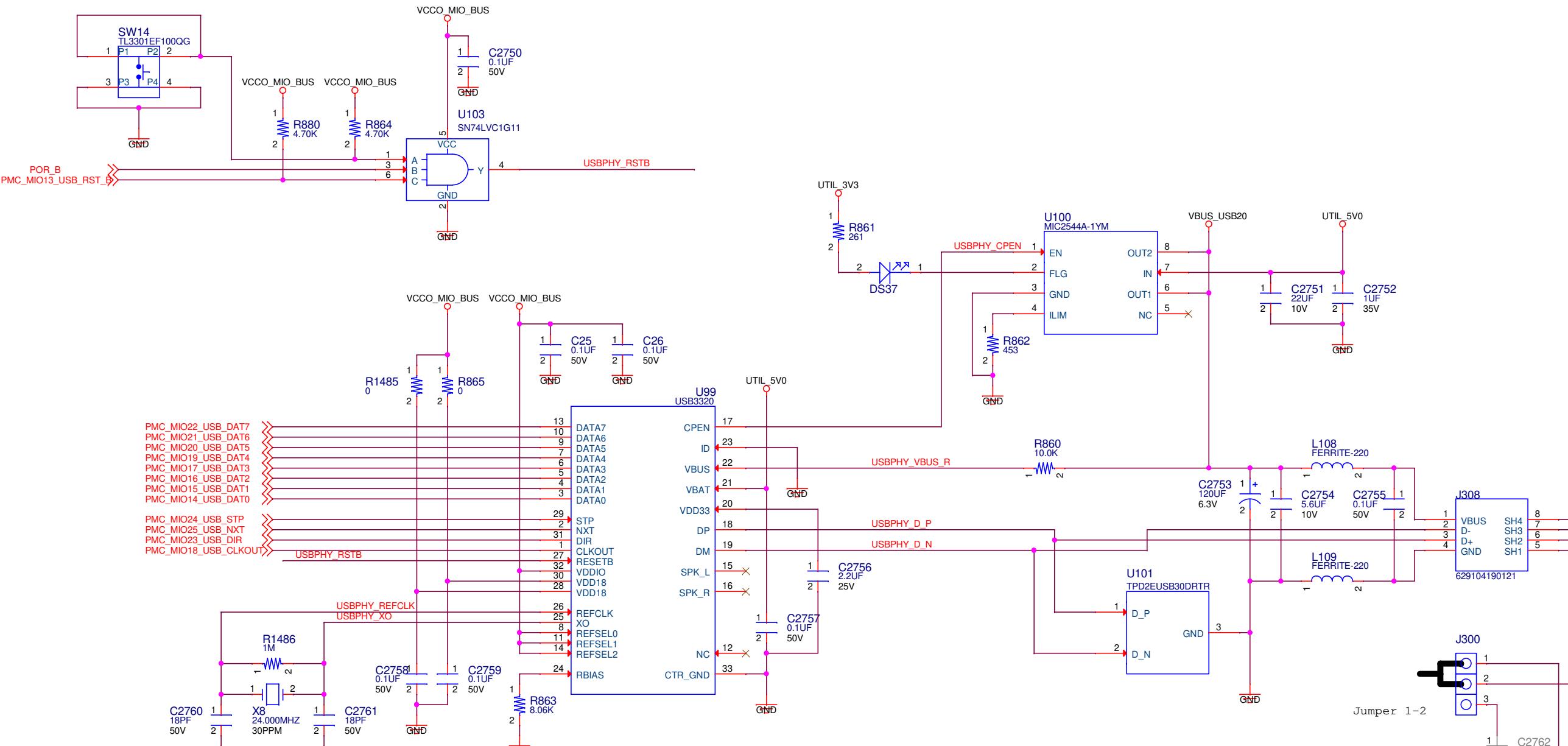
2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: **VPK120**
039 SD1 & Level Shifter

Size	Date Modified:	Document Number:	Rev
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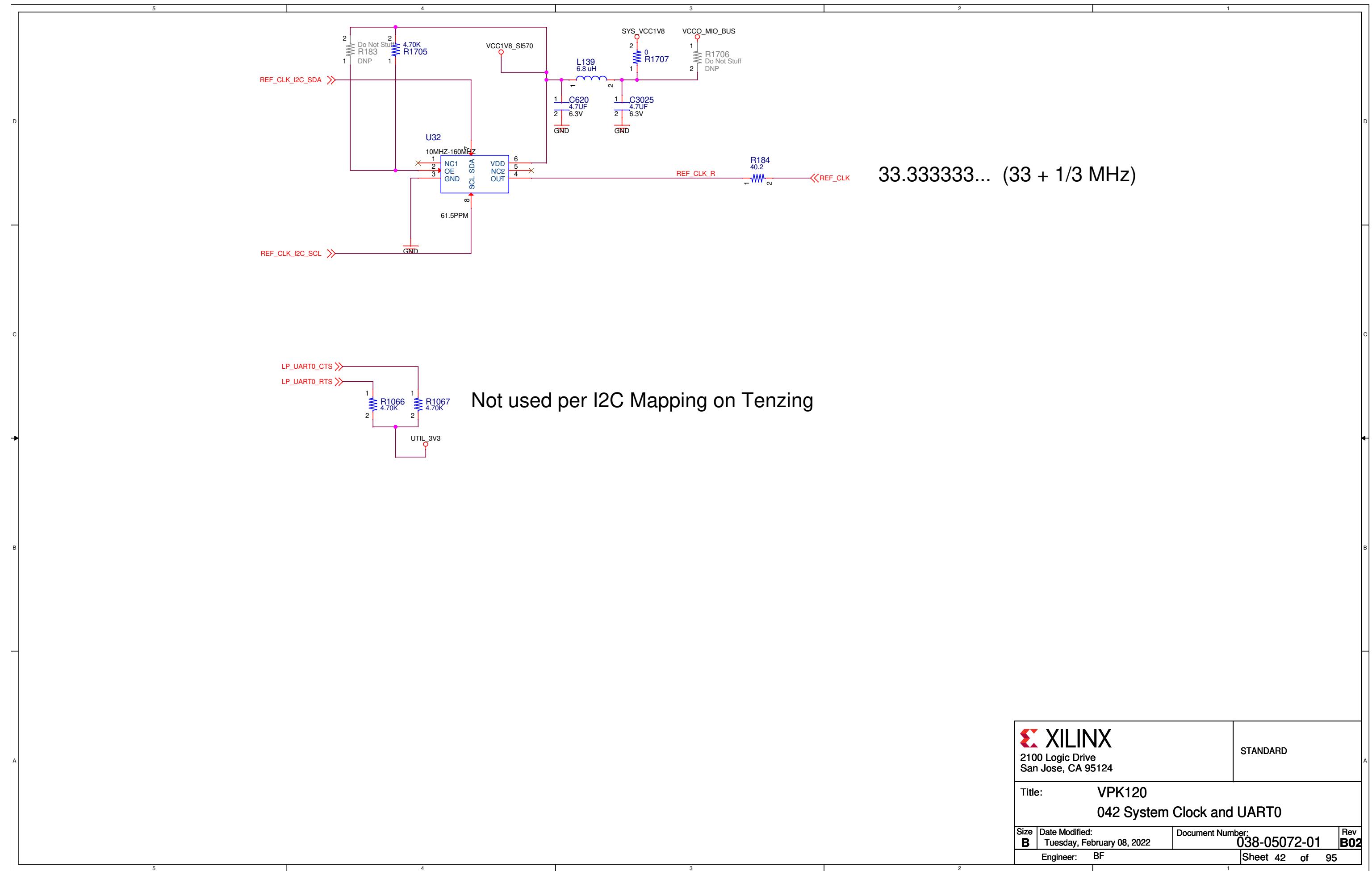
XILINX

2100 Logic Drive
San Jose, CA 95124

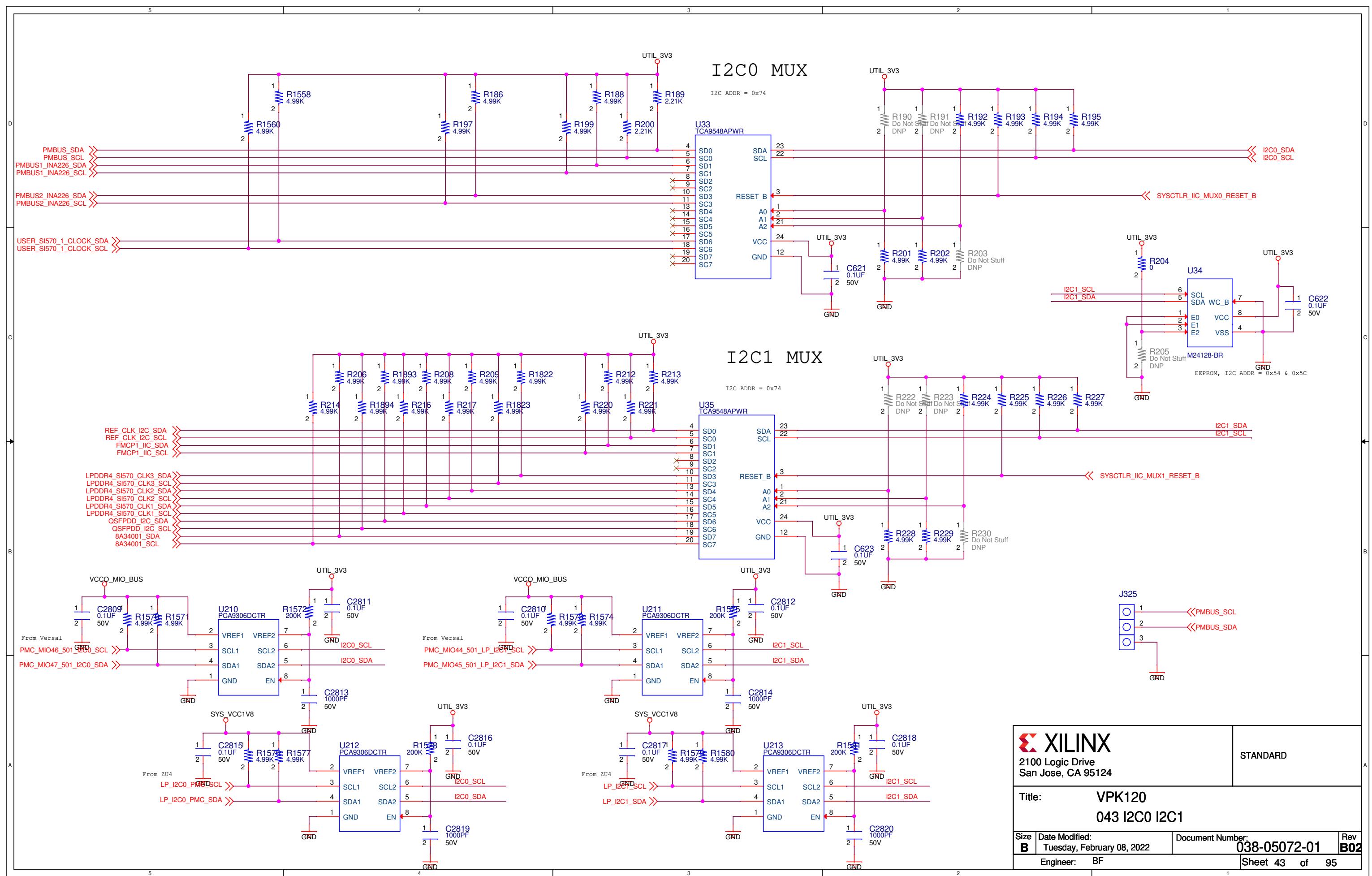
STANDARD

Title: VPK120
041 SMSC3320 ULPI USB

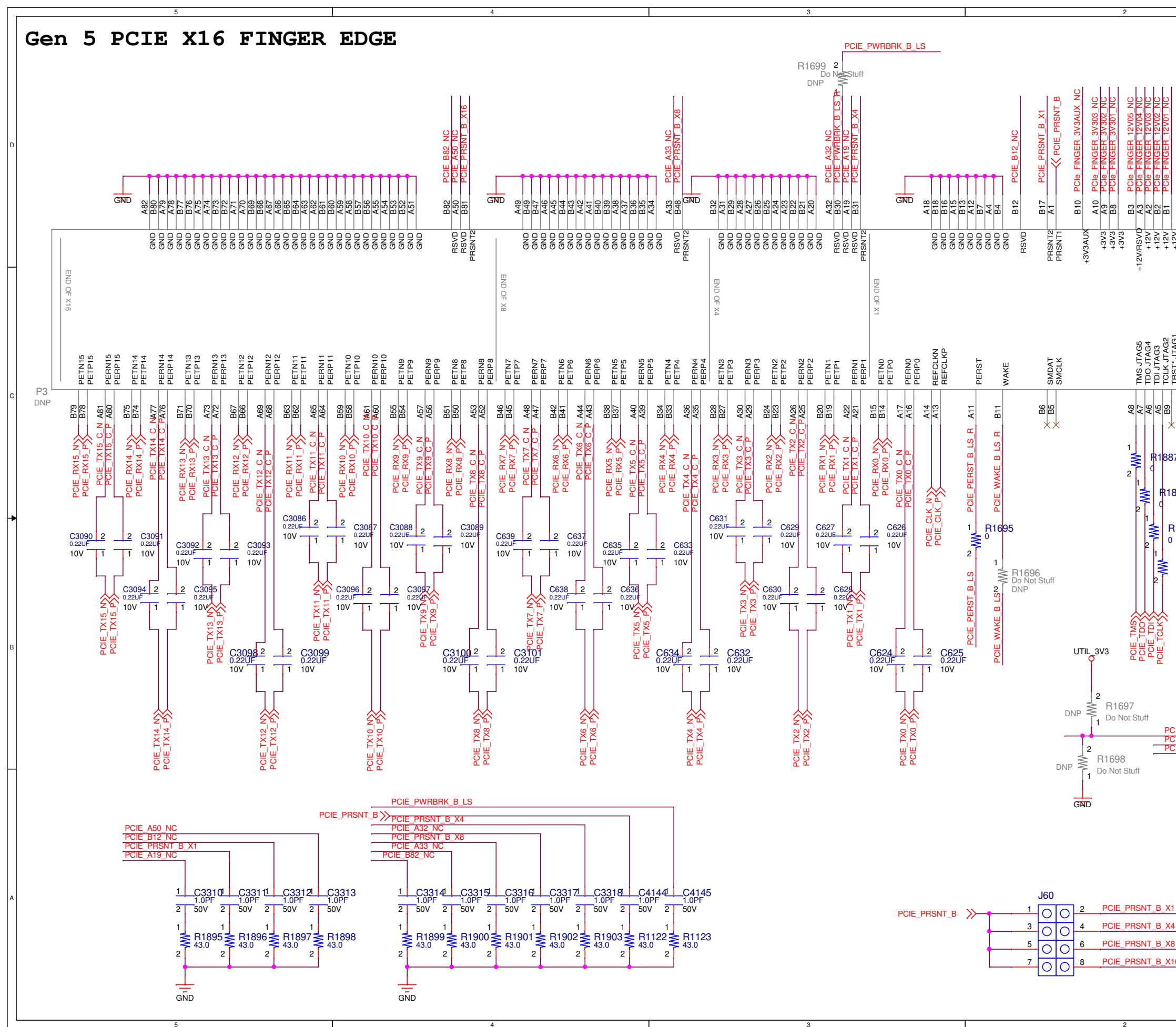
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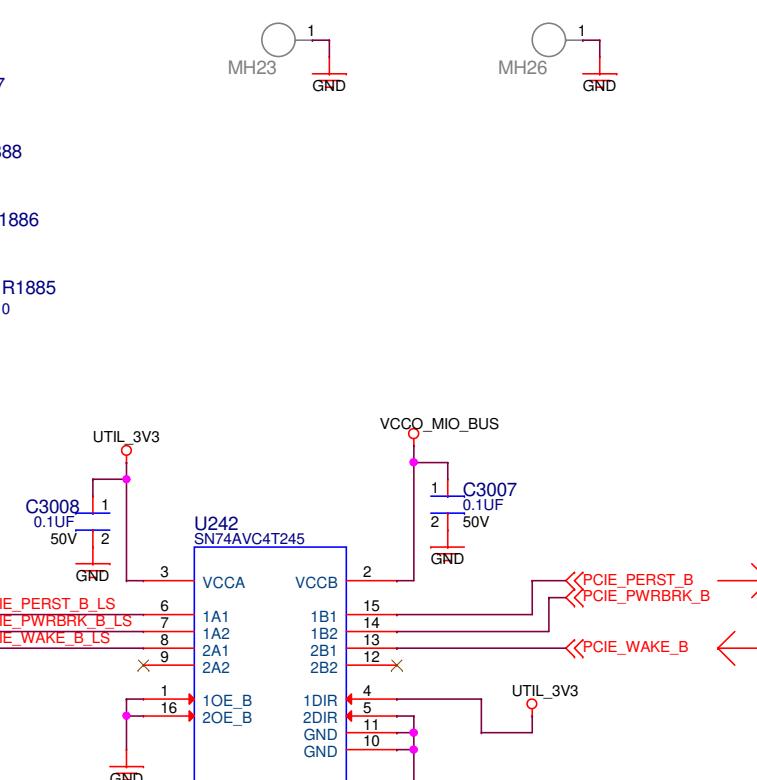
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2100 Logic Drive San Jose, CA 95124		
Title:	VPK120	
	042 System Clock and UART0	
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Gen 5 PCIE X16 FINGER EDGE

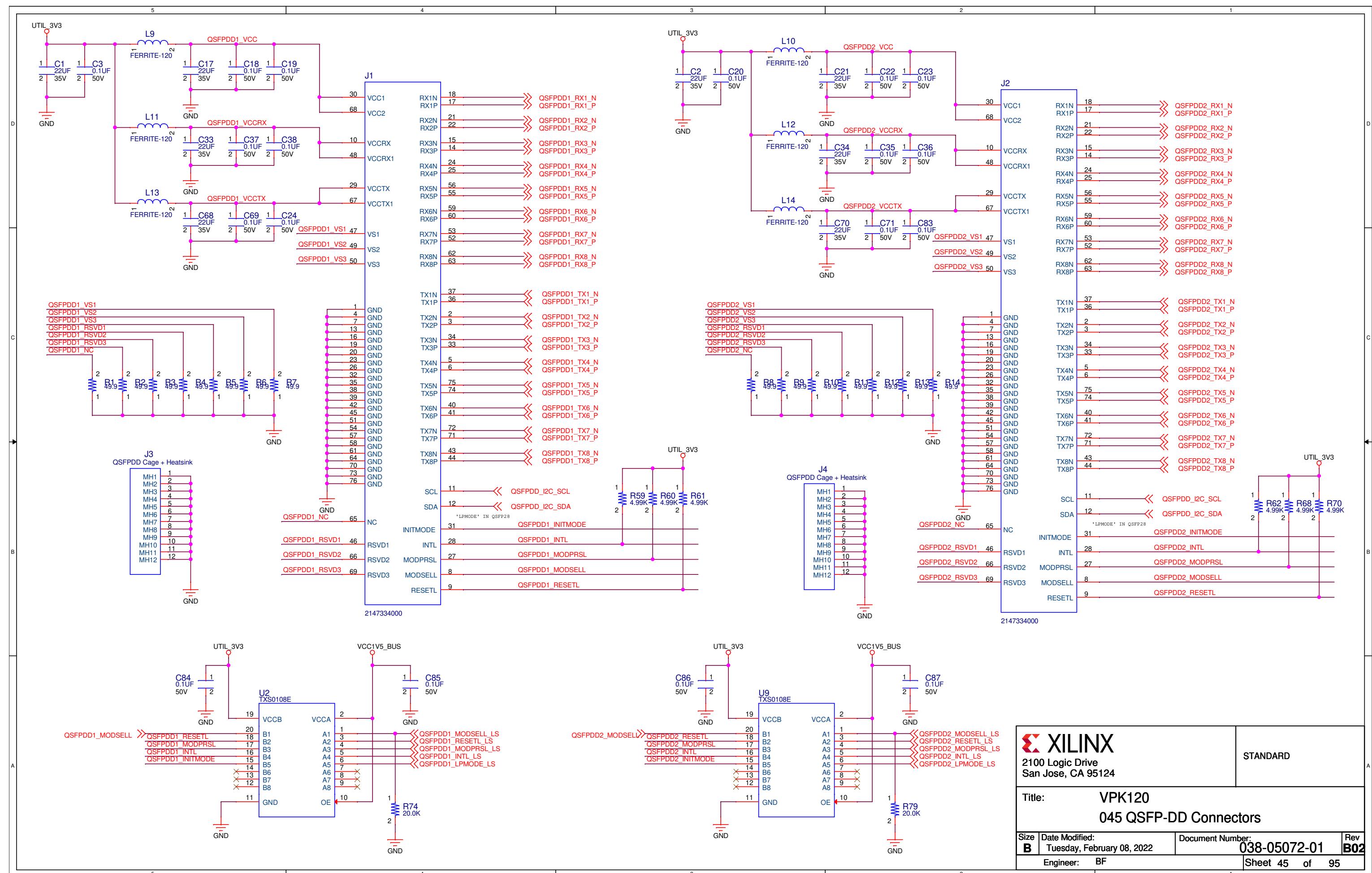


Bracket Mounting Holes



Title: VPK120
044 PCIe End Point

Size B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01	Rev B02
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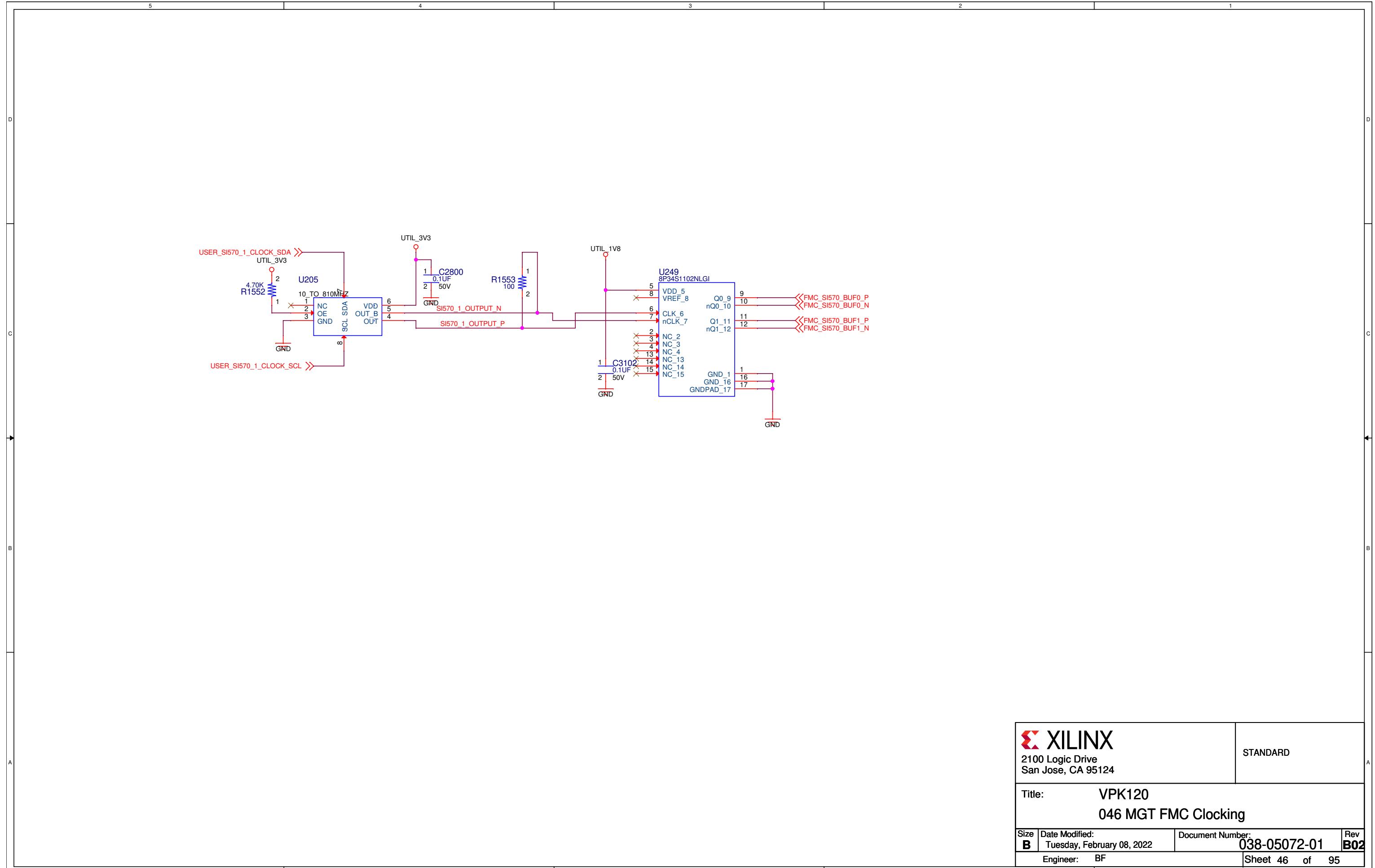
XILINX

2100 Logic Drive
San Jose, CA 95124

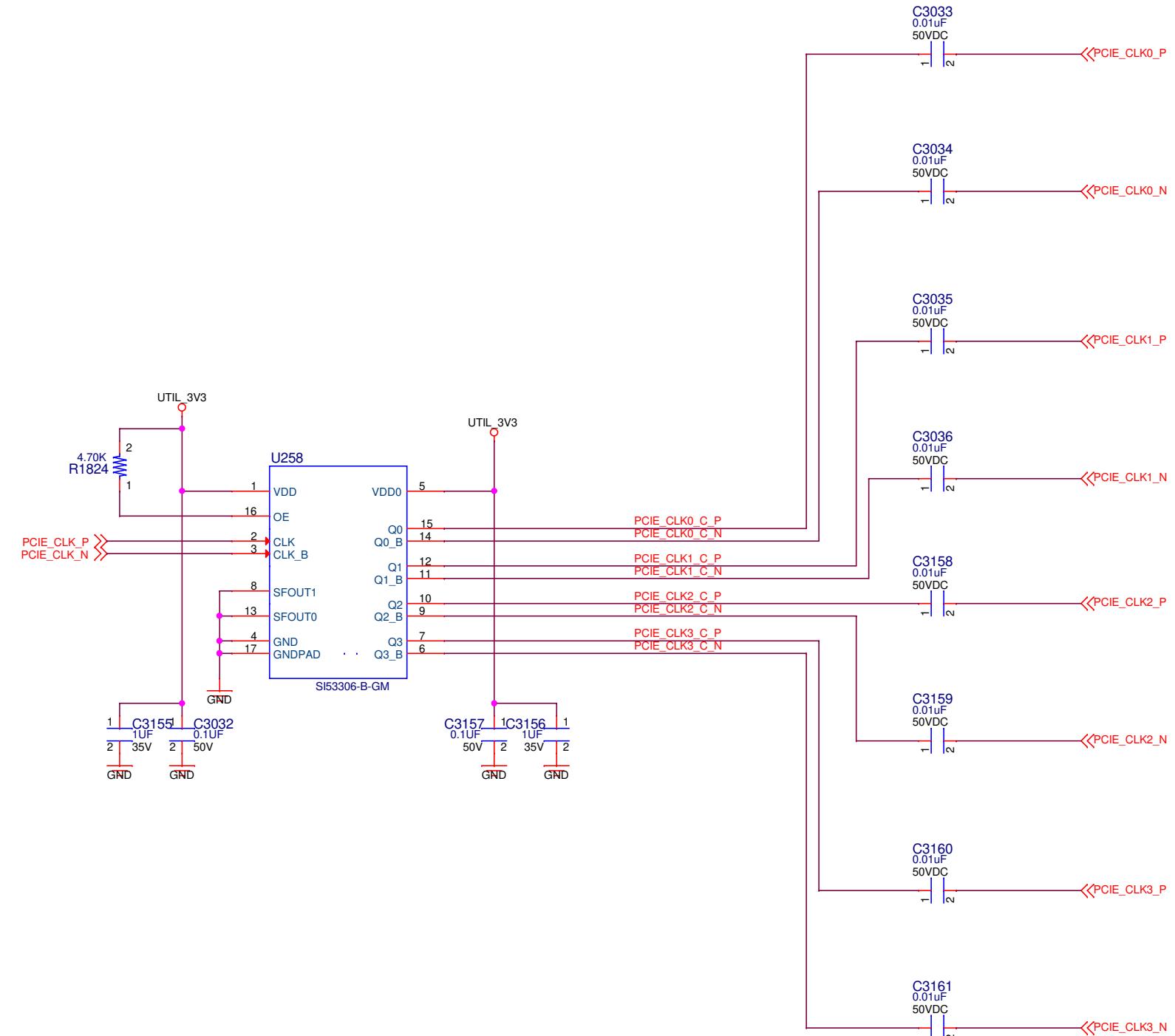
STANDARD

Title: **VPK120**
045 QSFP-DD Connectors

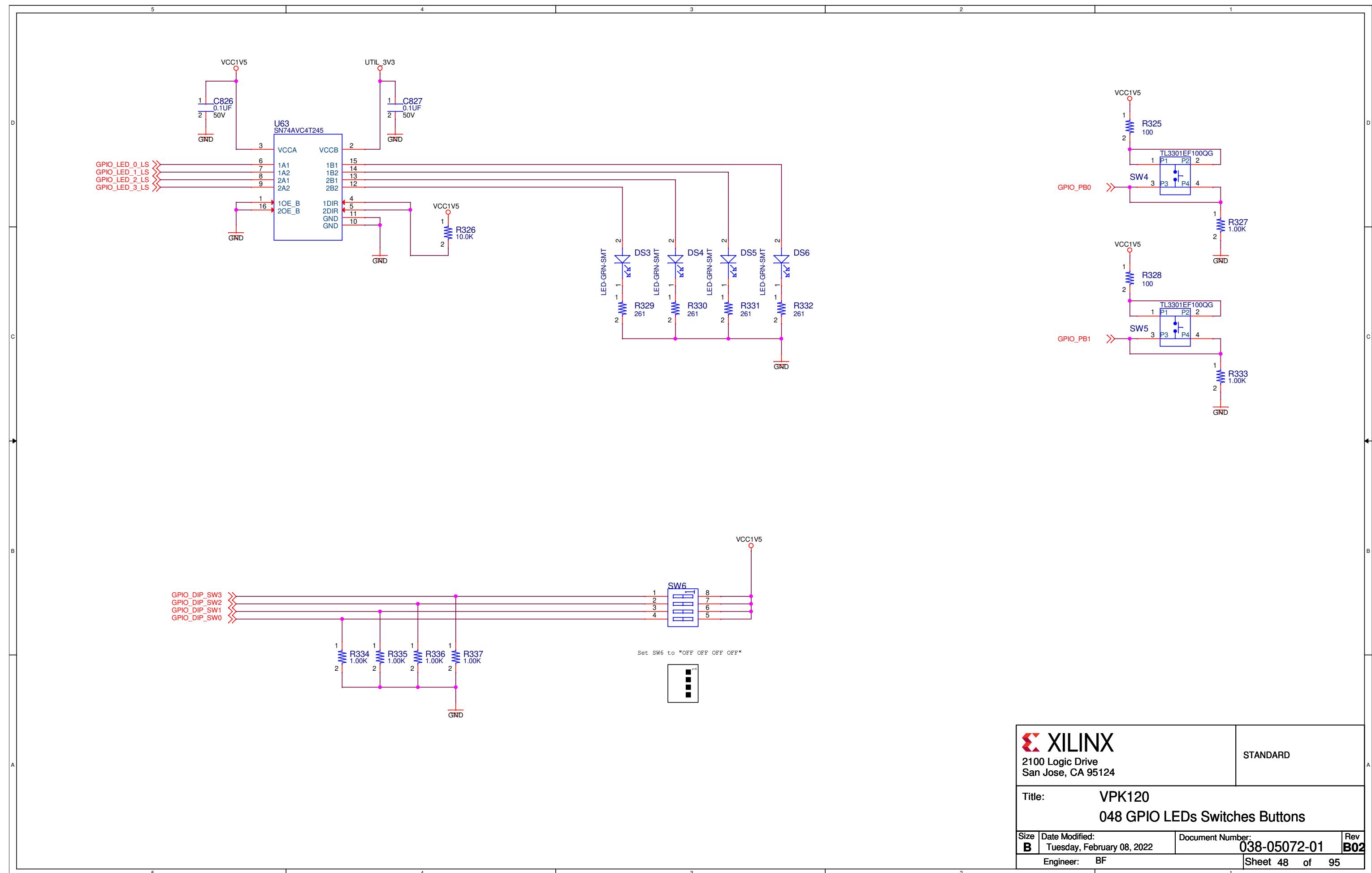
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B	Tuesday, February 08, 2022	038-05072-01	B02
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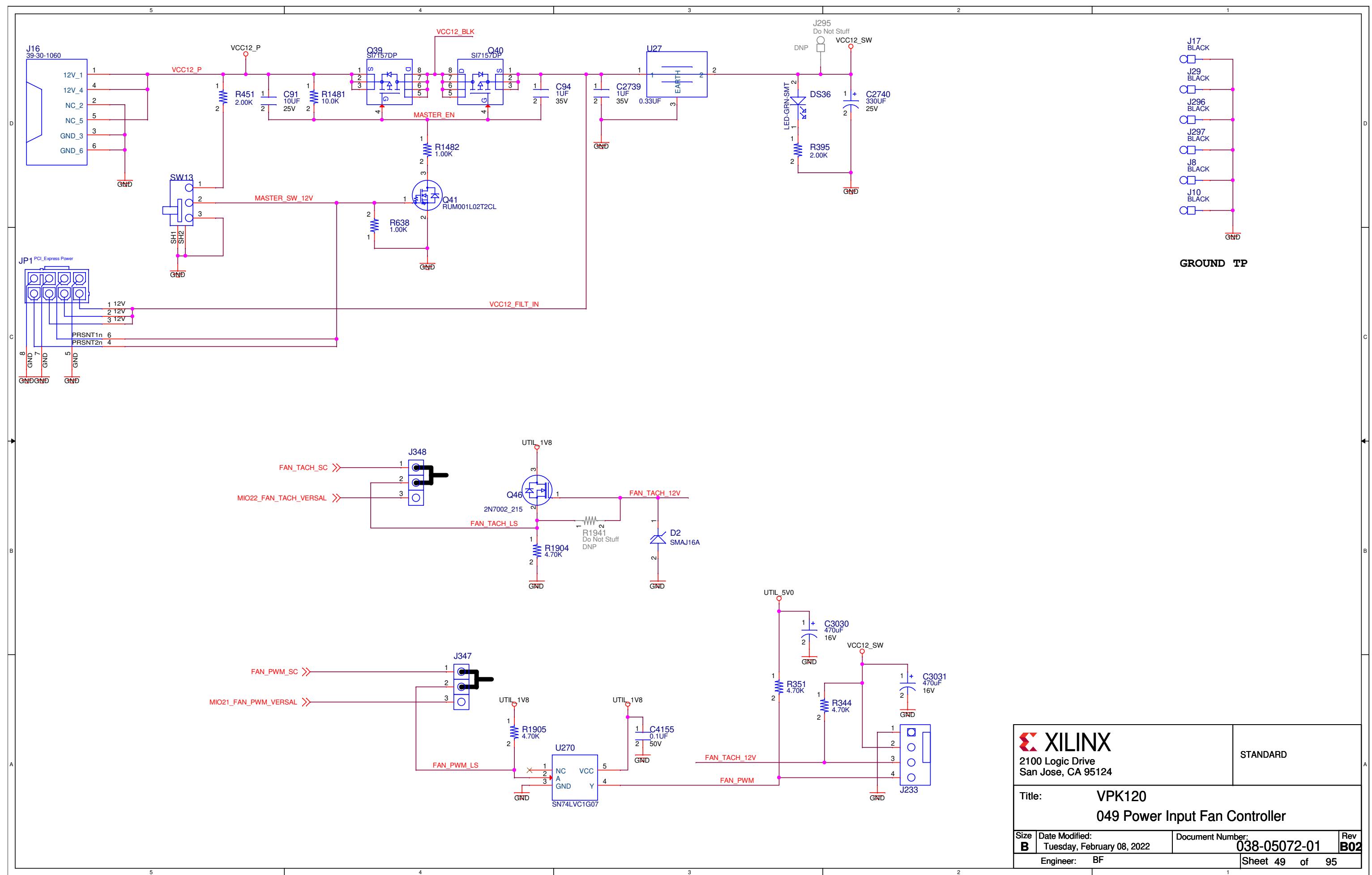
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2100 Logic Drive San Jose, CA 95124		
Title: VPK120 046 MGT FMC Clocking		
Size	Date Modified: B Tuesday, February 08, 2022	Document Number: 038-05072-01 Rev B02
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		XILINX	STANDARD
2100 Logic Drive San Jose, CA 95124			
Title: VPK120			
047 MGT PCIe Clocking			
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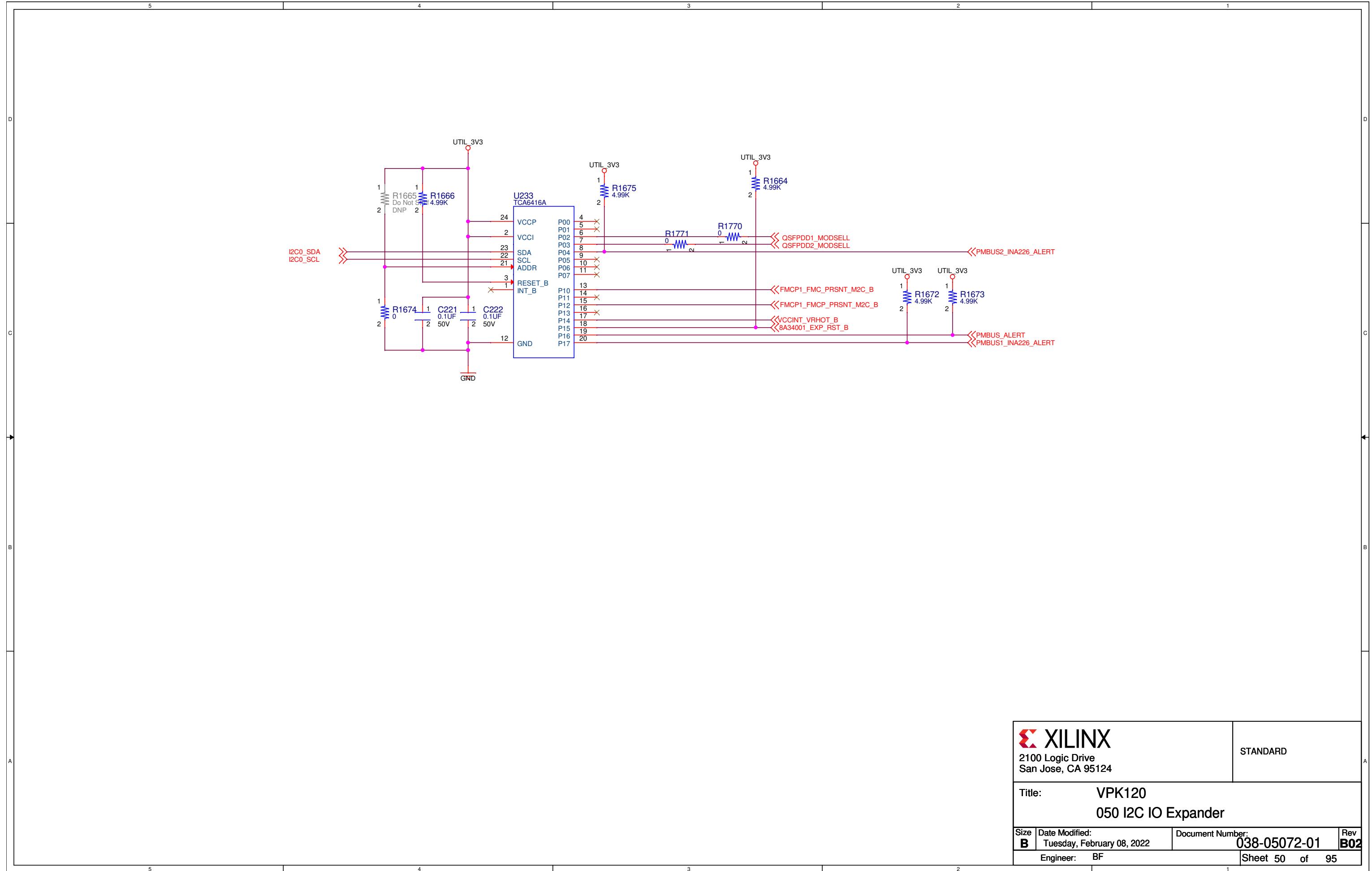
XILINX 2100 Logic Drive San Jose, CA 95124		STANDARD
Title: VPK120 048 GPIO LEDs Switches Buttons		
Size: B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01
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San Jose, CA 95124

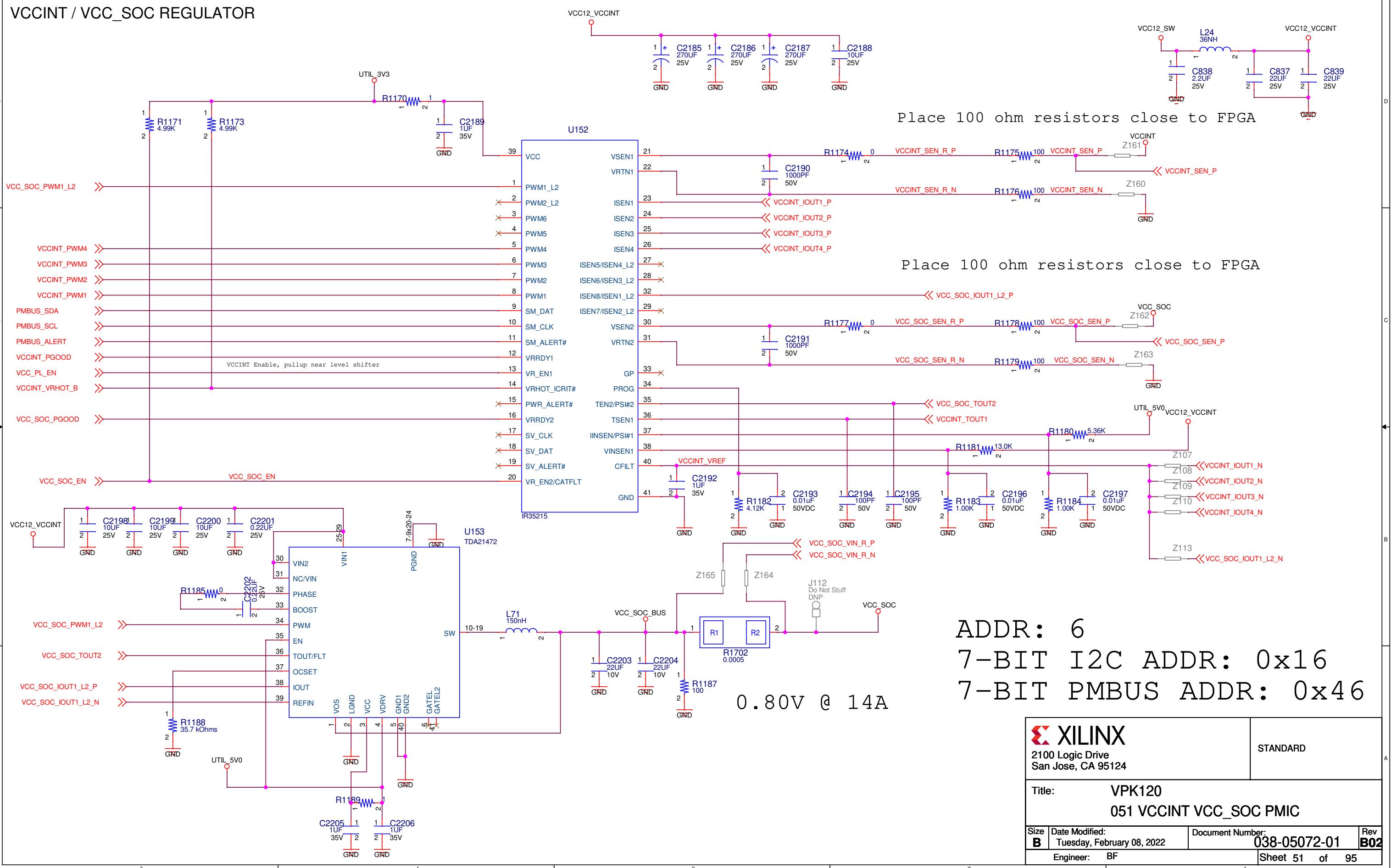
Title: **VPK120**
049 Power Input Fan Controller

Size	Date Modified:	Document Number:	Rev
B	Tuesday, February 08, 2022	038-05072-01	B02
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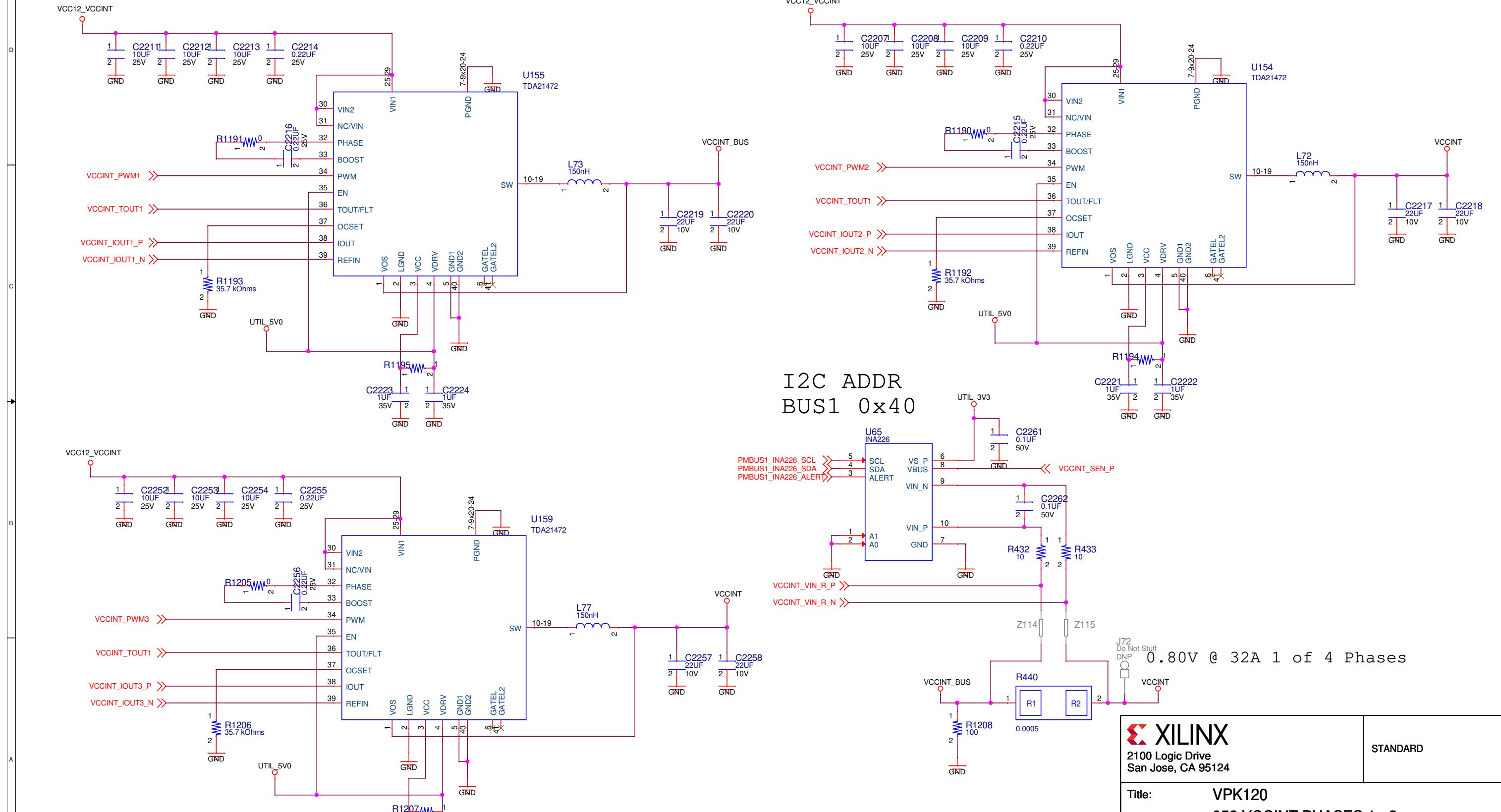


XILINX		STANDARD
2100 Logic Drive San Jose, CA 95124		
Title: VPK120 050 I2C IO Expander		
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VCCINT / VCC_SOC REGULATOR



VCCINT PHASES 1,2,3



XILINX

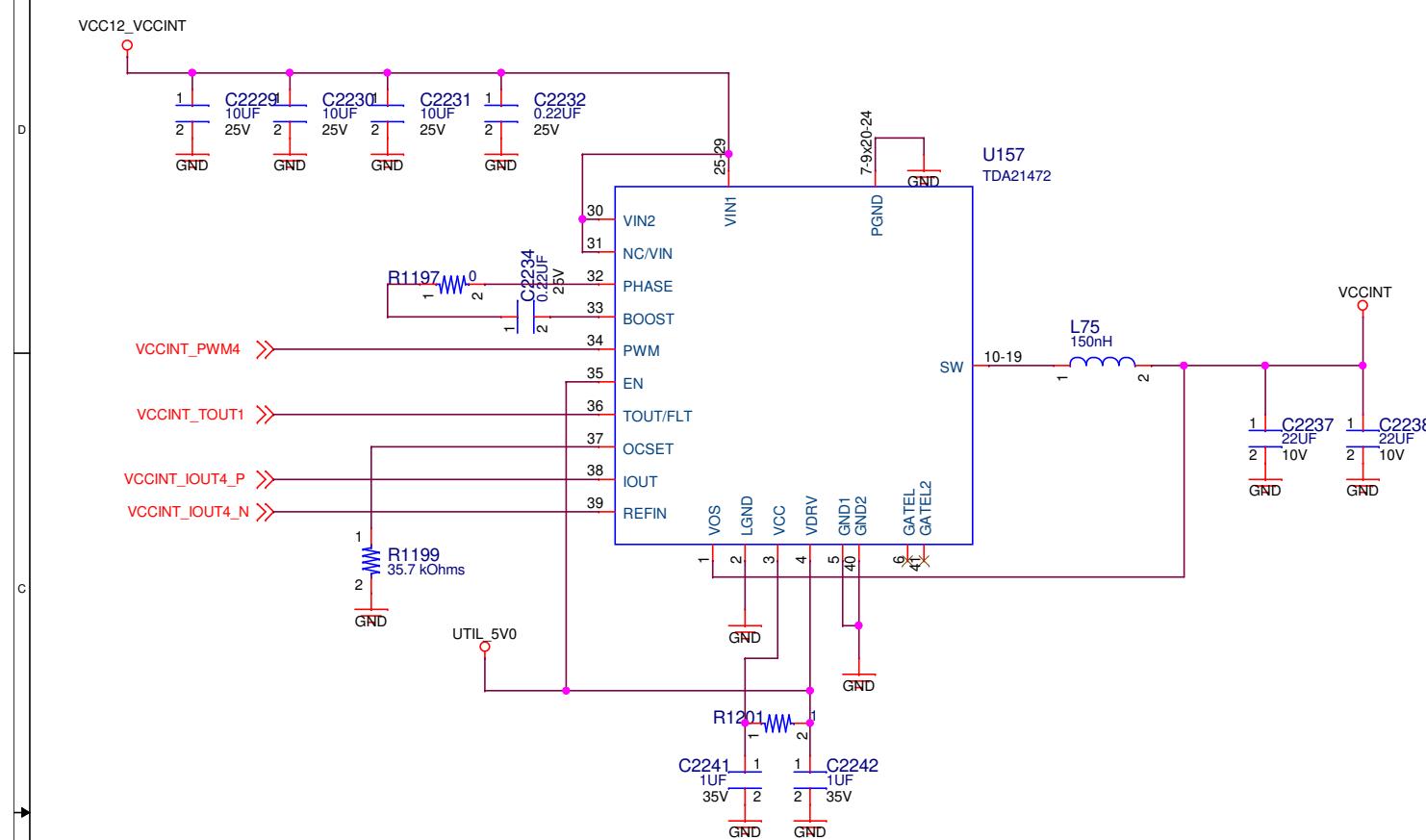
2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: VPK120
052 VCCINT PHASES 1 - 3

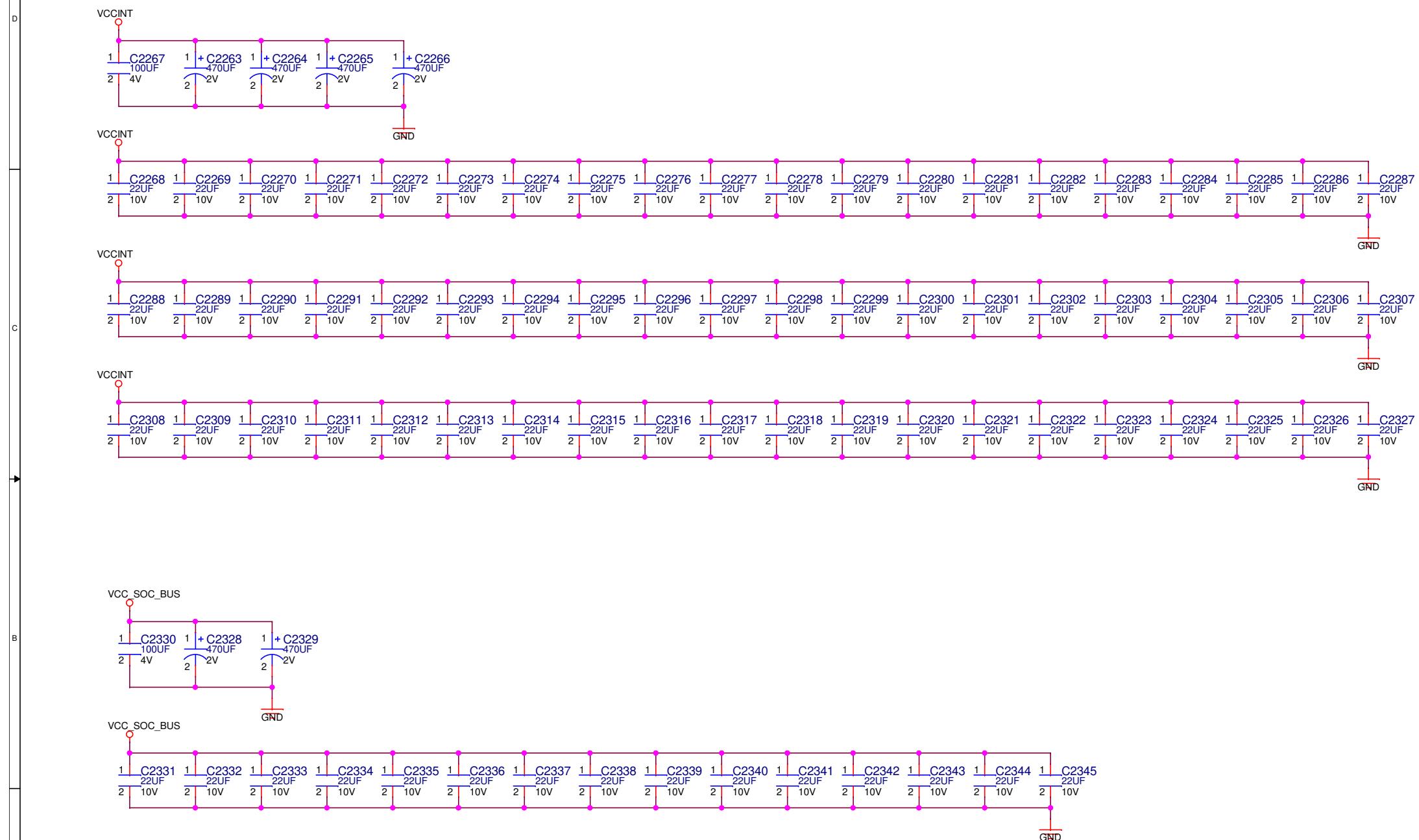
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VCCINT PHASE 4



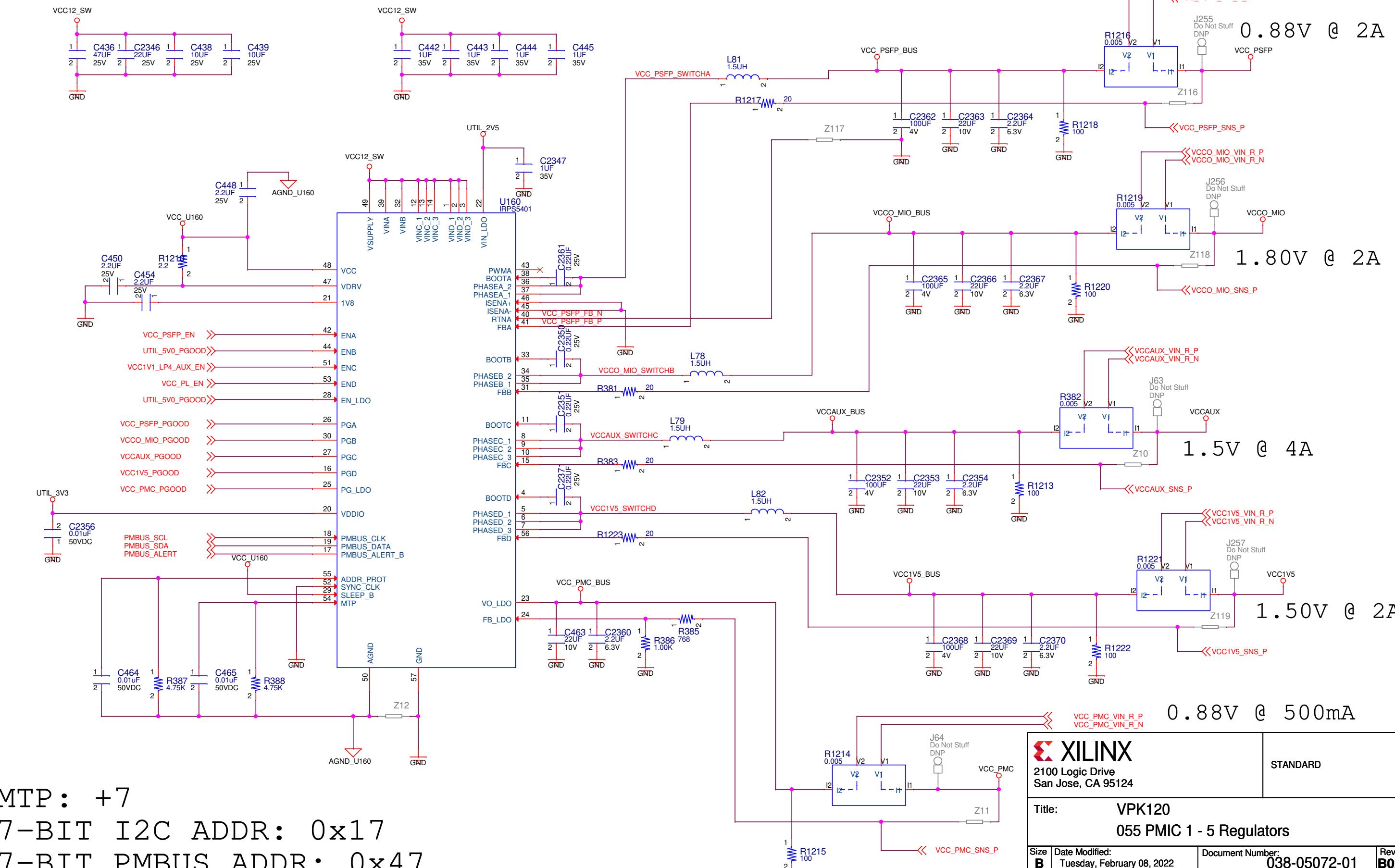
XILINX		STANDARD
2100 Logic Drive San Jose, CA 95124		
Title: VPK120 053 VCCINT PHASE 4		
Size	Date Modified: B Tuesday, February 08, 2022	Document Number: 038-05072-01
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VCCINT / VCC_SOC Decap



 XILINX		STANDARD
2100 Logic Drive San Jose, CA 95124		
Title:	VPK120	
054 VCCINT / VCC_SOC Decap		
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VCC_PMC/PSFP/VCCO_MIO/VCCAUX/VCC1V5 REGULATOR



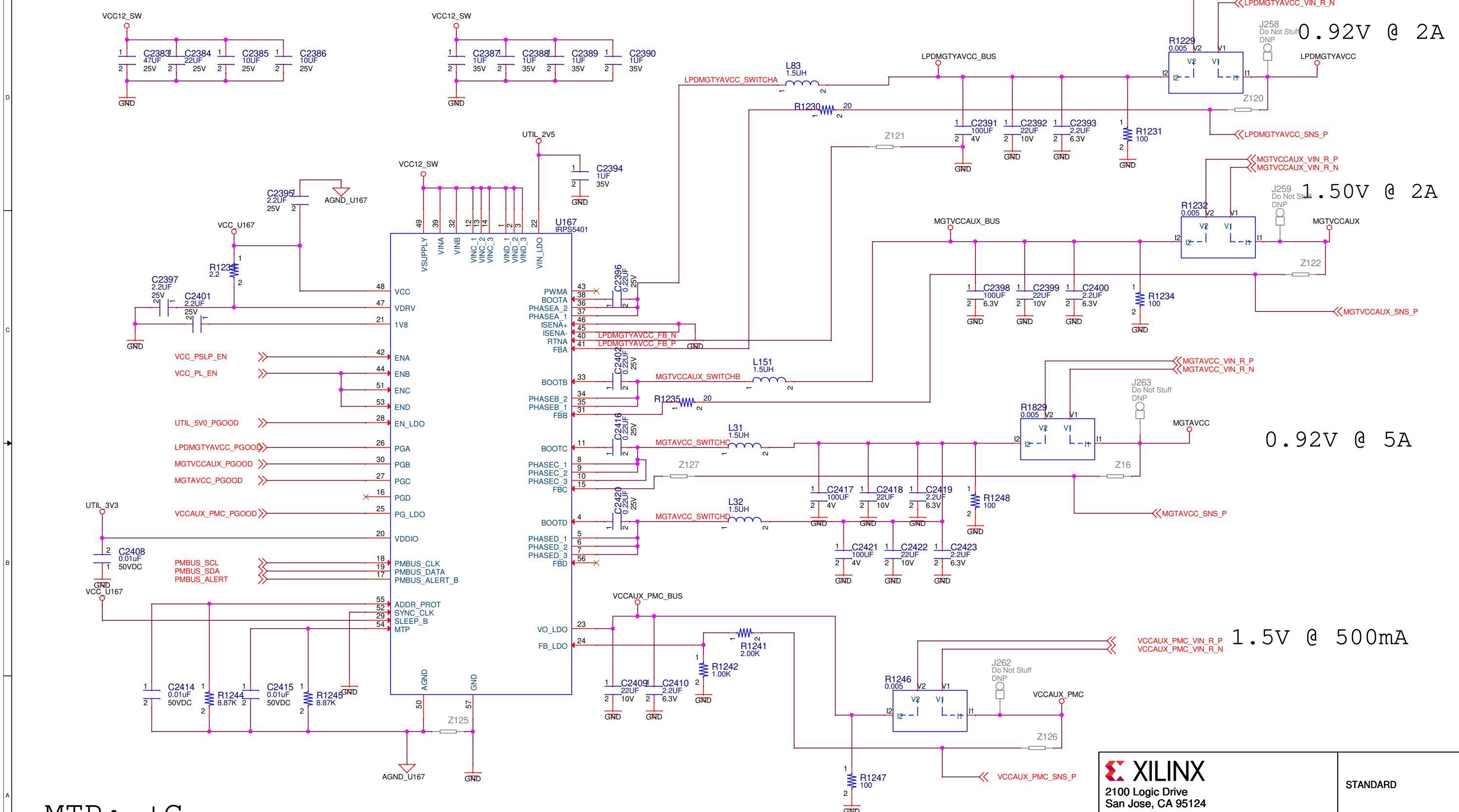
XILINX

2100 Logic Drive
San Jose, CA 95124

Title: VPK120
055 PMIC 1 - 5 Regulators

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VCCAUX_PMC/LPDMGTYAVCC/MGTVCCAUX/MGTAVCC REGULATO



MTP : +C

7-BIT I2C ADDR: 0x10

7-BIT PMBUS ADDR: 0x40

YII INY

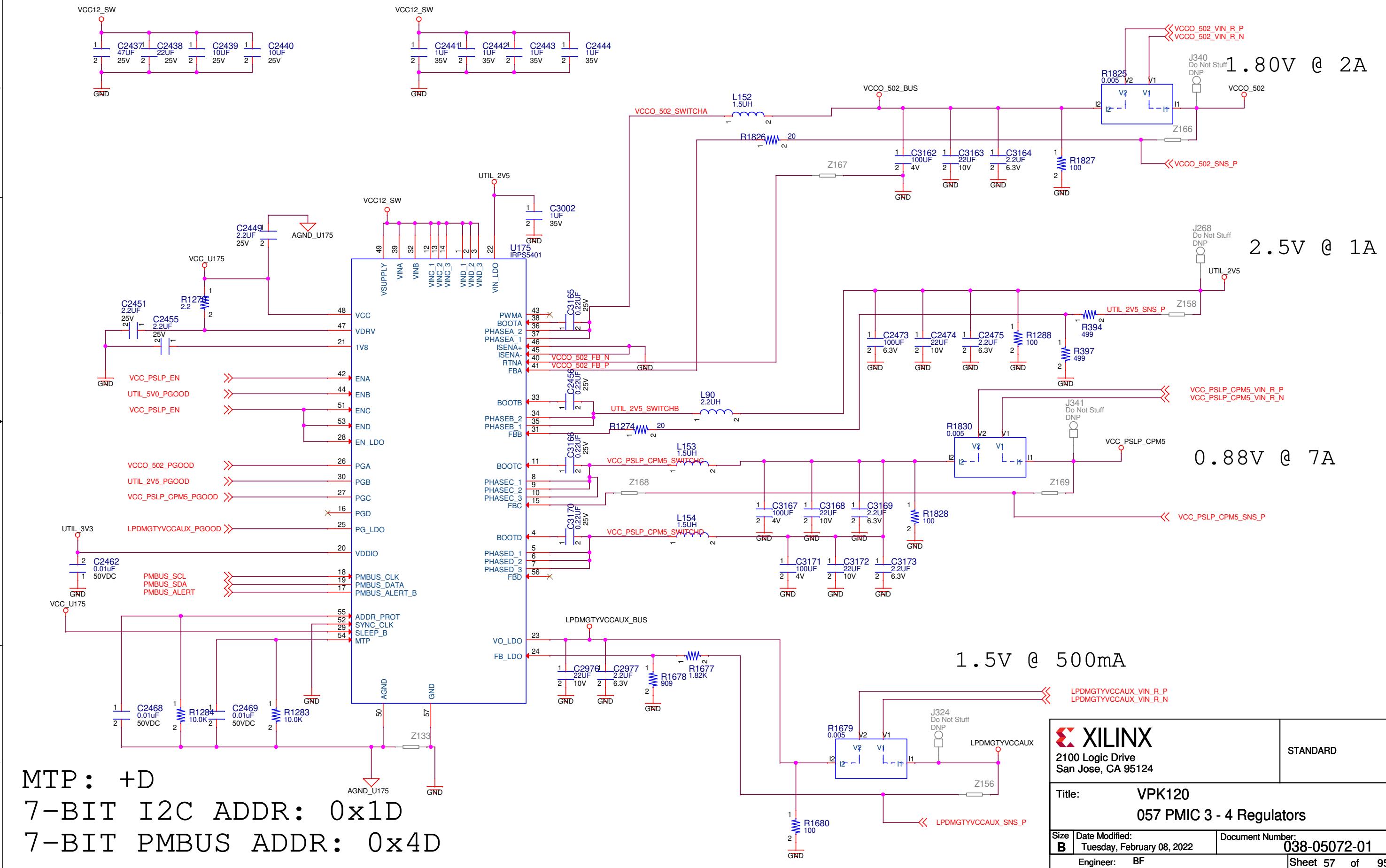
XILINX
2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: VPK120
056 PMIC 2 - 4 Regulators

Size B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01	Rev B02
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VCCO_502/UTIL_2V5/PSLP_CPM5/MGTYVCCAUX REGULATOR



XILINX

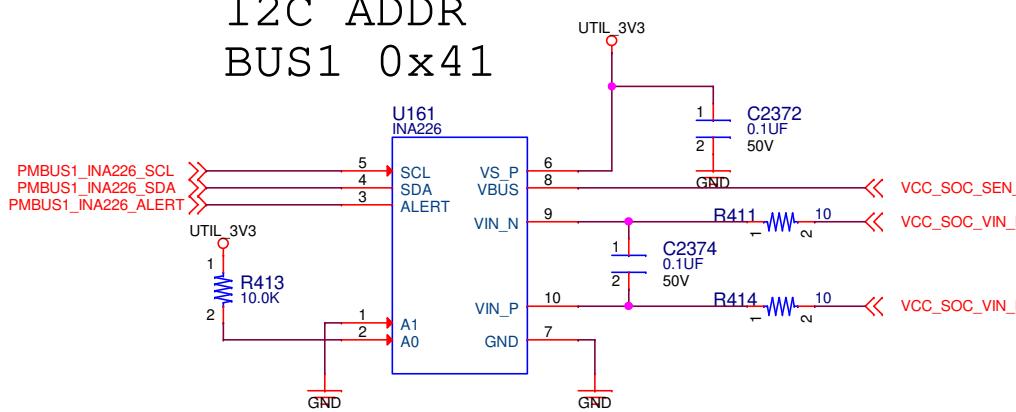
2100 Logic Drive
San Jose, CA 95124

STANDARD

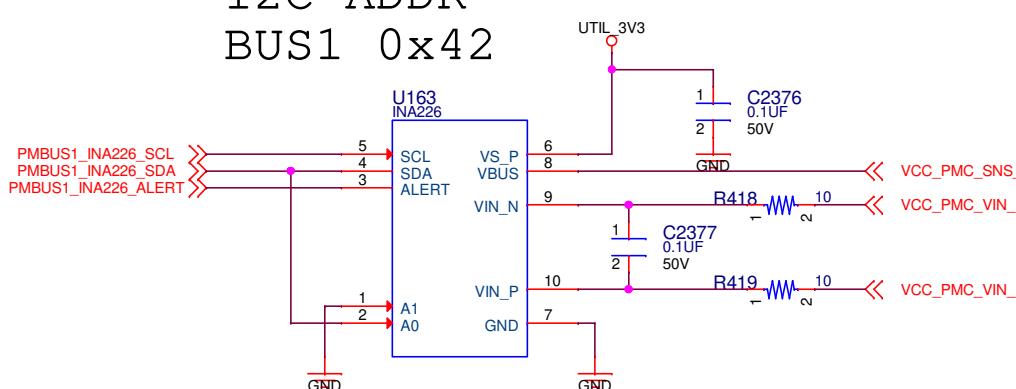
Title: VPK120
057 PMIC 3 - 4 Regulators

Size	Date Modified:	Document Number:	Rev
B	Tuesday, February 08, 2022	038-05072-01	B02
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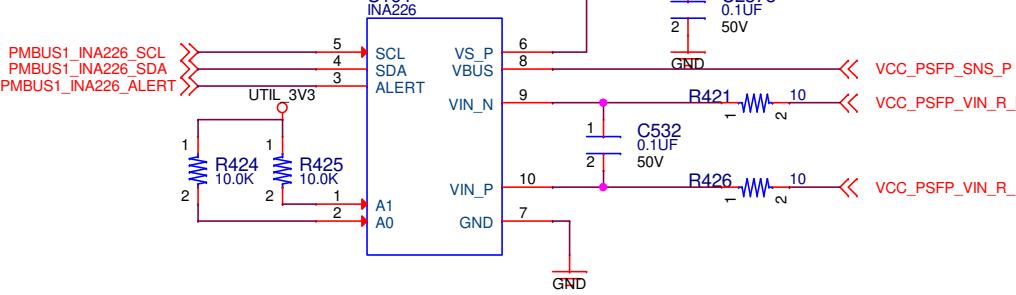
I2C ADDR BUS1 0x41



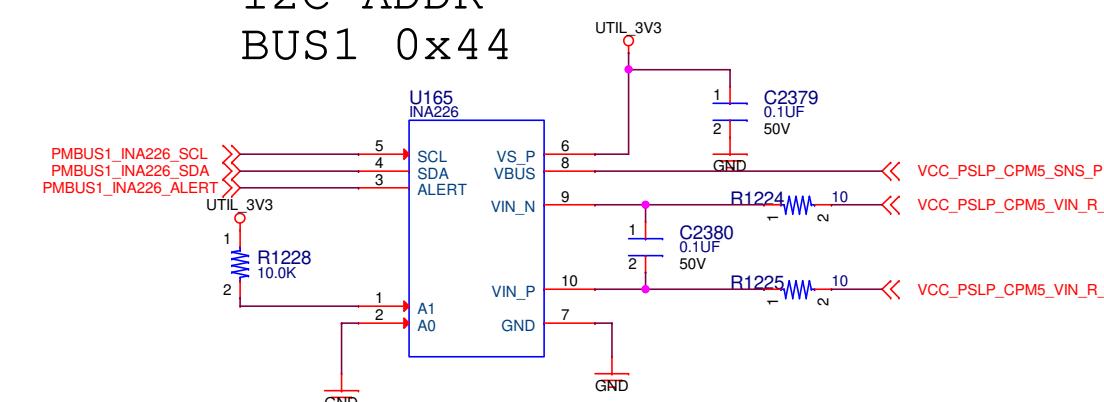
I2C ADDR BUS1 0x42



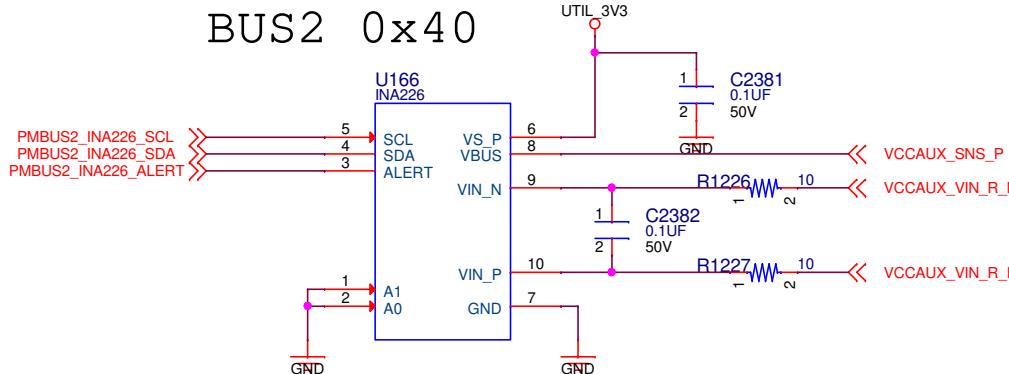
I2C ADDR BUS1 0x45



I2C ADDR BUS1 0x44



I2C ADDR BUS2 0x40



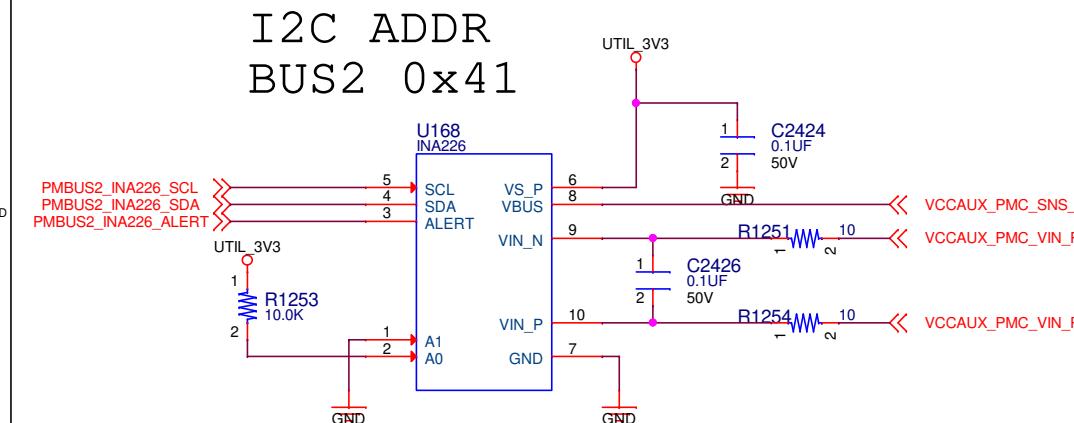
XILINX
2100 Logic Drive
San Jose, CA 95124

STANDARD

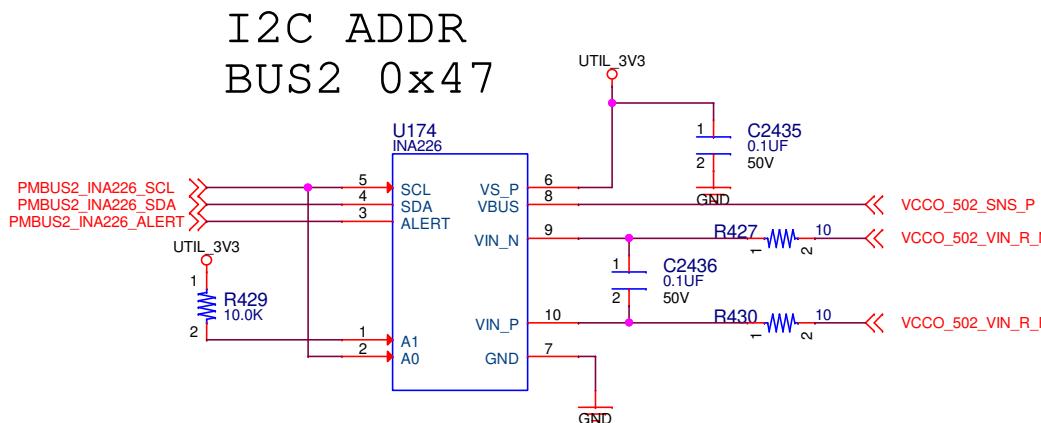
Title: VPK120
058 Current Sense 1

Size	Date Modified:	Document Number:	Rev
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Engineer:	BF	Sheet 58 of 95	1

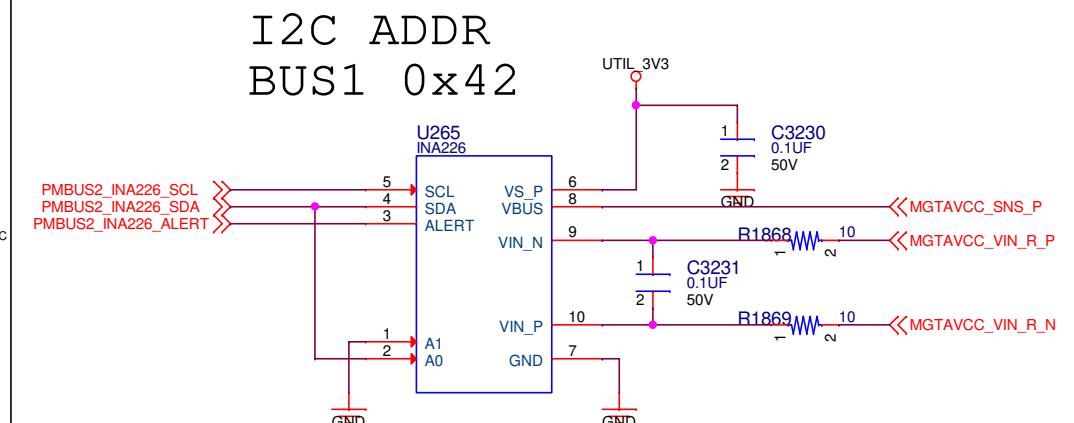
I2C ADDR BUS2 0x41



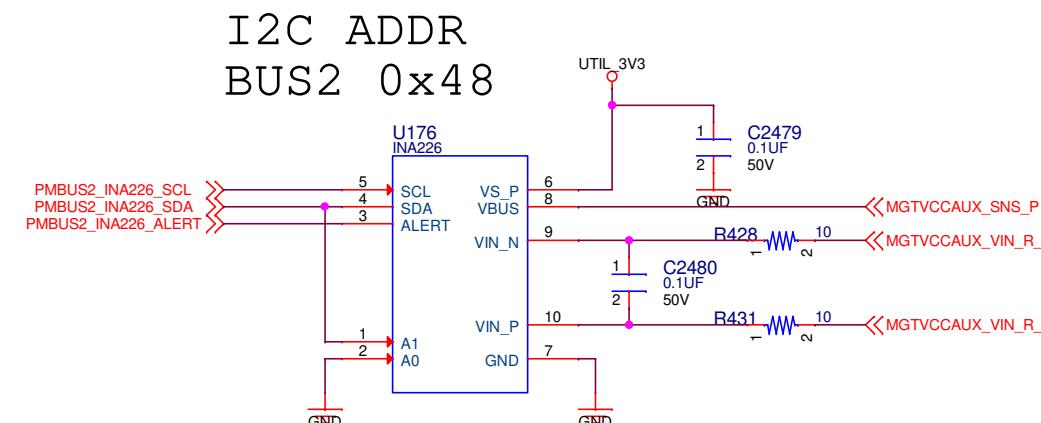
I2C ADDR BUS2 0x47



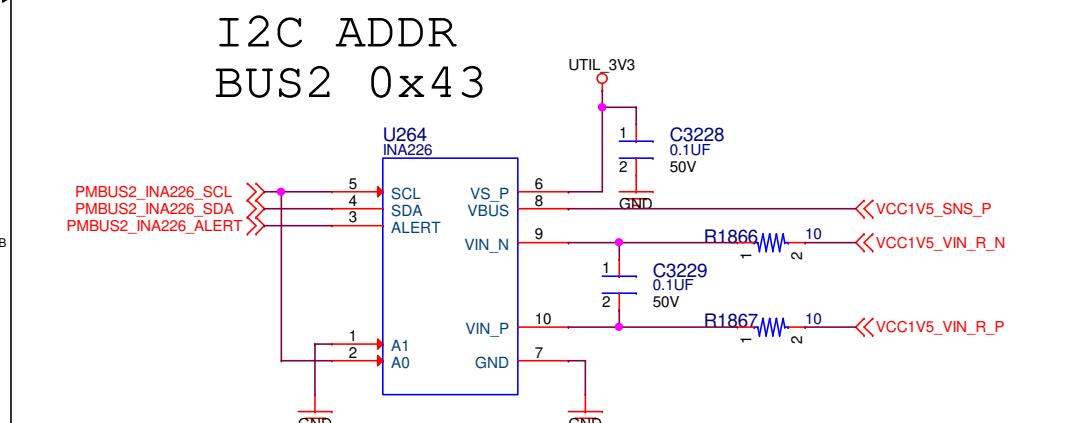
I2C ADDR BUS1 0x42



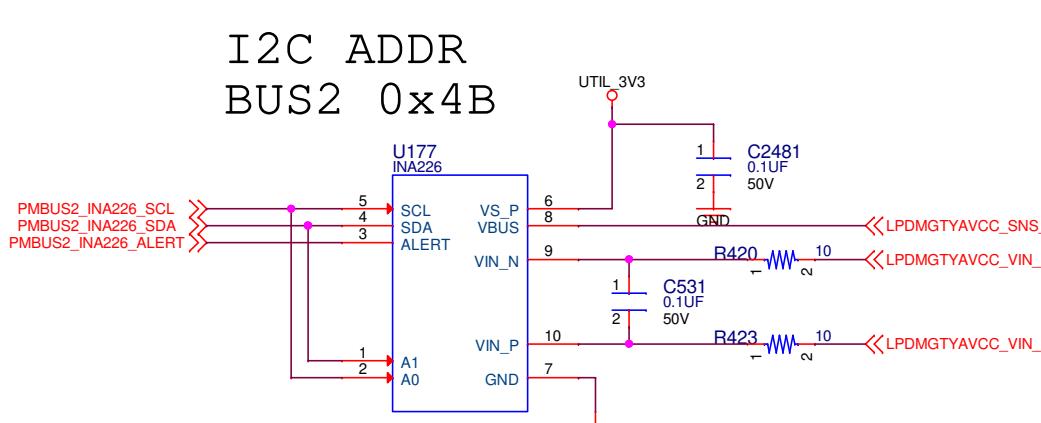
I2C ADDR BUS2 0x48



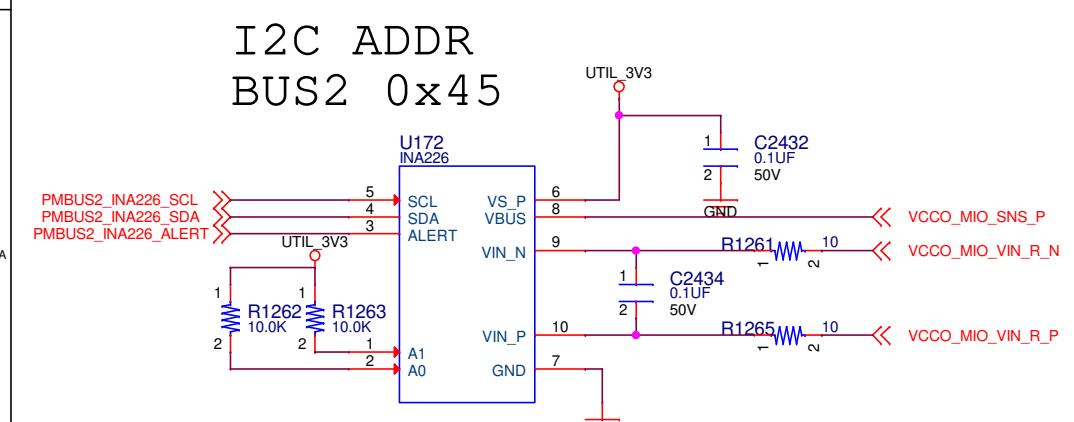
I2C ADDR BUS2 0x43



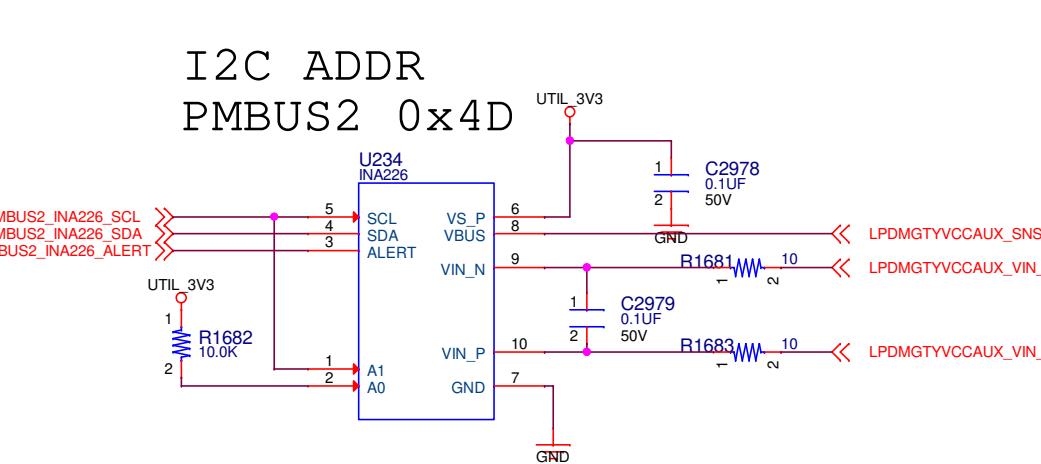
I2C ADDR BUS2 0x4B



I2C ADDR BUS2 0x45



I2C ADDR PMBUS2 0x4D



XILINX

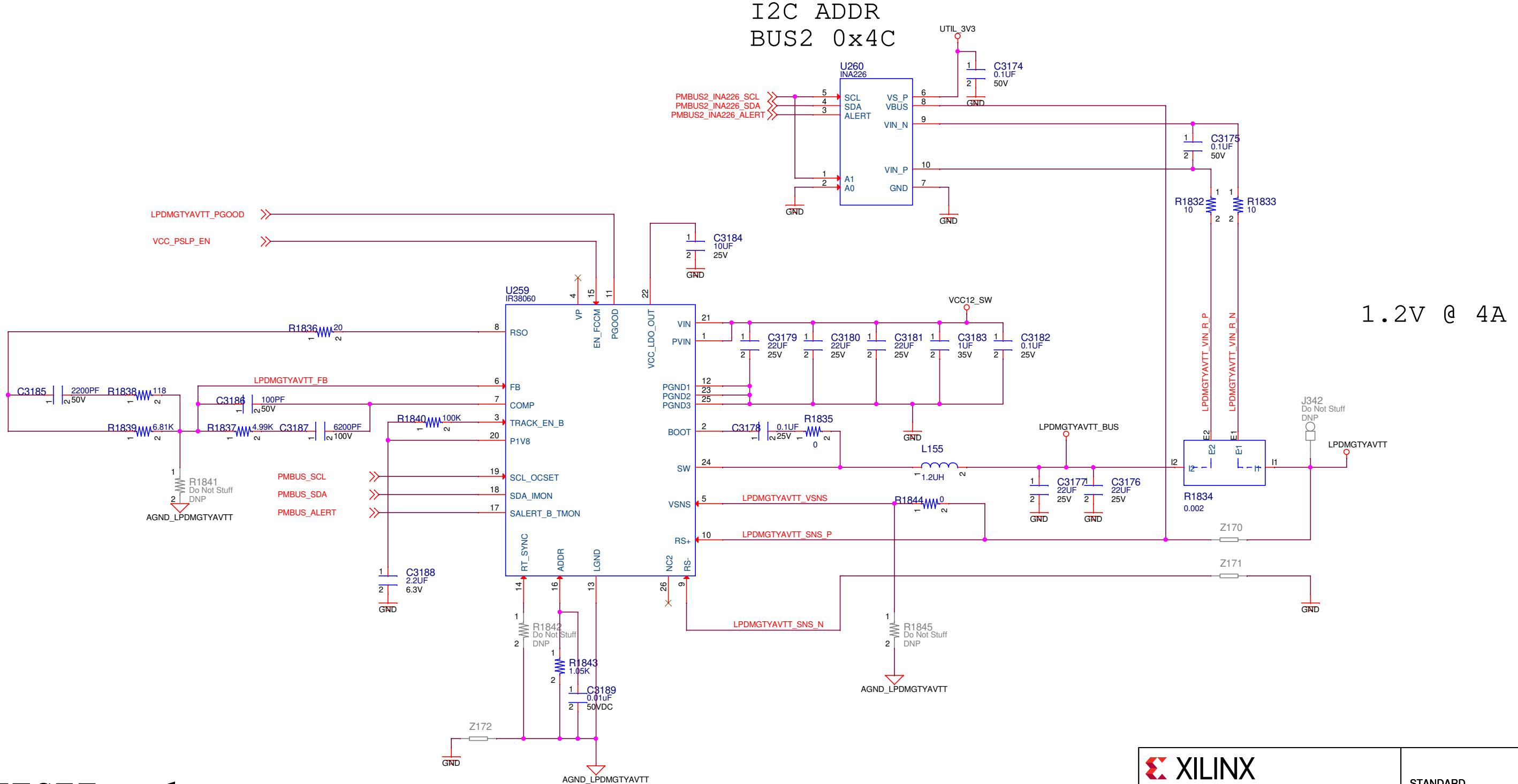
2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: VPK120
059 Current Sense 2

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LPDMGTYAVTT REGULATOR



OFFSET: +1

7-BIT I₂C ADDR: 0x11

7-BIT PMBUS ADDR: 0x41

XILINX
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San Jose, CA 95124

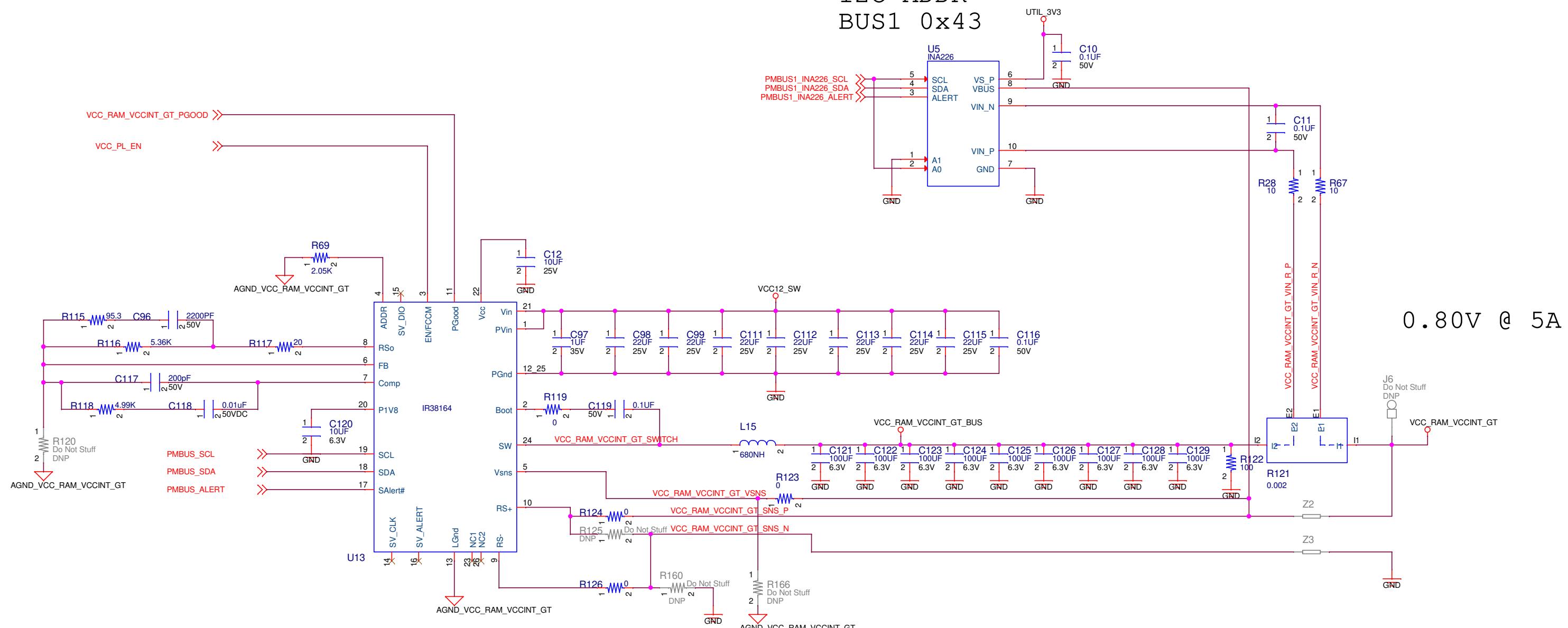
STANDARD

Title: VPK120
060 LPDMGTYAVTT Regulator

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VCC_RAM_VCCINT_GT REGULATOR

I2C ADDR
BUS1 0x43



OFFSET: +3

7-BIT I2C ADDR: 0x13

7-BIT PMBUS ADDR: 0x43

XILINX

2100 Logic Drive
San Jose, CA 95124

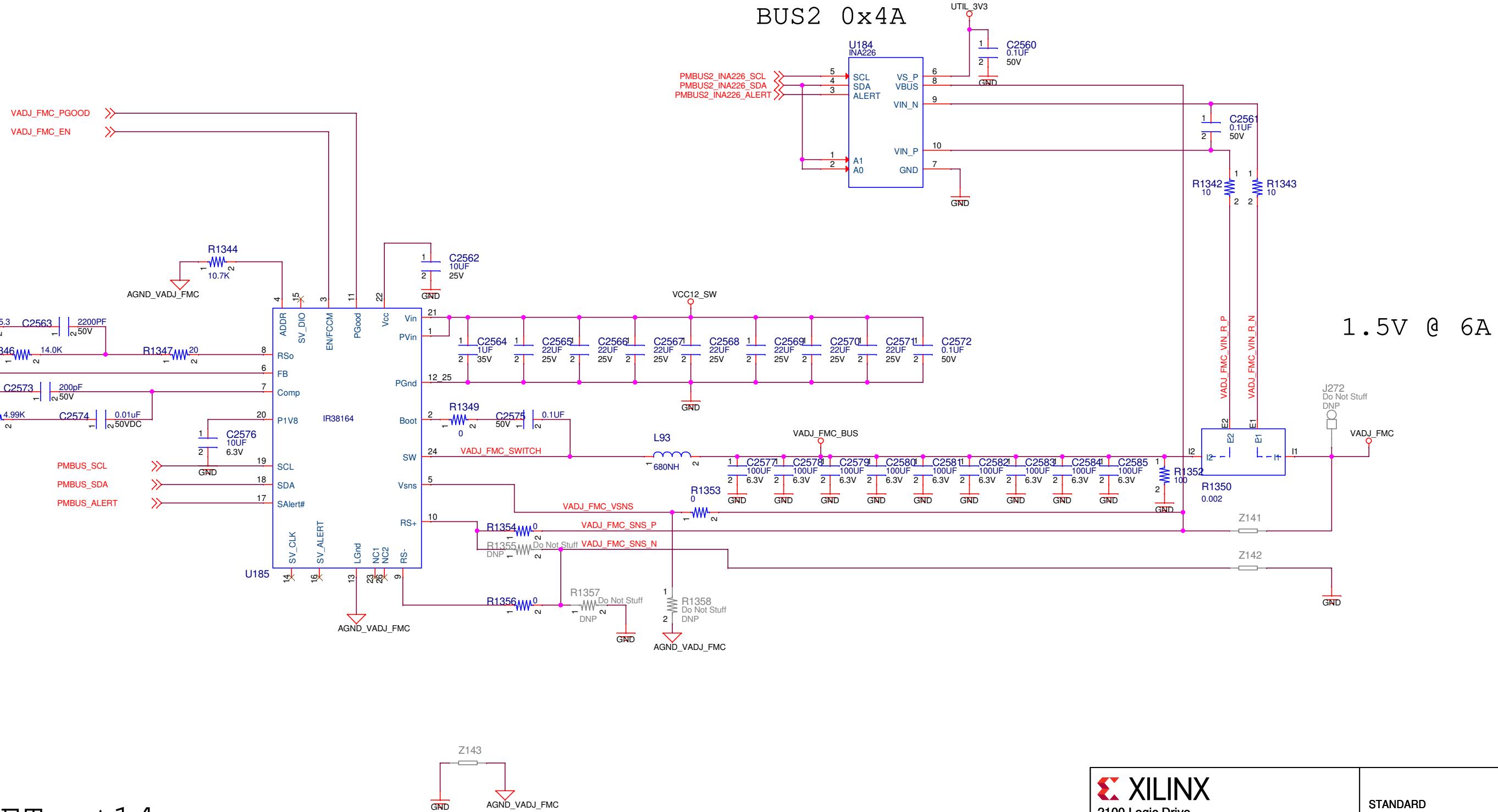
STANDARD

Title: VPK120
061 VCC_RAM_VCCINT_GT Regulator

Size: B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01	Rev: B02
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VADJ_FMC REGULATOR

I2C ADDR
BUS2 0x4A



OFFSET: +14

7-BIT I2C ADDR: 0x1E

7-BIT PMBUS ADDR: 0x4E



XENON
2100 Logic Drive
San Jose, CA 95124

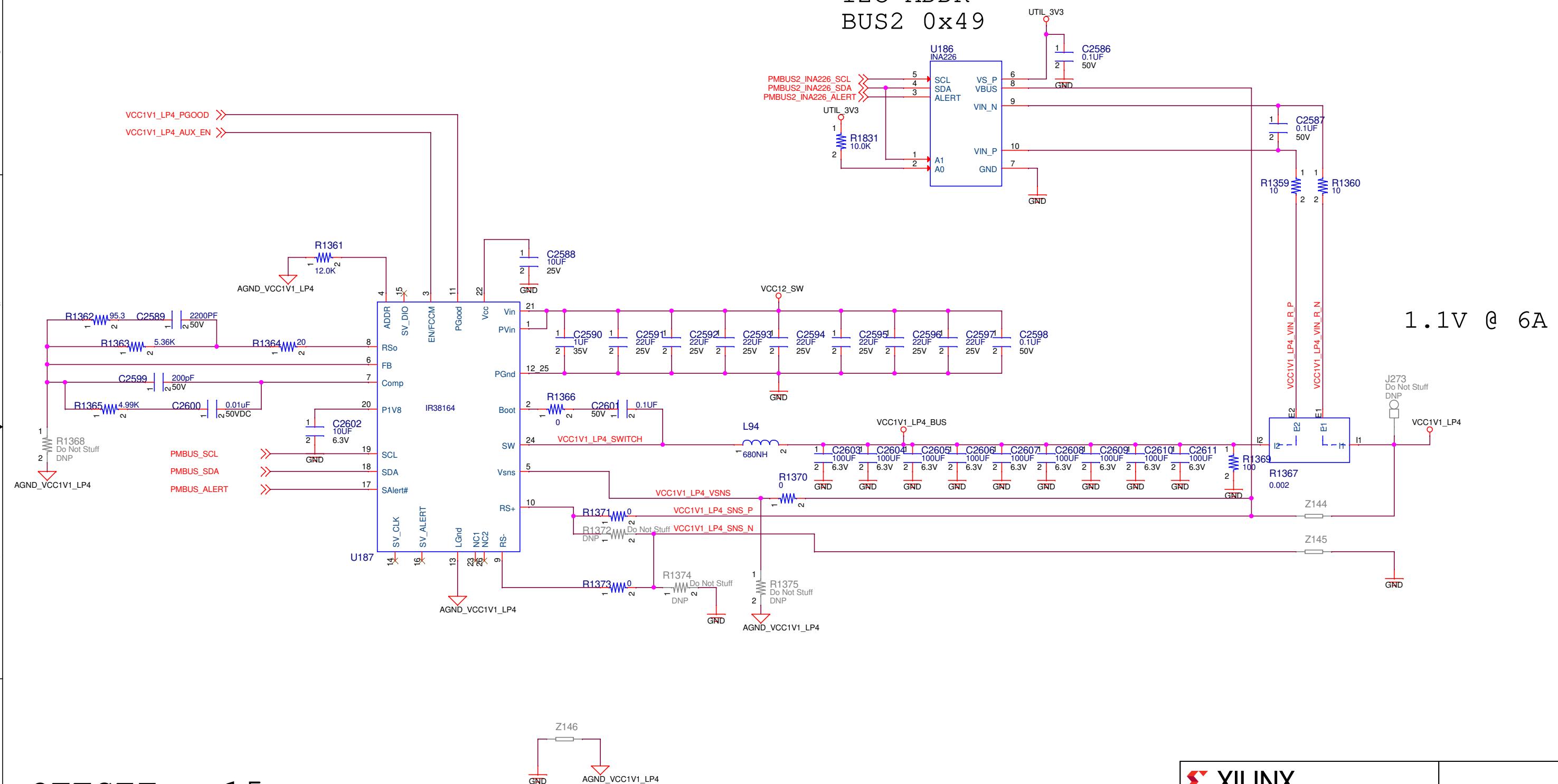
STANDARD

Title: VPK120
062 VADJ_FMC Regulator

Size	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01	Rev B02
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VCC1V1_LP4 REGULATOR

I2C ADDR
BUS2 0x49

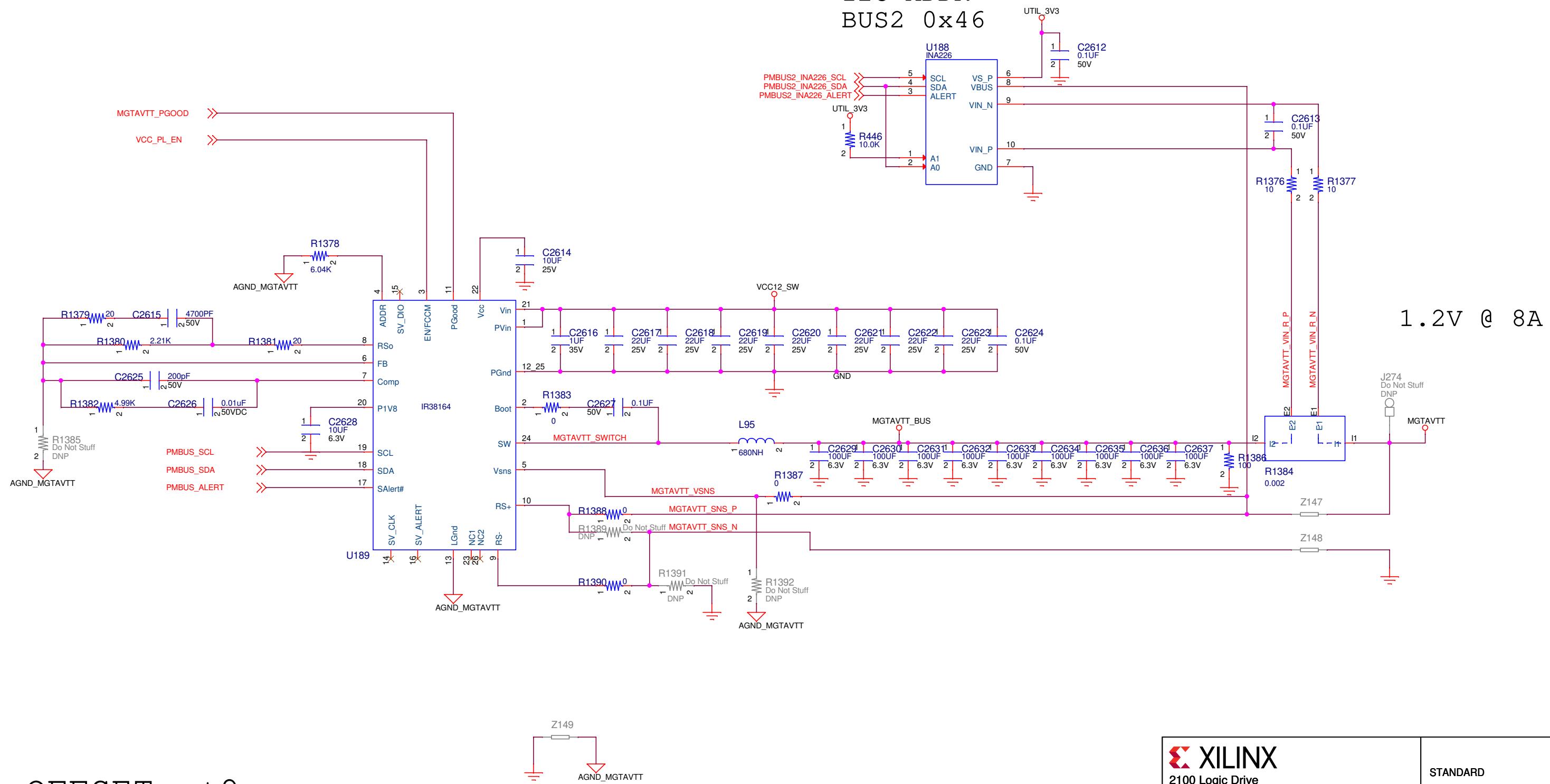


OFFSET: +15
7-BIT I2C ADDR: 0x1F
7-BIT PMBUS ADDR: 0x4F

XILINX 2100 Logic Drive San Jose, CA 95124		STANDARD
Title: VPK120 063 VCC1V1_LP4 Regulator		
Size: B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01 Rev B02
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MGTYAVTT REGULATOR

I2C ADDR
BUS2 0x46



OFFSET: +9

7-BIT I2C ADDR: 0x19

7-BIT PMBUS ADDR: 0x49

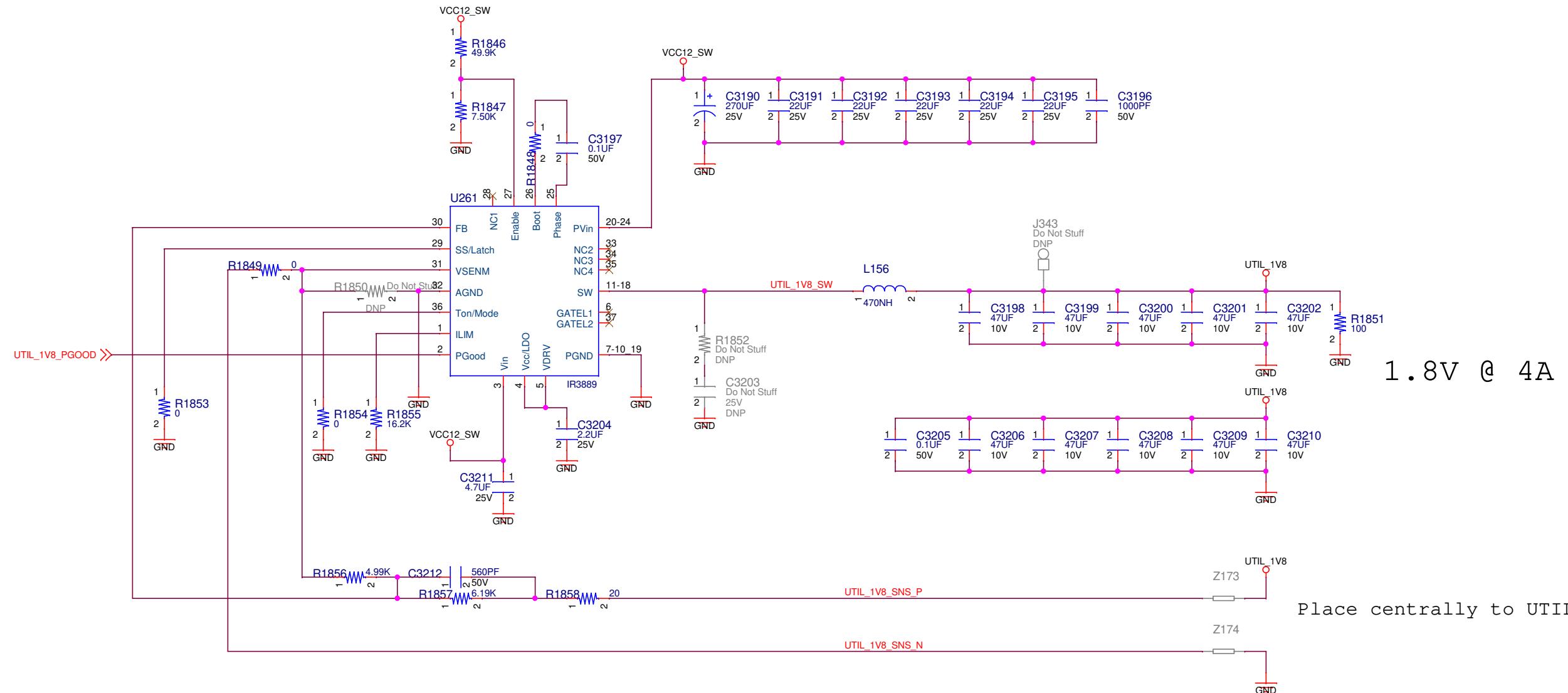
XILINX
2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: VPK120
064 MGTYAVTT Regulator

Size: B	Date Modified: Tuesday, February 08, 2022	Document Number: 038-05072-01	Rev: B02
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UTIL_1V8 REGULATOR

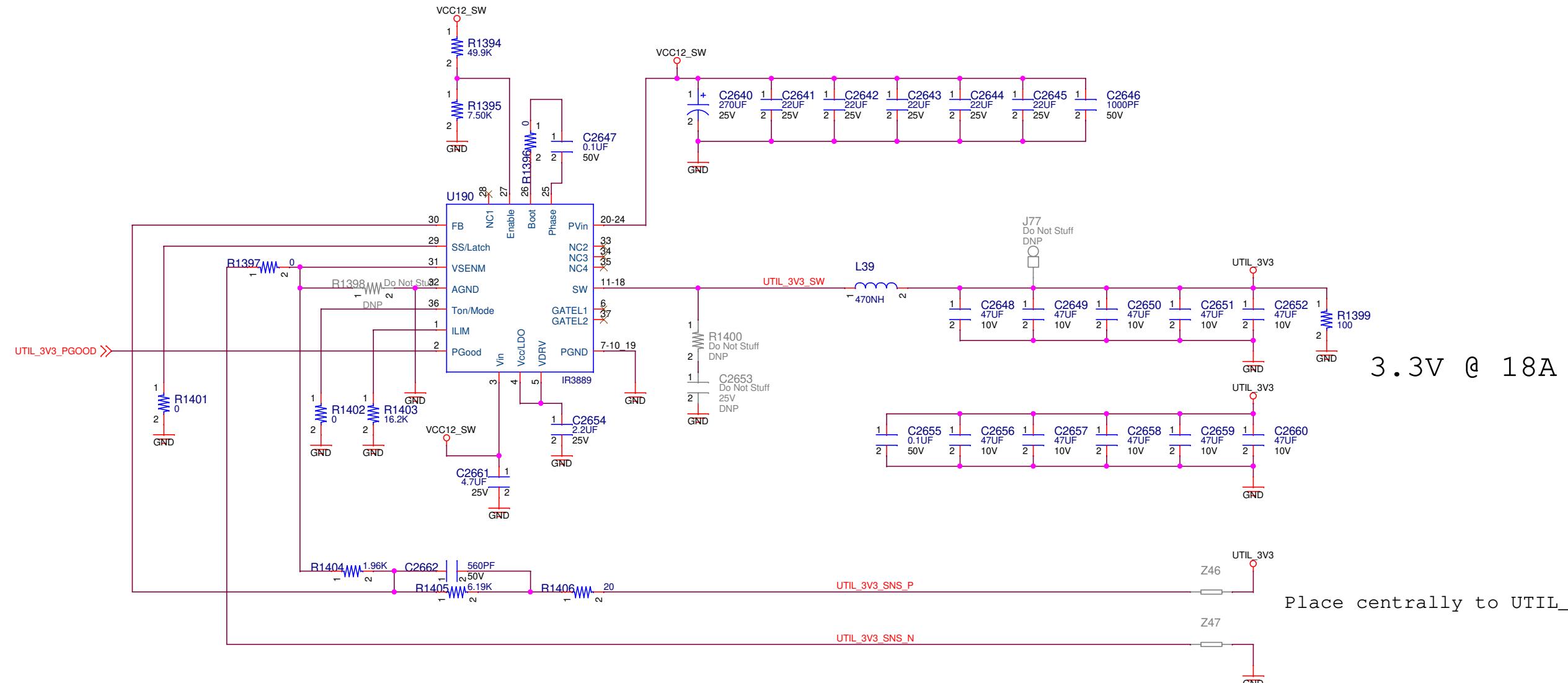


XILINX
2100 Logic Drive
San Jose, CA 95124

Title: VPK120
065 UTIL_1V8 Regulator

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UTIL_3V3 REGULATOR

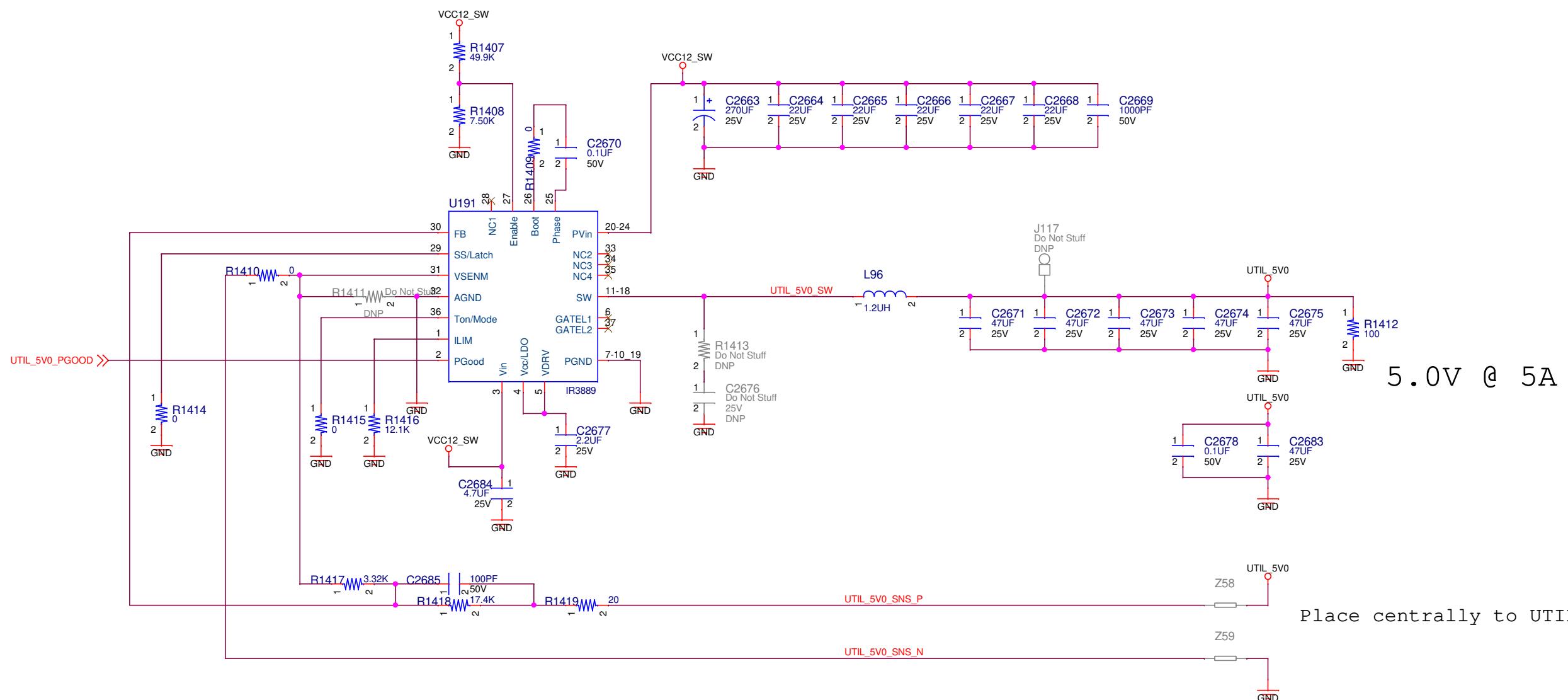


XILINX
2100 Logic Drive
San Jose, CA 95124

Title: VPK120
066 UTIL_3V3 Regulator

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UTIL_5V0 REGULATOR



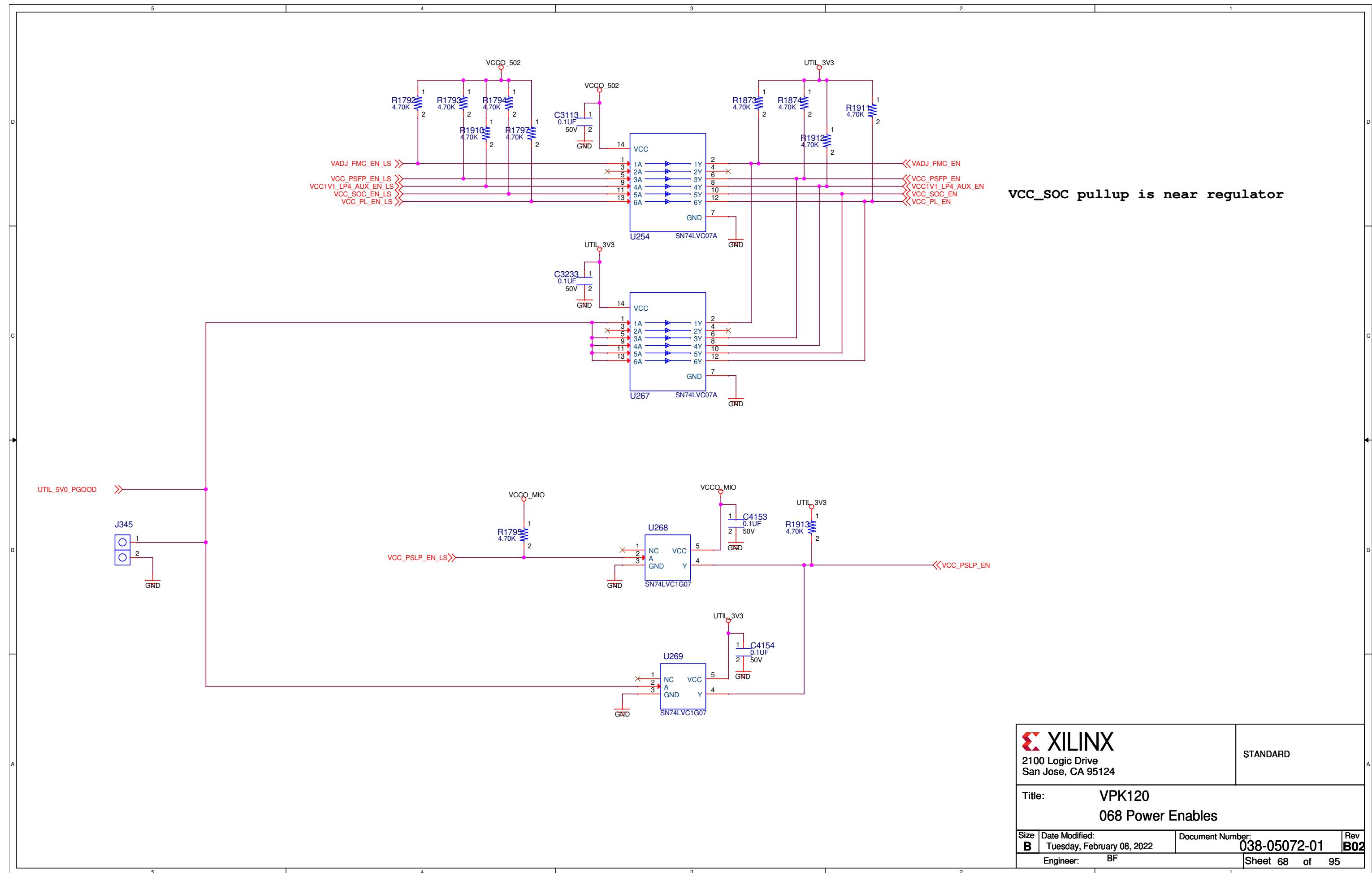
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STANDARD

Title: VPK120
067 UTIL_5V0 Regulator

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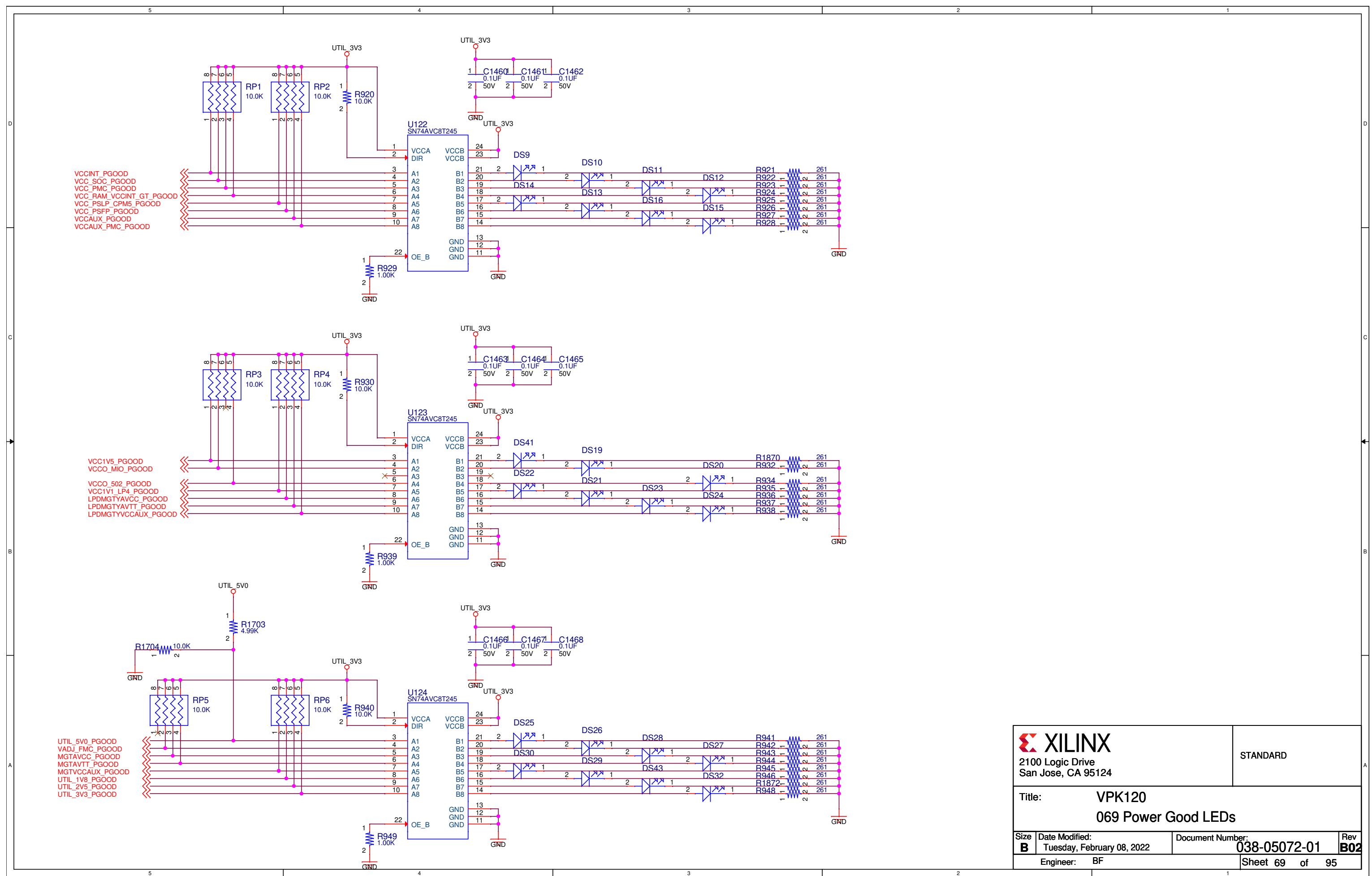
XILINX

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San Jose, CA 95124

STANDARD

Title: VPK120
068 Power Enables

Size	Date Modified:	Document Number:	Rev
B	Tuesday, February 08, 2022	038-05072-01	B02
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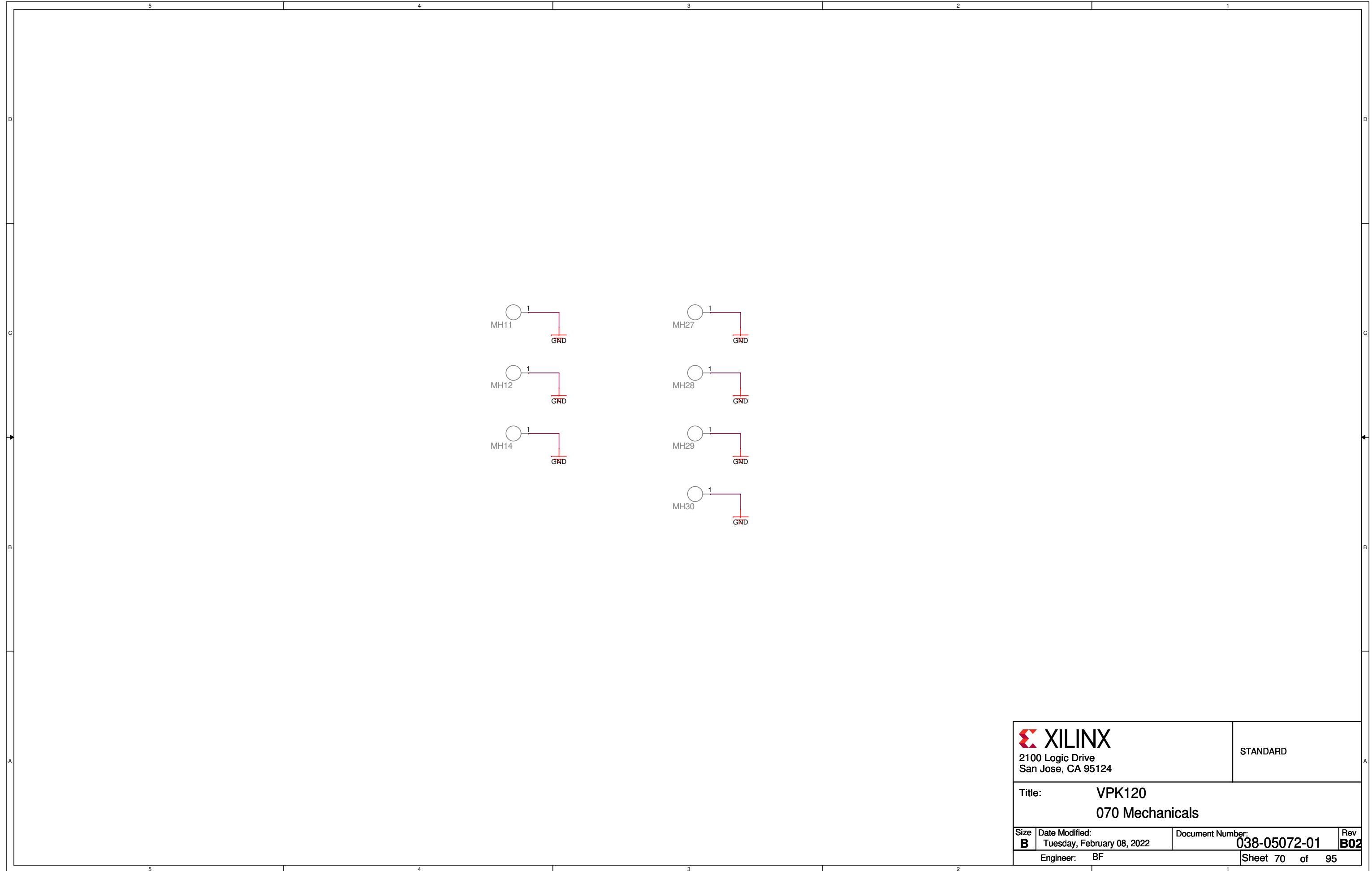
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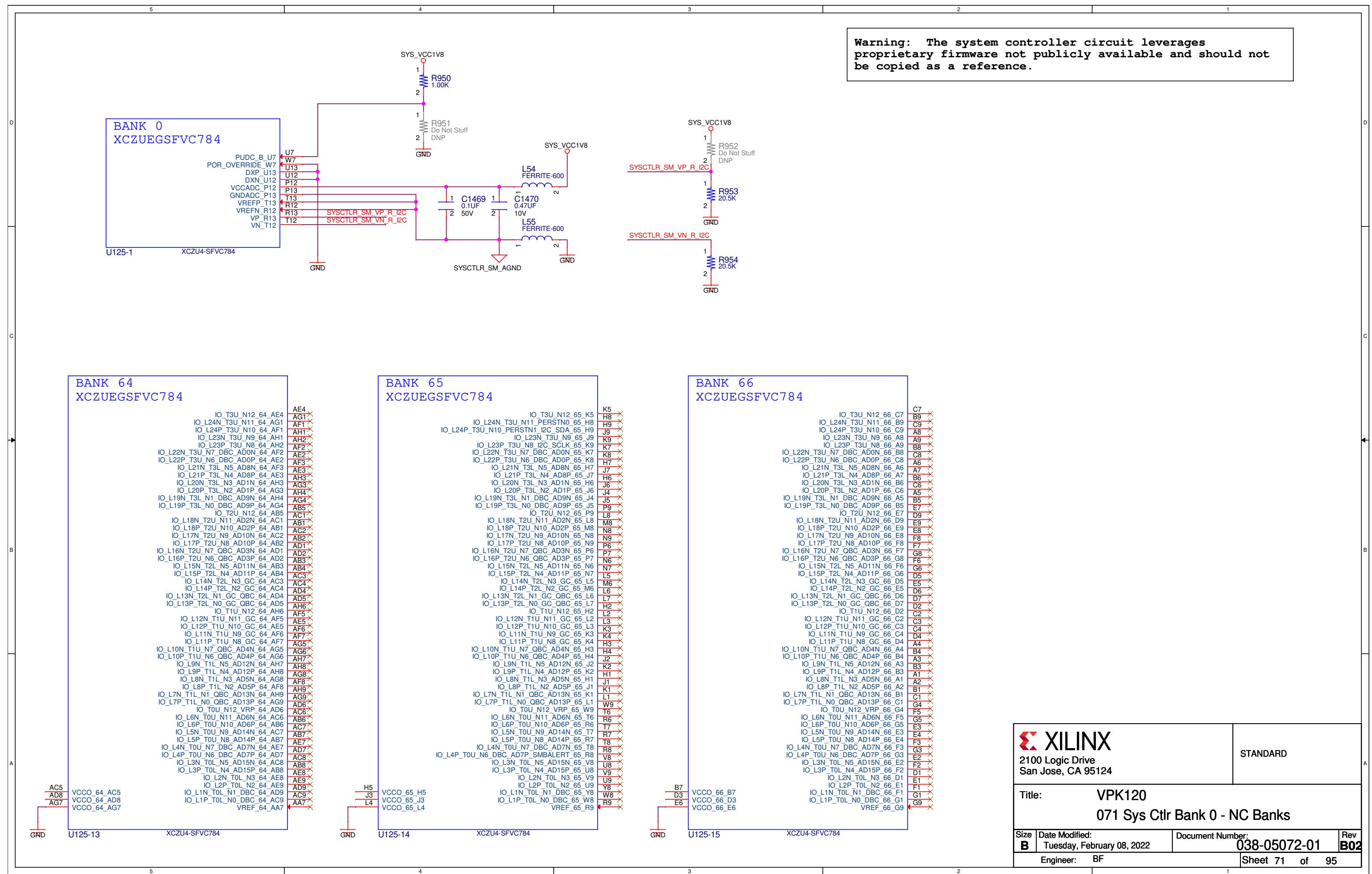
STANDARD

Title: VPK120
069 Power Good LEDs

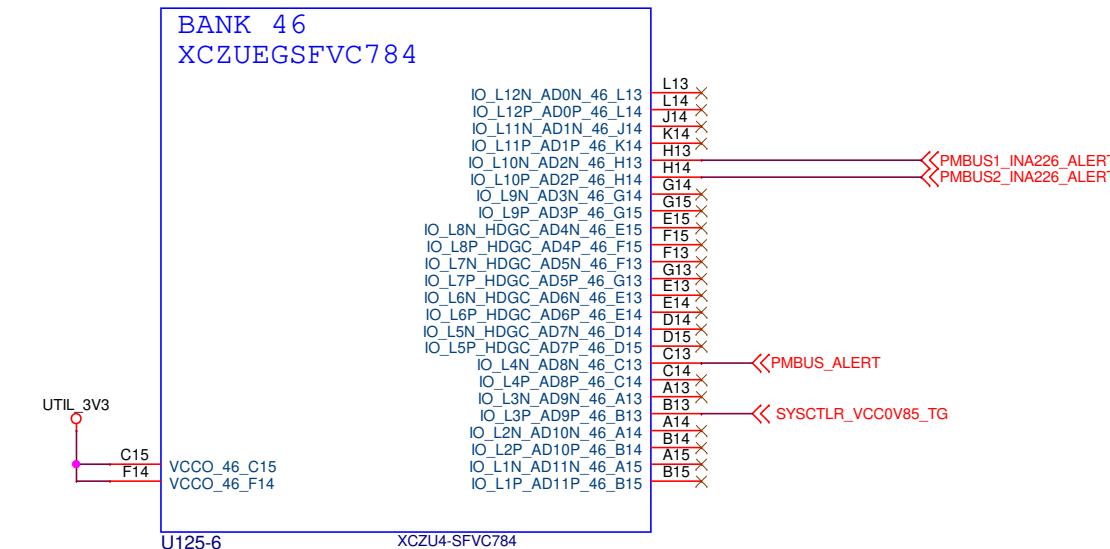
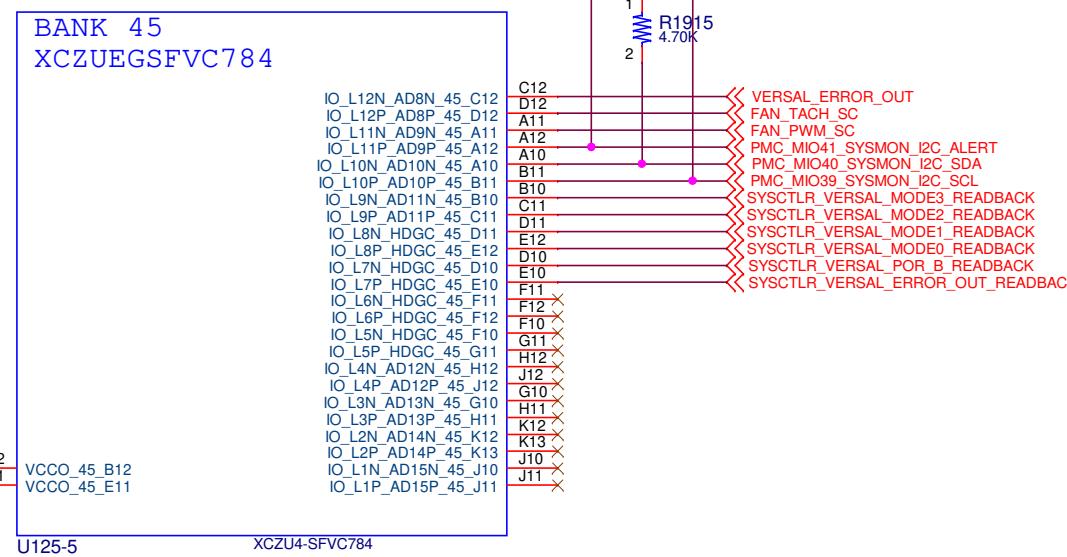
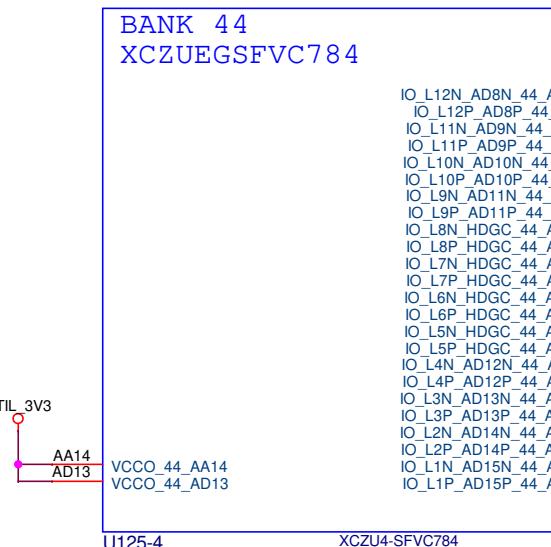
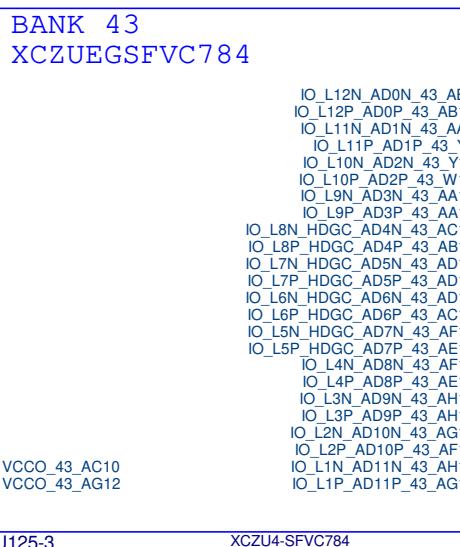
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	2100 Logic Drive San Jose, CA 95124	
Title:	VPK120	
	070 Mechanicals	
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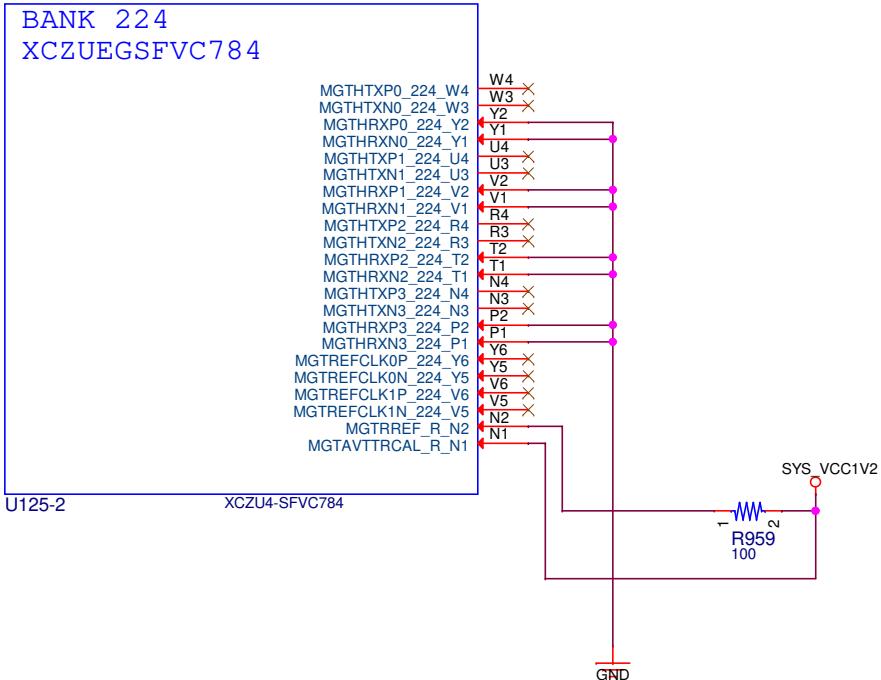
2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: **VPK120**
072 Sys Ctlr Banks 43-46

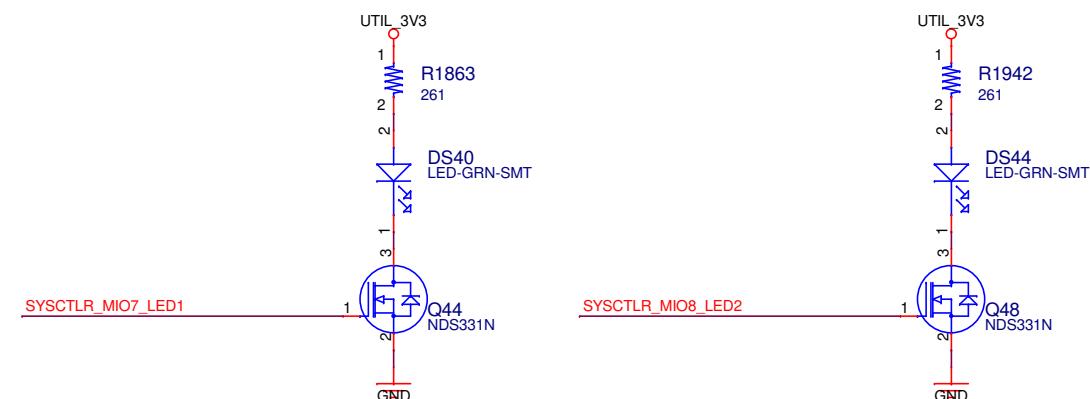
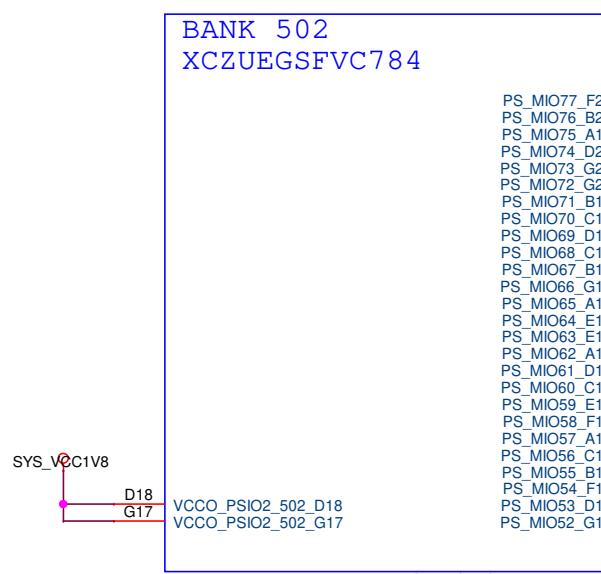
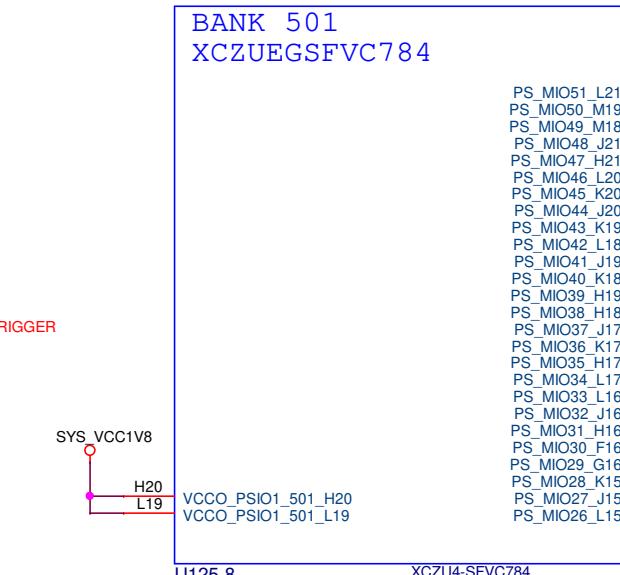
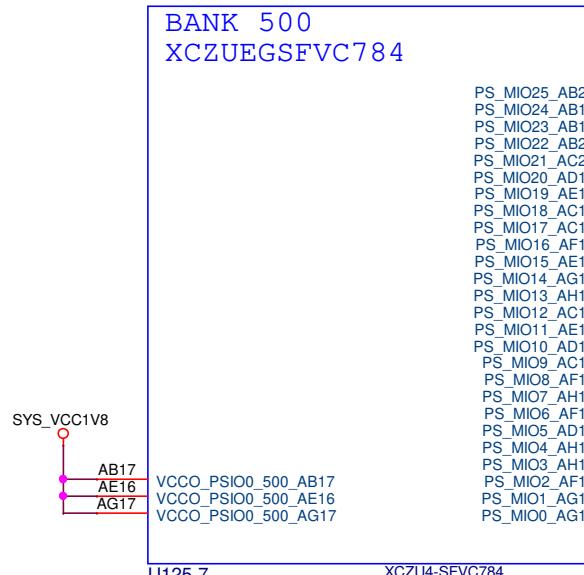
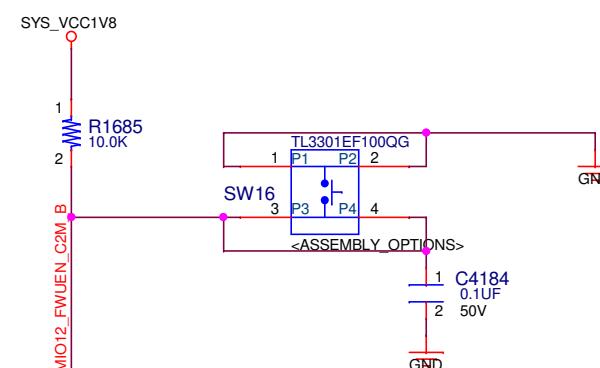
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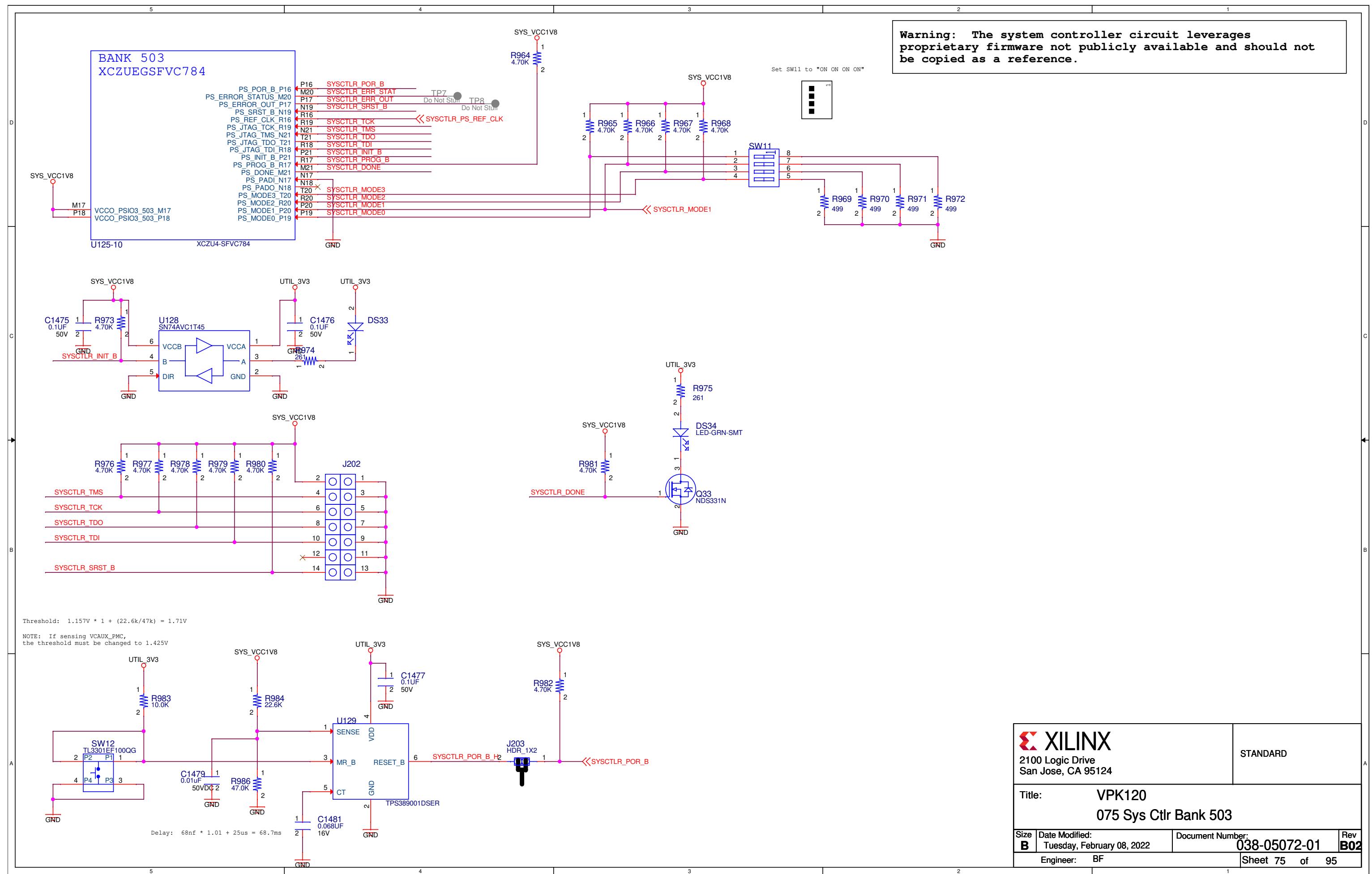


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2100 Logic Drive San Jose, CA 95124		
Title: VPK120		
073 Sys Ctlr Bank 224		
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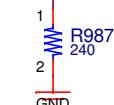
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2100 Logic Drive San Jose, CA 95124		
Title: VPK120 074 Sys Ctlr Banks 500 501 502		
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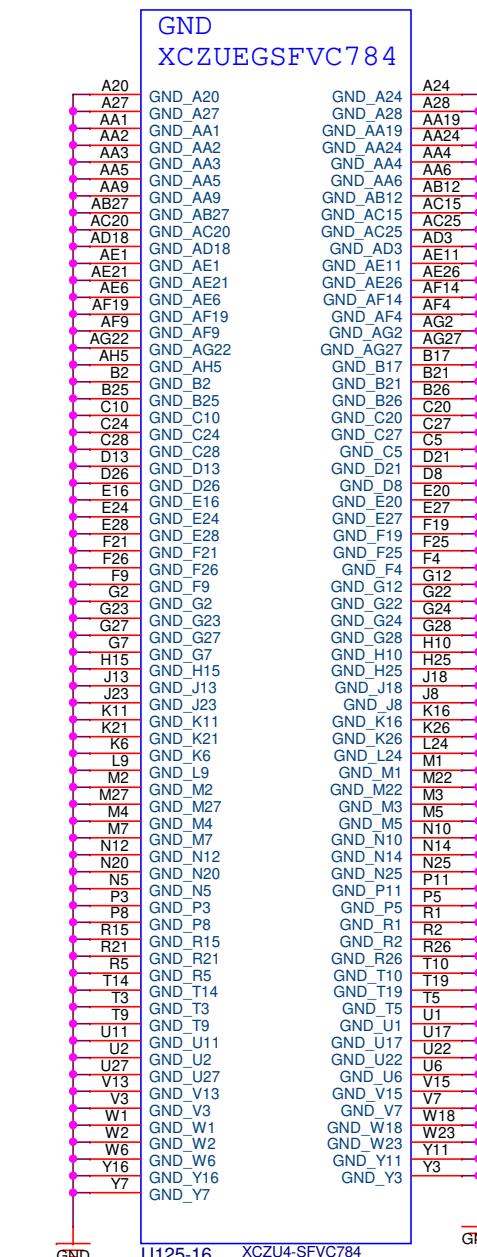
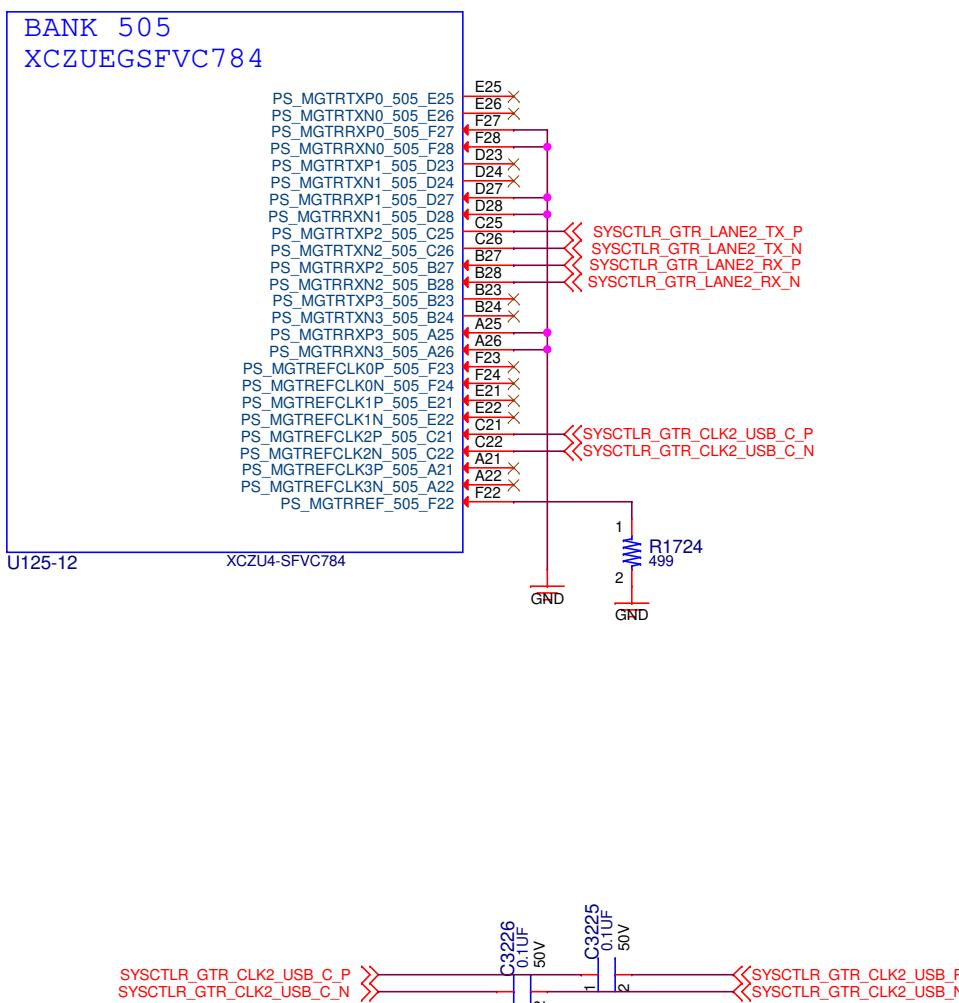
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Title: VPK120
076 Sys Ctlr Bank 504

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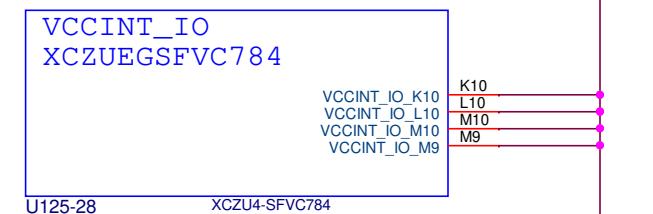
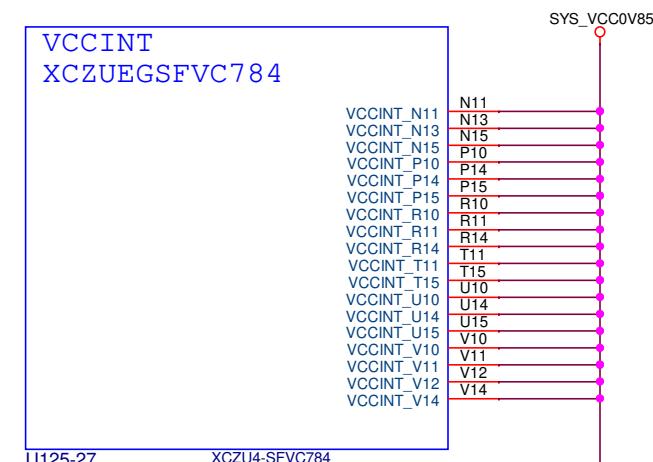
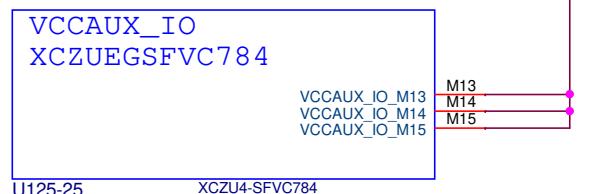
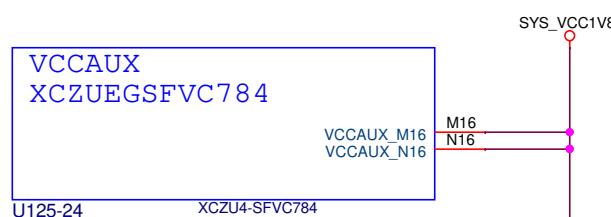
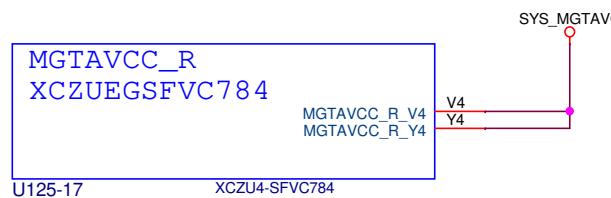
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Title: VPK120
077 Sys Ctlr Bank 505 - GND

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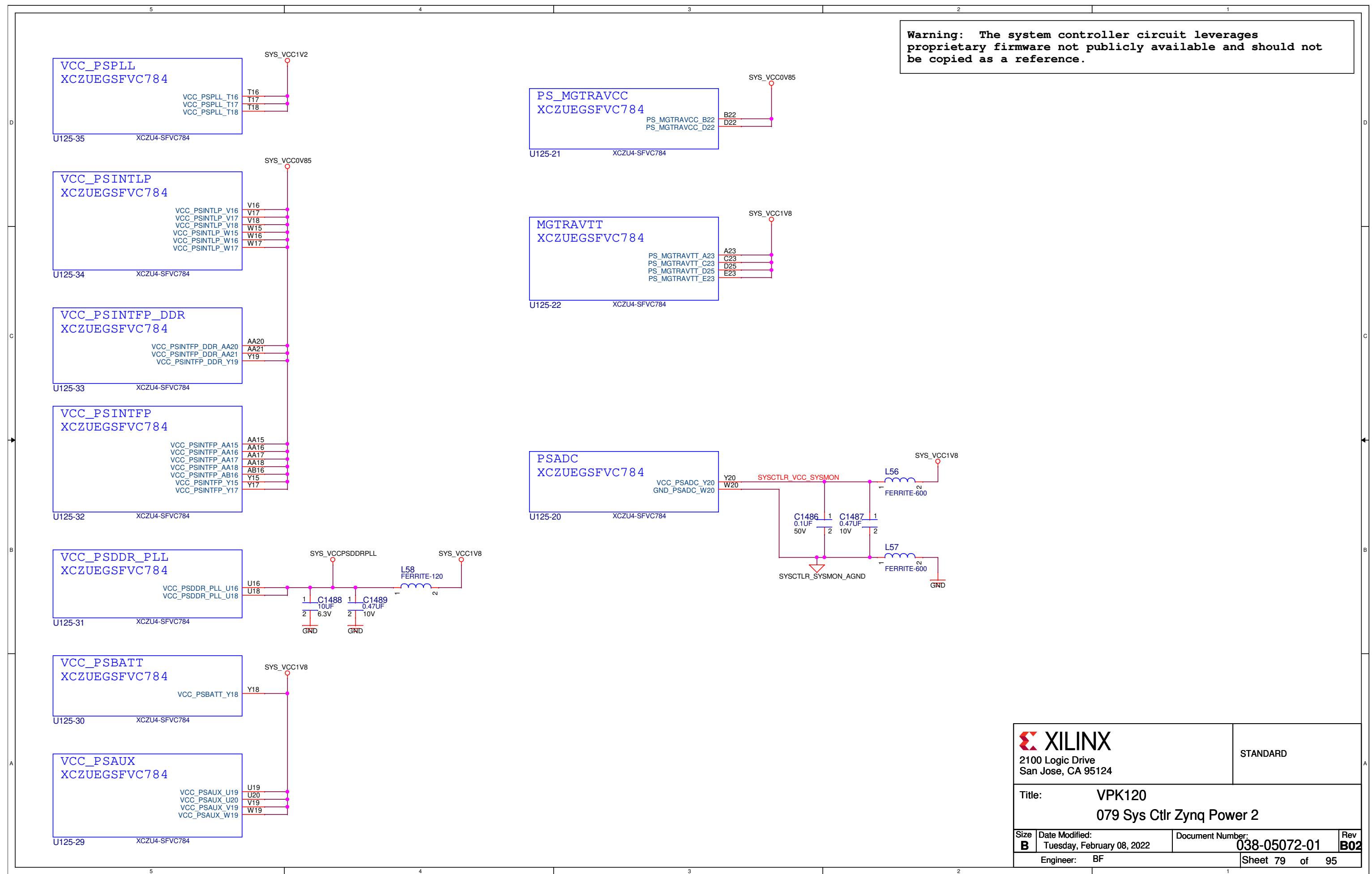


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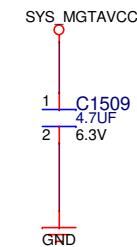
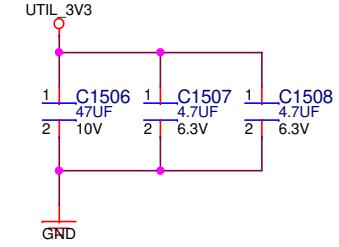
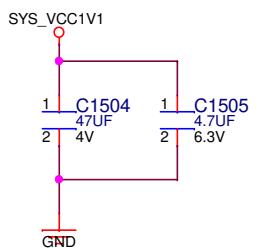
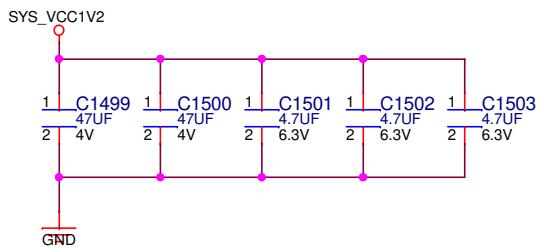
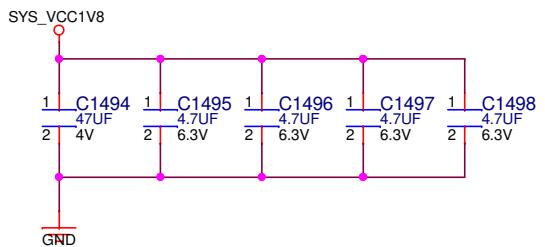
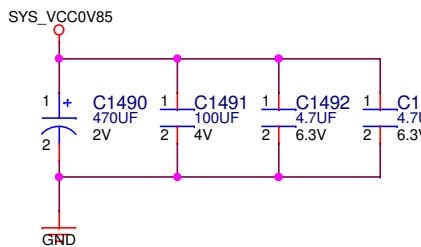
STANDARD

Title: **VPK120**
078 Sys Ctlr Zynq Power 1

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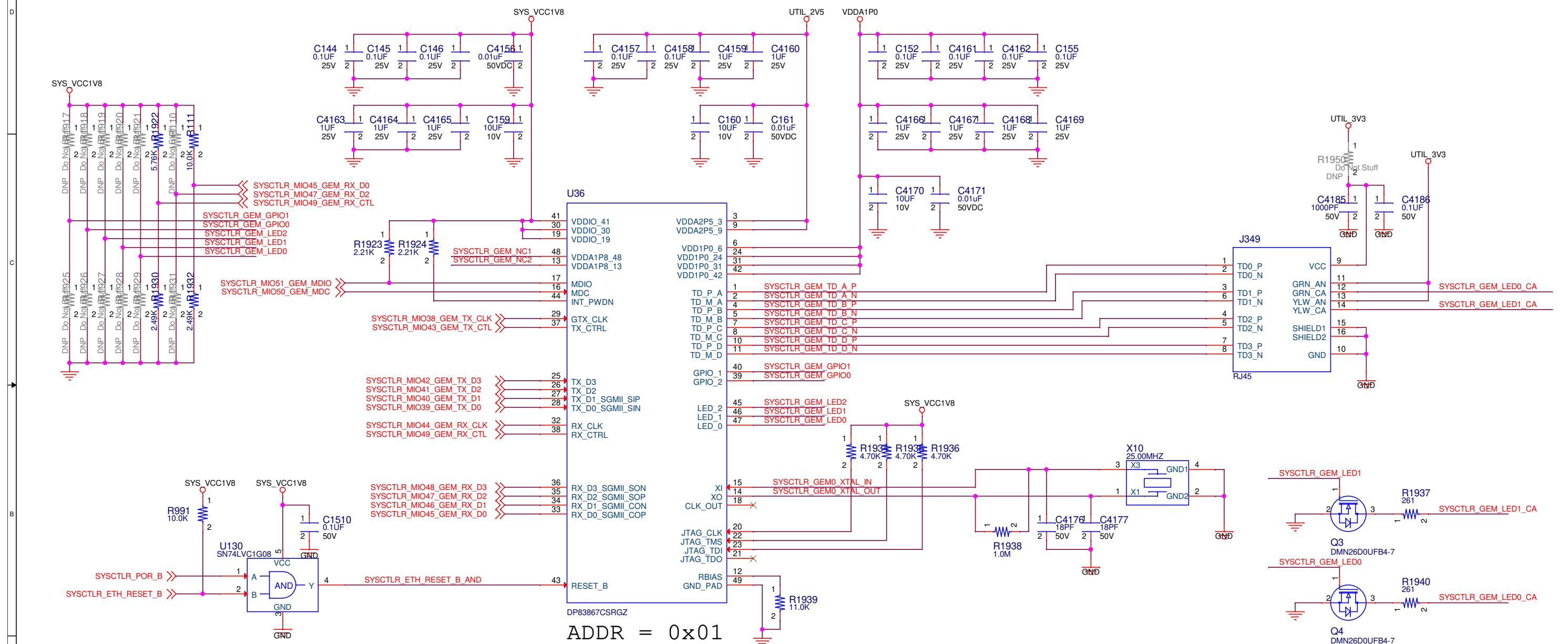
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Title: VPK120
080 Sys Ctrl Zynq Decoupling

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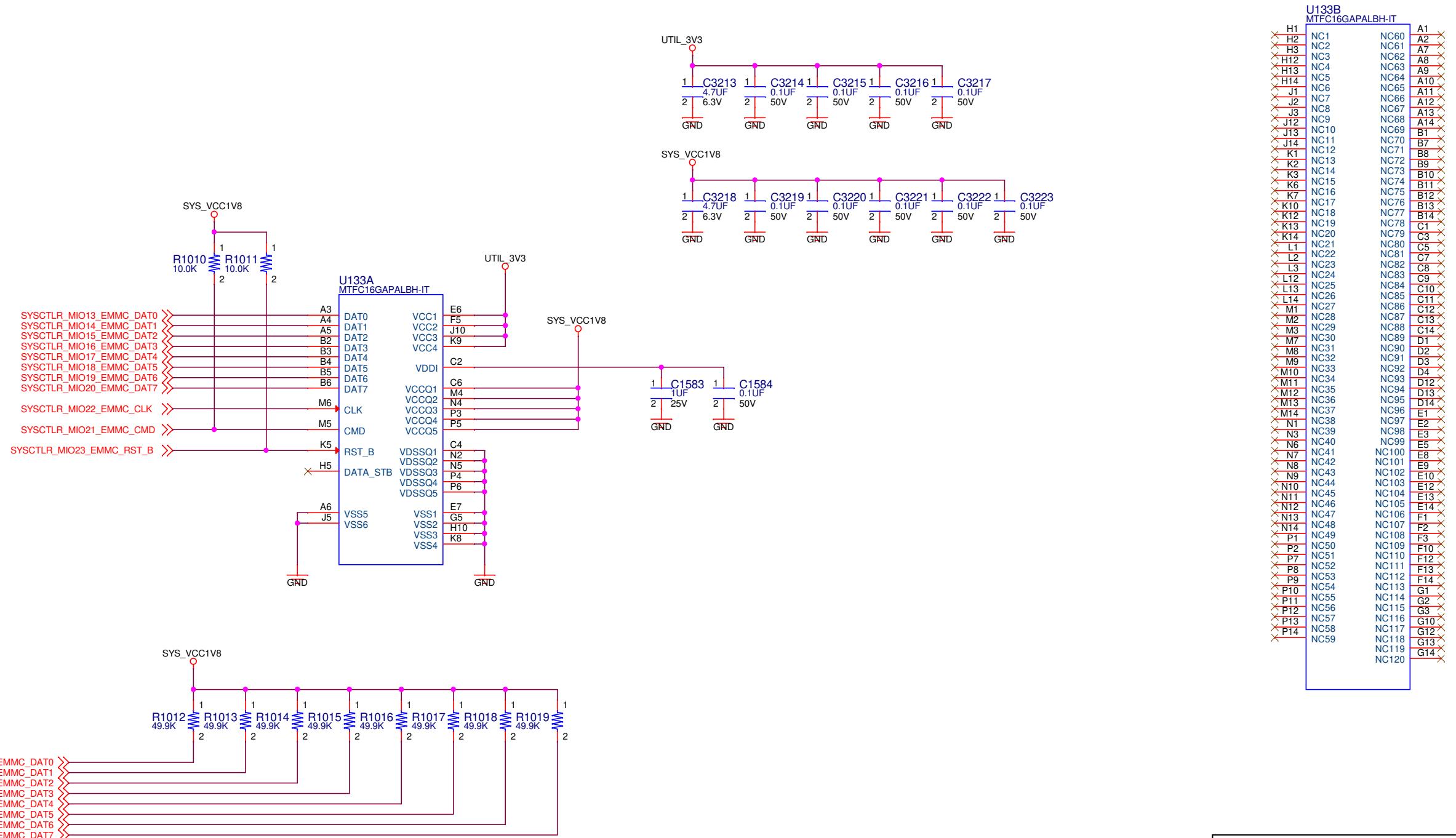
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Title: VPK120
081 Sys Ctrl Ethernet

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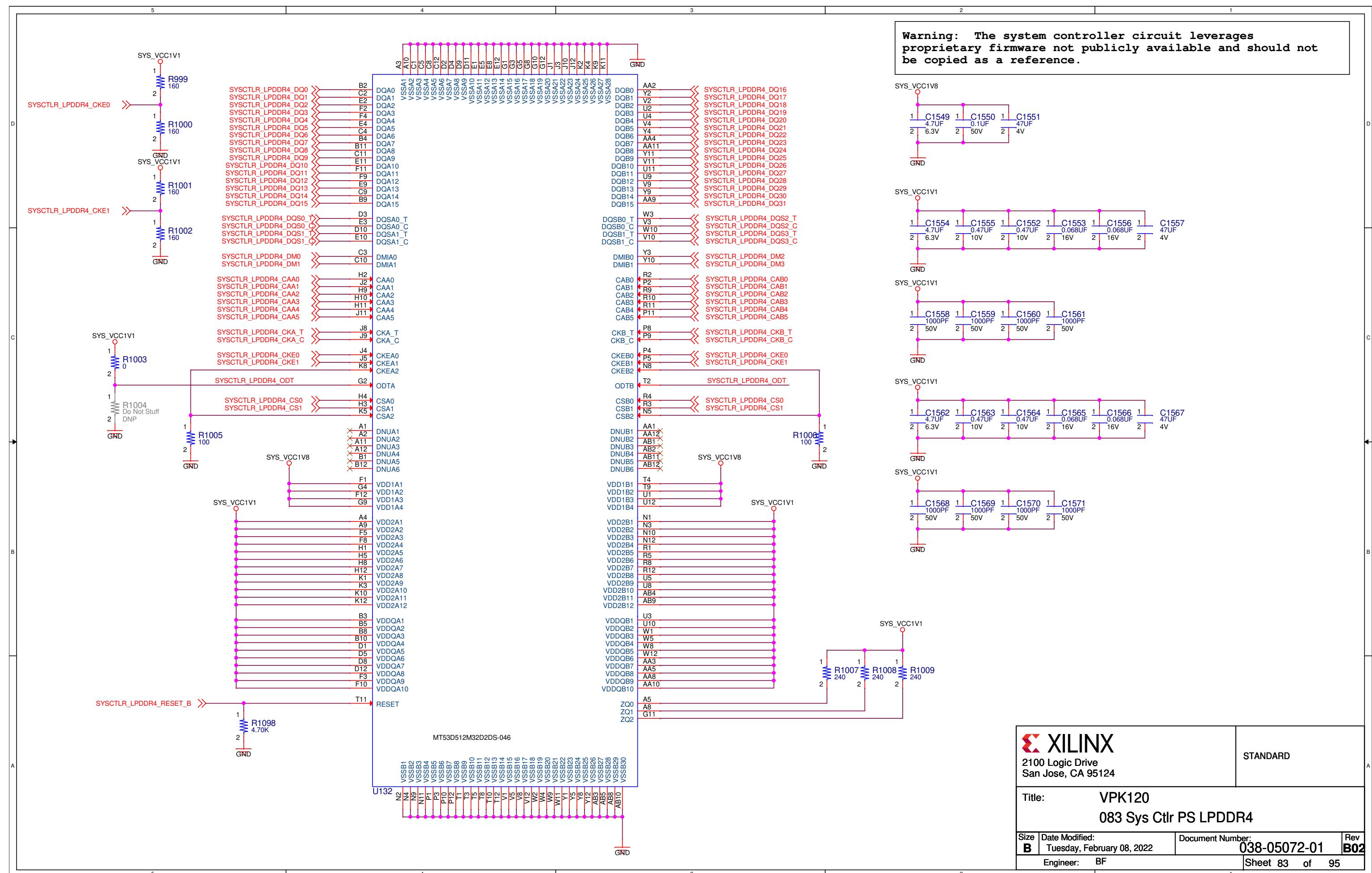


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Title: **VPK120**
082 Sys Ctlr eMMC

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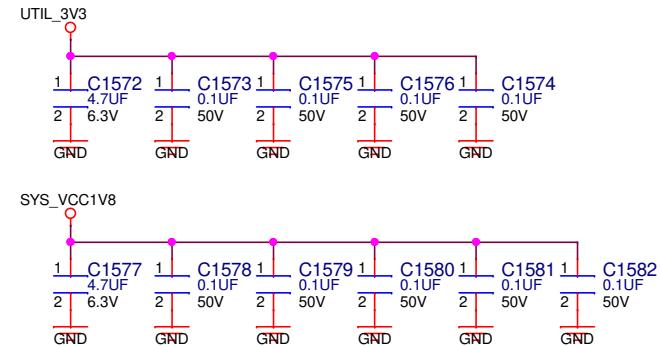
2100 Logic Drive
San Jose, CA 95124

STANDARD

Title: **VPK120**
083 Sys Ctrl PS LPDDR4

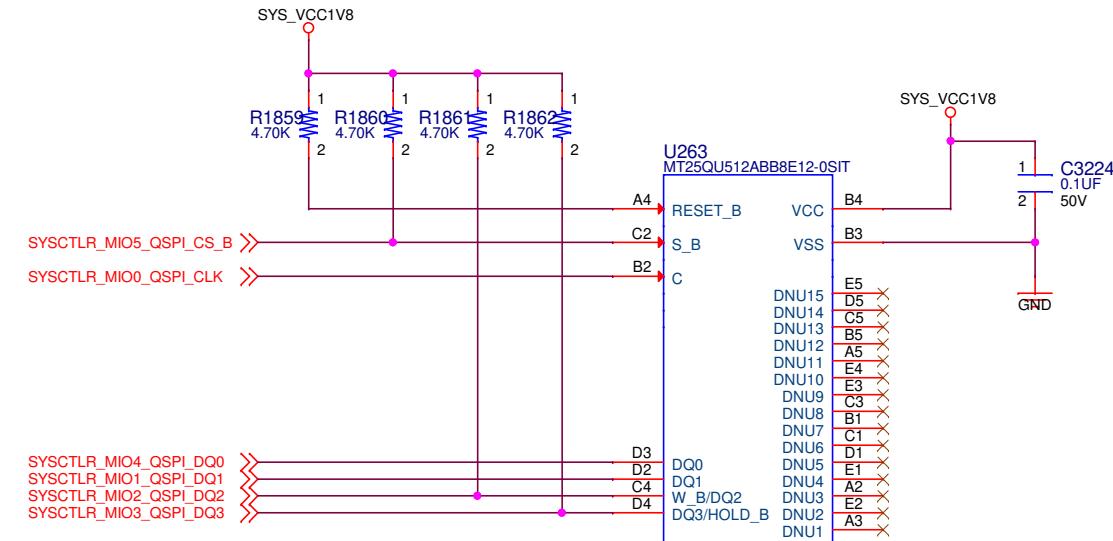
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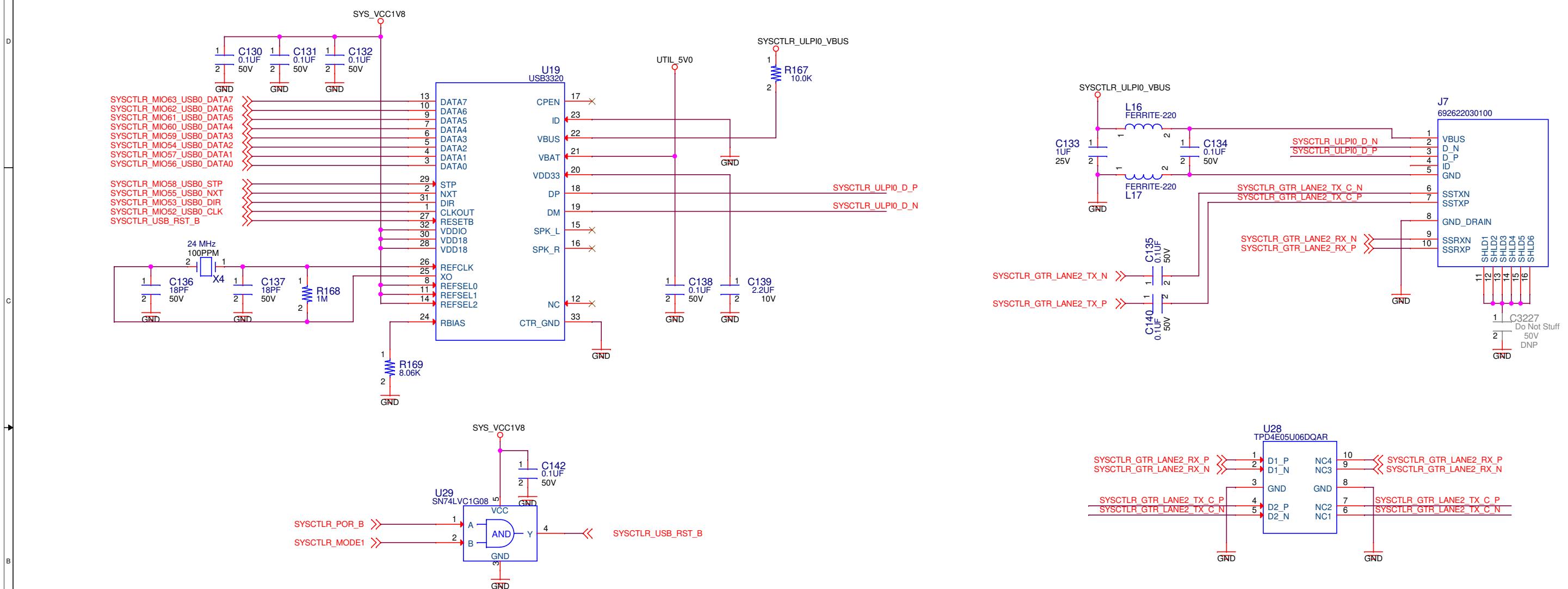
XILINX		STANDARD
2100 Logic Drive San Jose, CA 95124		
Title: VPK120 084 Sys Ctlr Caps		
Size	Date Modified: B Tuesday, February 08, 2022	Document Number: 038-05072-01
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2100 Logic Drive San Jose, CA 95124		
Title: VPK120 085 Sys Ctrl QSPI		
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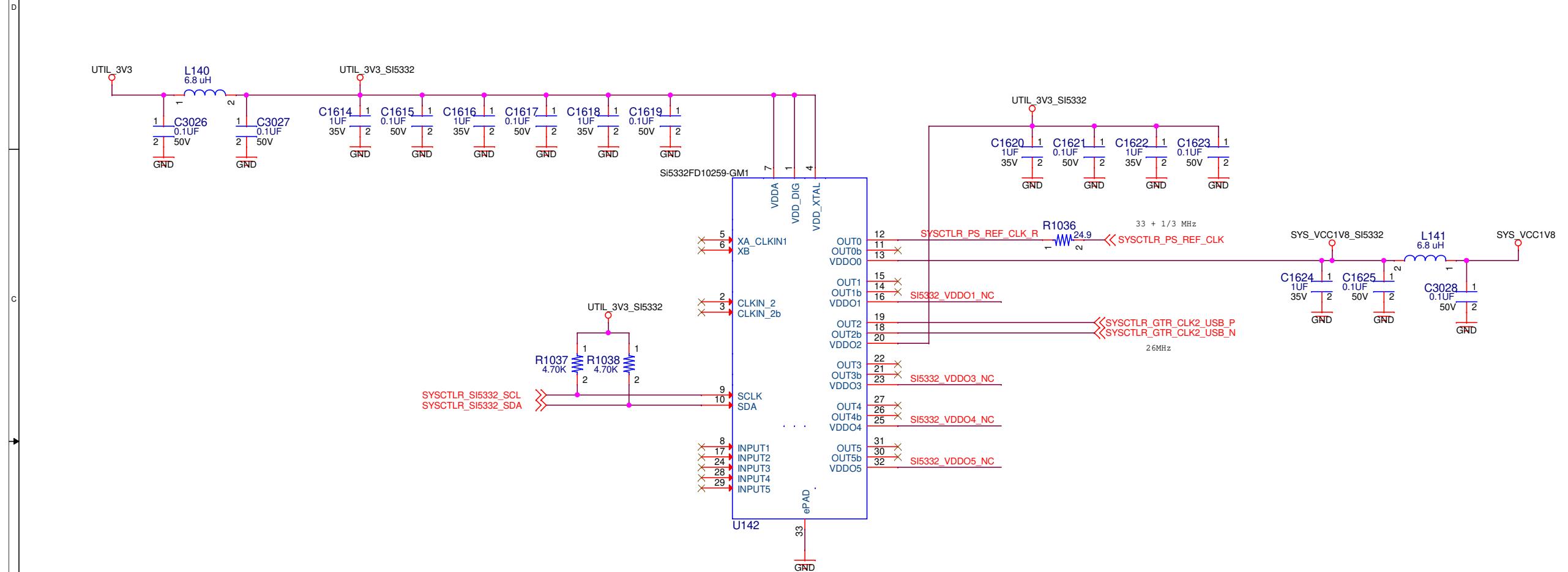
STANDARD

Title: VPK120

086 Sys Ctlr USB3 Upstream

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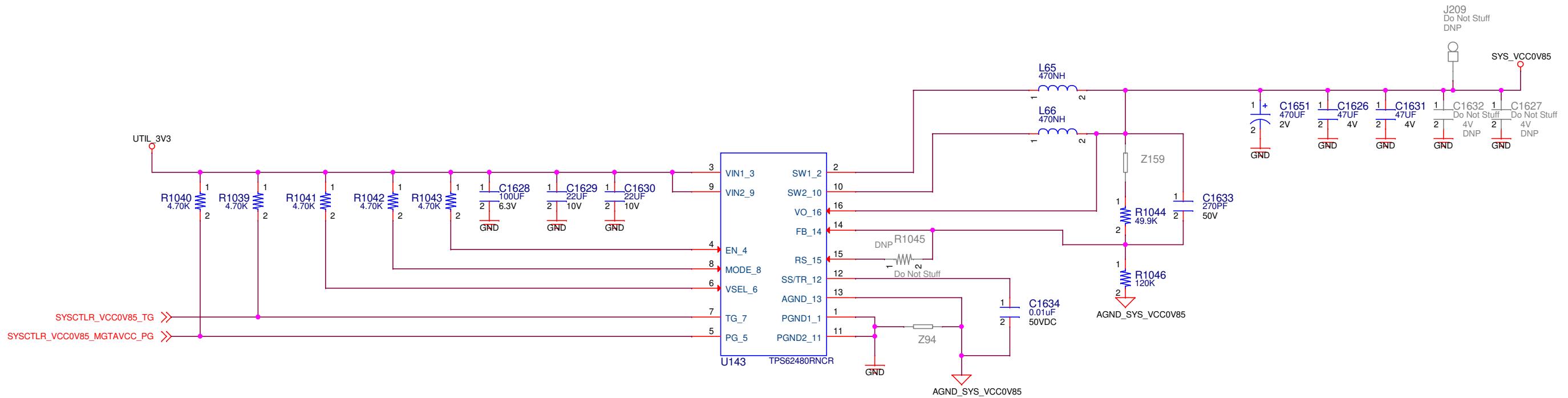
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San Jose, CA 95124

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Title: VPK120
087 Sys Ctlr Clocks

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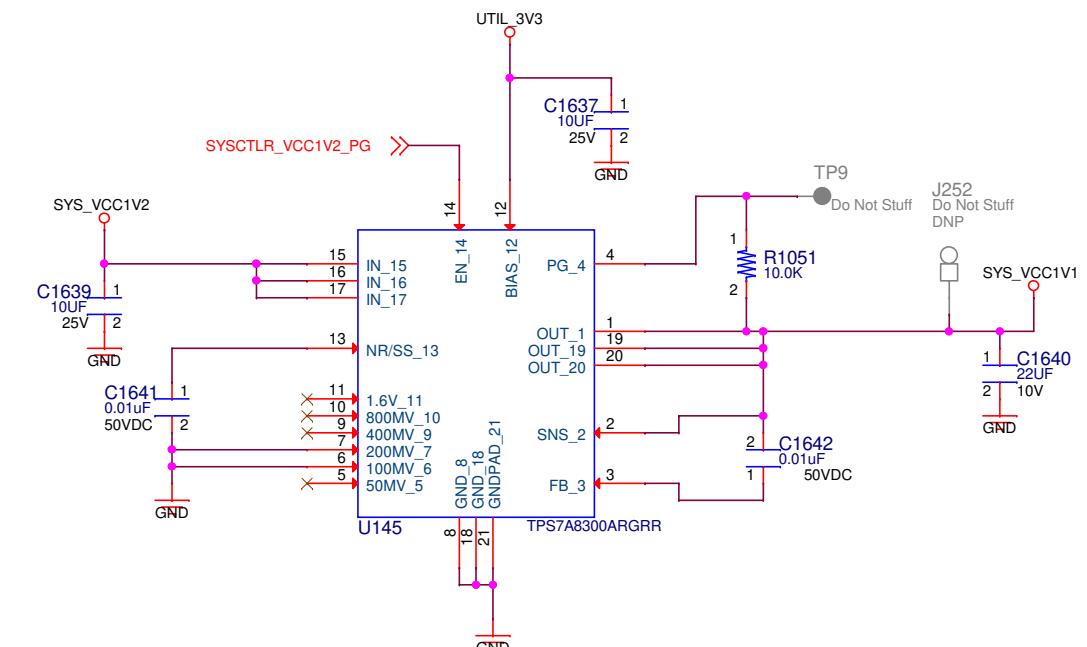
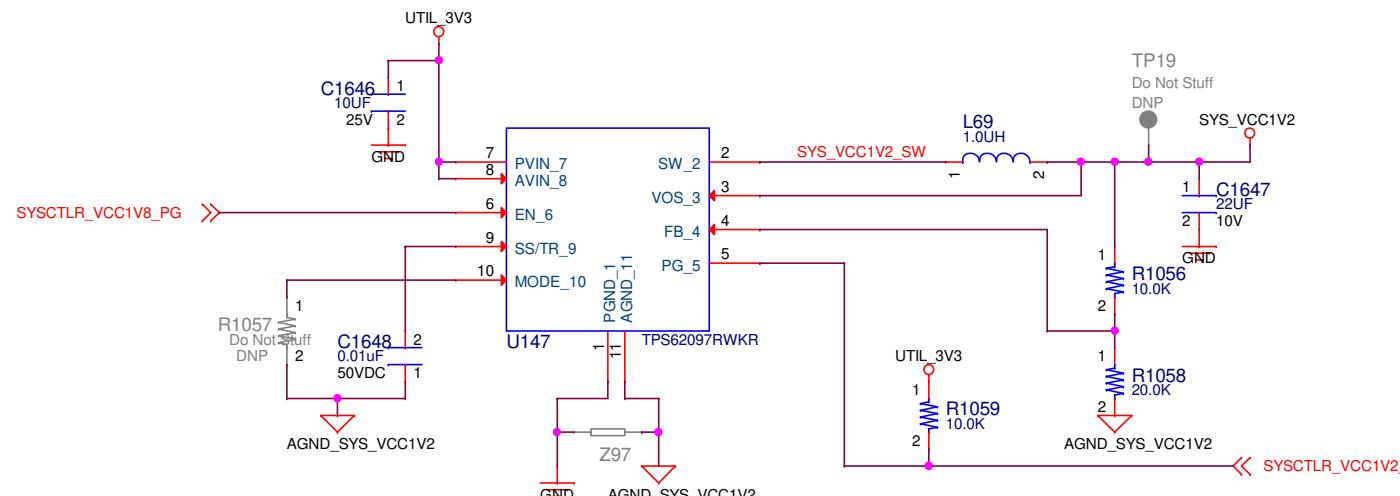
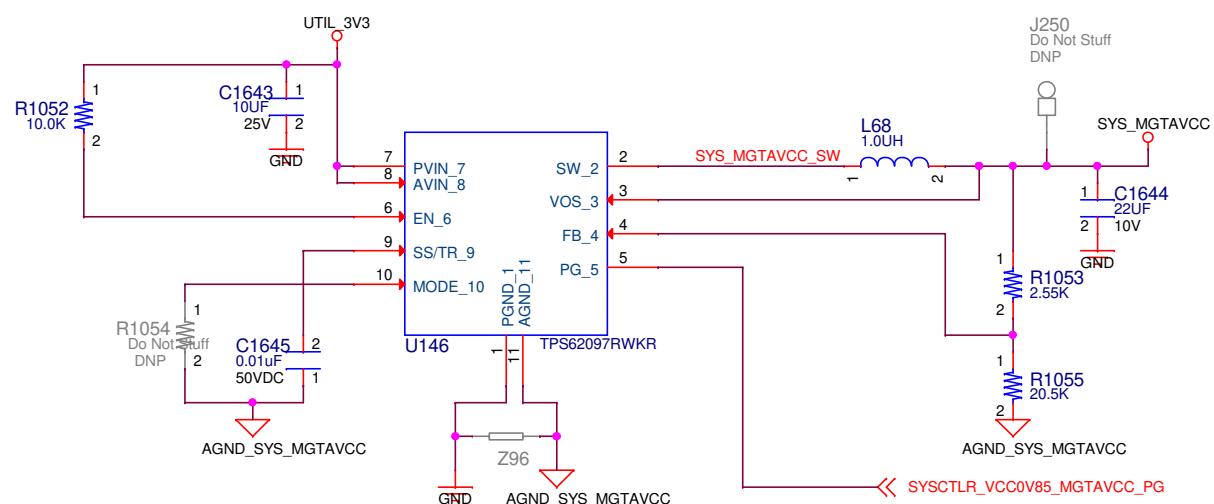
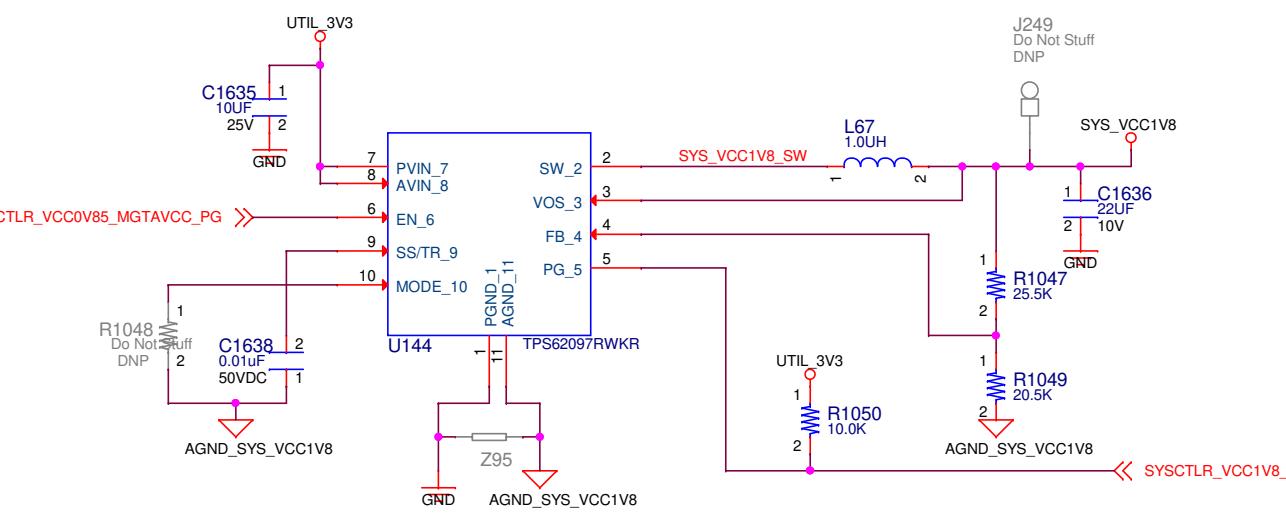
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Title: **VPK120**
088 SYS_VCC0V85 Regulator

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B	Tuesday, February 08, 2022	038-05072-01	B02
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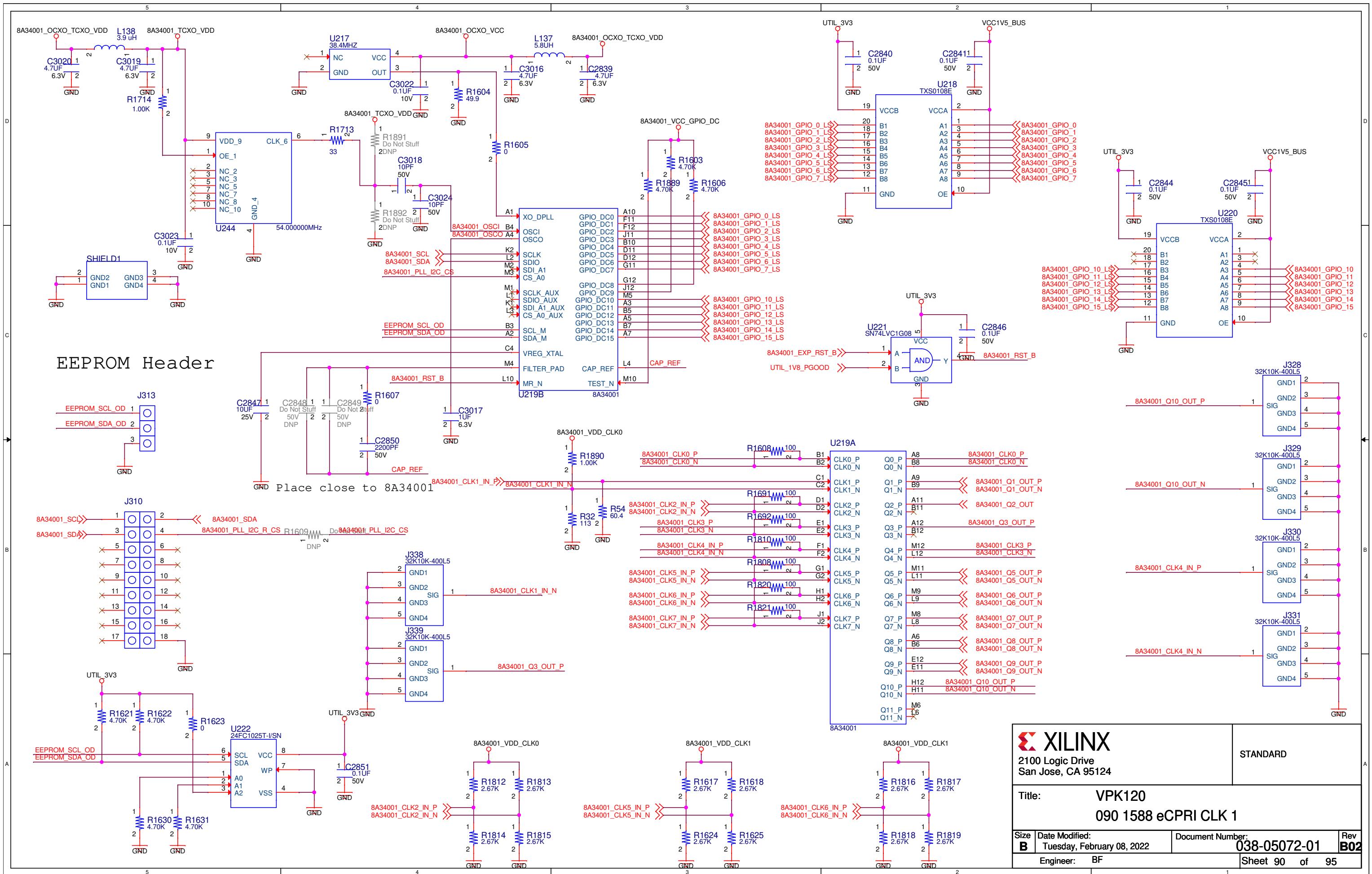
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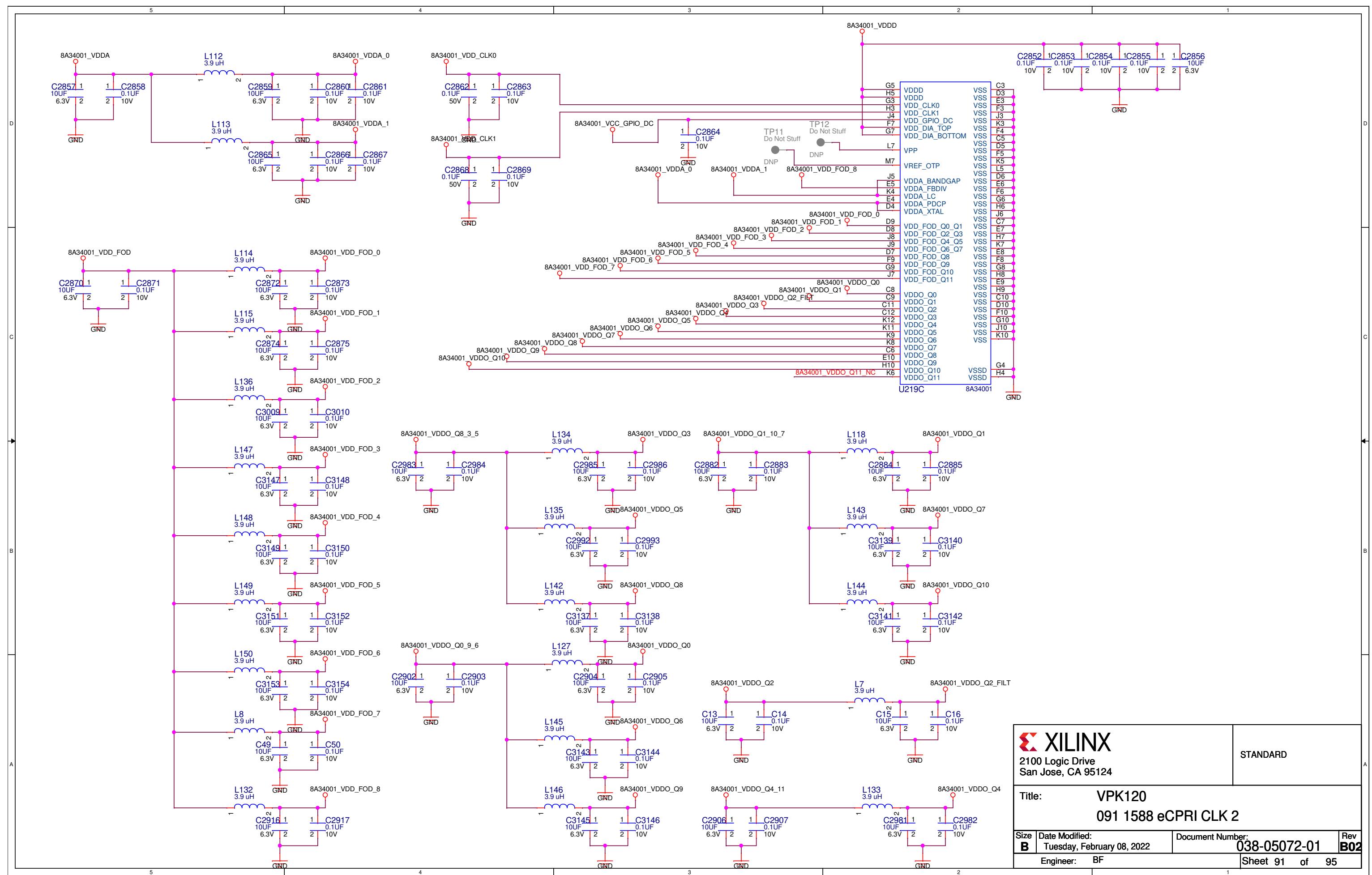
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Title: VPK120
089 Sys Ctrl 1.8, 1.2, 1.1, 0.9

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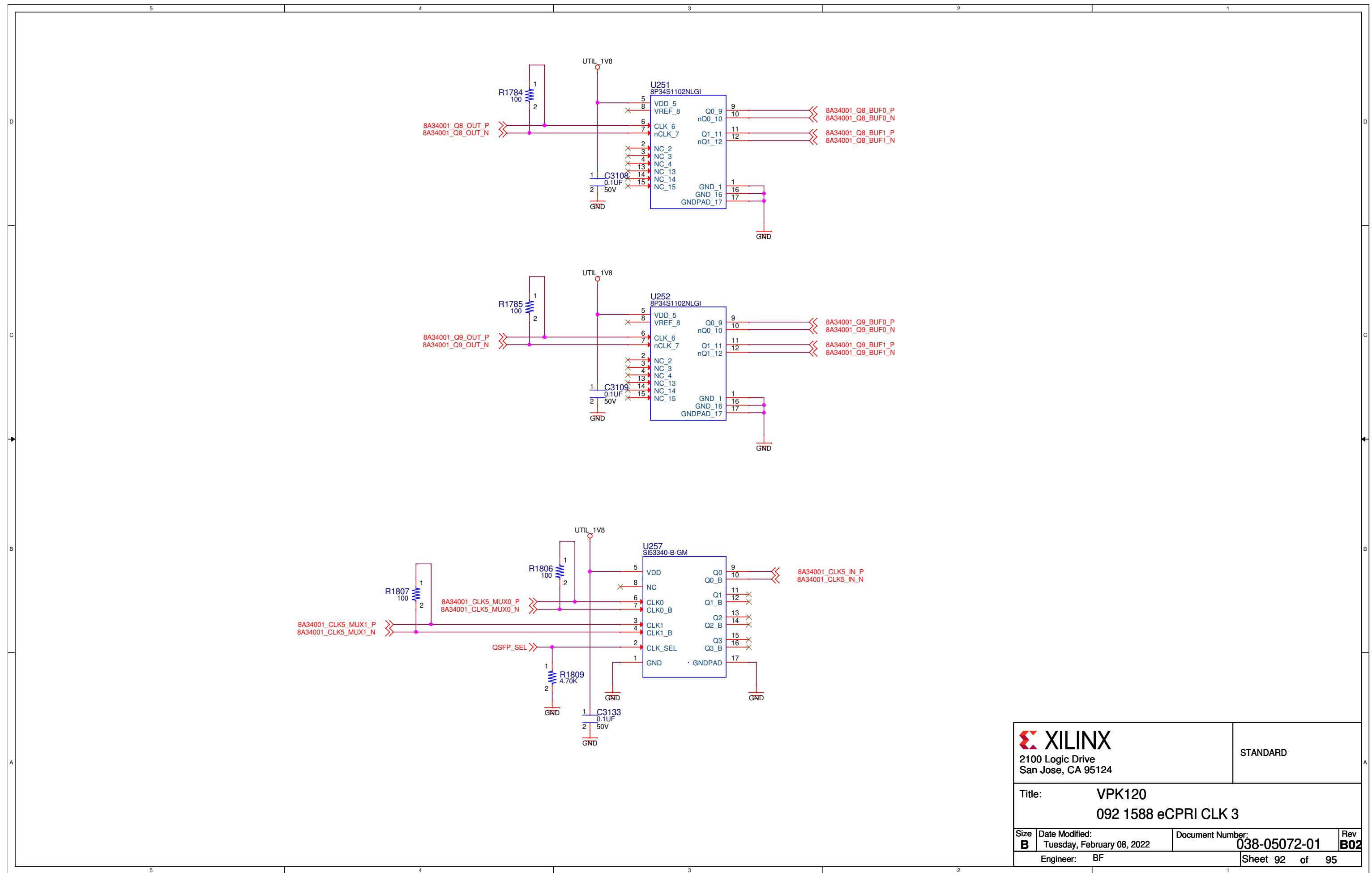
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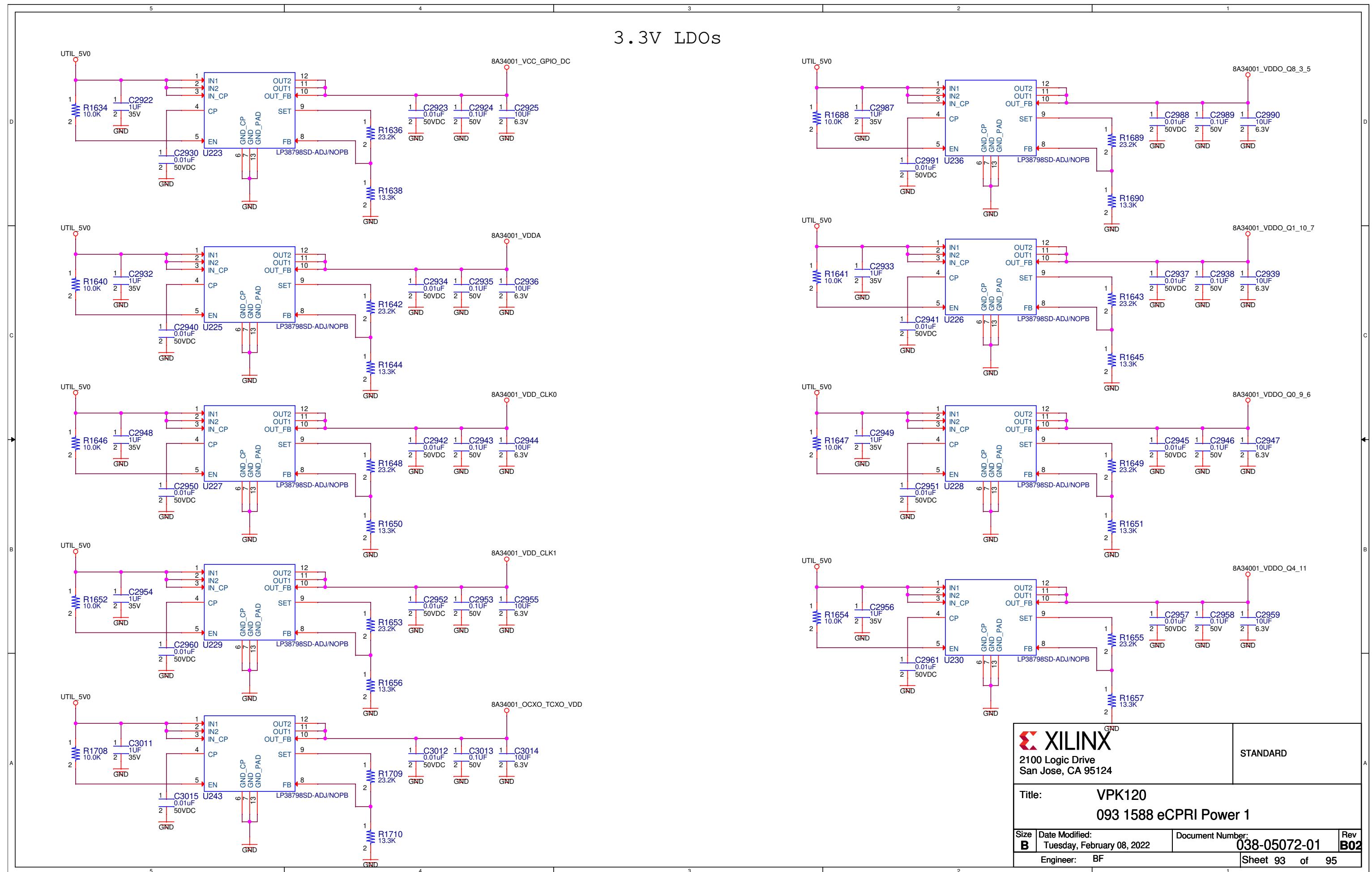
Title: **VPK120**
091 1588 eCPRI CLK 2

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2100 Logic Drive San Jose, CA 95124		
Title: VPK120 092 1588 eCPRI CLK 3		
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3.3V LDOs



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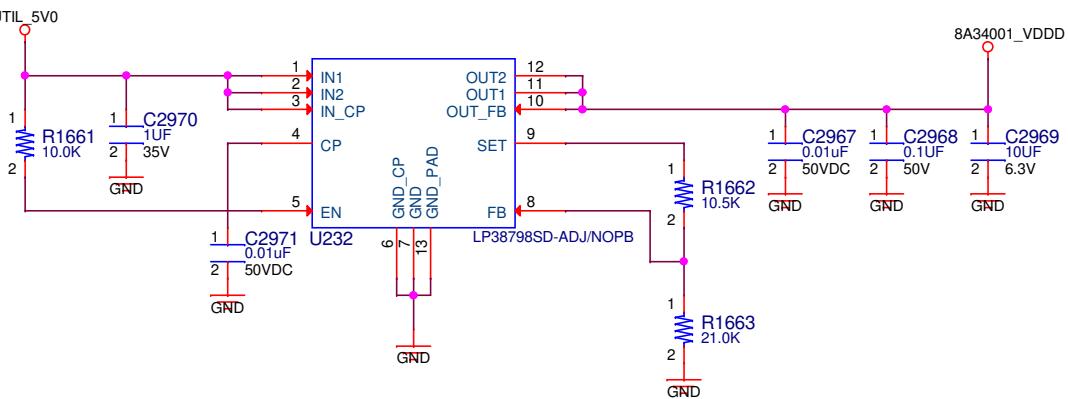
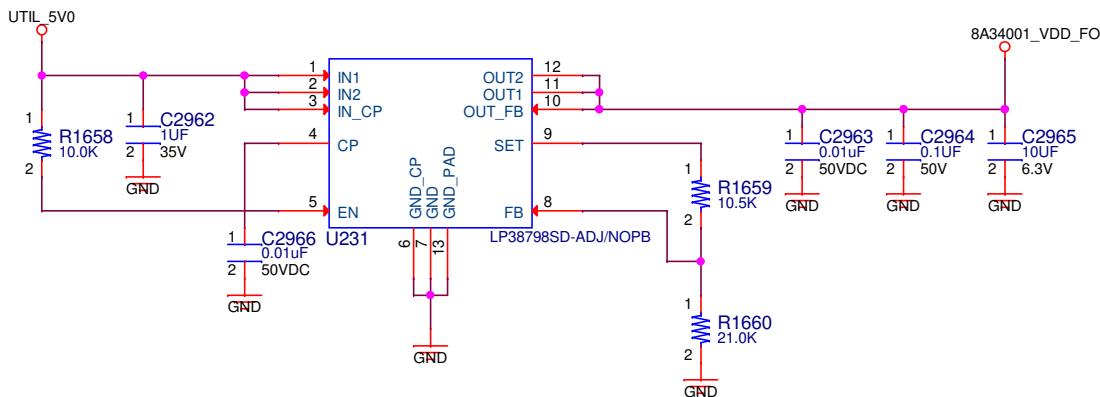
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San Jose, CA 95124

STANDARD

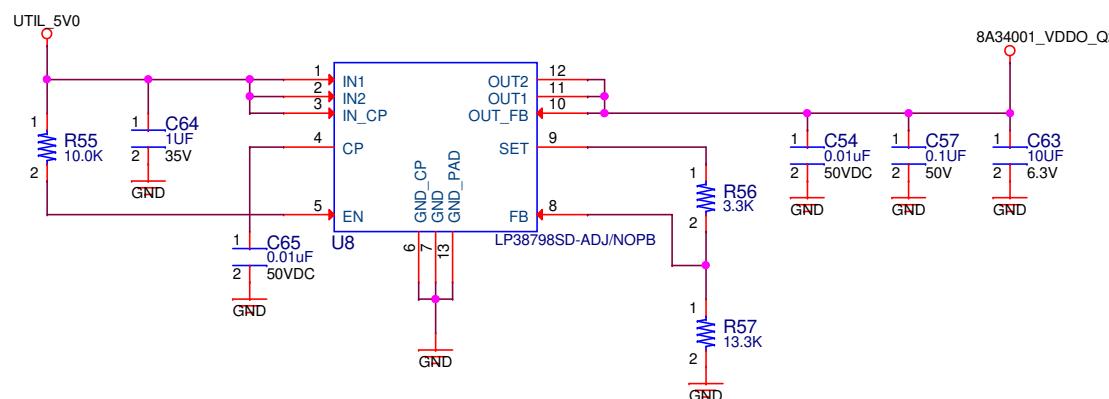
Title: VPK120
093 1588 eCPRI Power 1

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1.8V LDOs



1.5V LDO



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094 1588 eCPRI Power 2

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