Vaastav Anand

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https://vaastavanand.com/

PUBLICATIONS | *Pedro Las-Casas, Giorgi Papakerashvili, Vaastav Anand, Jonathan Mace. Sifter: Scalable Sampling for Distributed Traces, without Feature Engineering. To Appear at Symposium on Cloud Computing 2019*

Vaastav Anand. Dara: Hybrid Model Checking of Distributed Systems. In The ACM Joint European Software Engineering Conference and Symposium on the Foundations of Software Engineering, ESEC/FSE 2018, Lake Buena Vista, Florida, USA. (SRC)

RESEARCH | *Sifter* | 2019

- Sifter is an online-tool designed for sampling a unique and representative set of traces of distributed systems

Dara [In development] | 2018

- Dara models distributed systems from execution logs. Models are checked against specifications by an abstract and an implementation-level model checker.
- https://github.com/DARA-Project/dara

eTone | 2017-2018

- A tone matching game created to measure the brain myelination in people while learning tonal languages.
- Member of the Language Sciences Initiative Communicating Mind and Body Working Group.

EDUCATION | *MSc*, Computer Science | 2018-2020
University of British Colombia, Vancouver, BC

Bachelors of Science, Computer Science | 2013-2018
University of British Colombia, Vancouver, BC

EMPLOYMENT | *Research Intern, MPI-SWS* | 2019
Graduate Teaching Assistant | 2018-2019
University of British Coluimbia, Department of Computer Science
- 1 semester TA for Graduate Operating Systems (CPSC 508) | 2019
- 1 semester TA for Distributed Systems (CPSC 416) | 2018

Academic Assistant | 2018
Vancouver Summer Program
- Teaching Assistant for the Algorithms and the World Wide Web course.

Undergraduate Research Assistant | 2018
University of British Columbia, Under Ivan Beschastnikh
- Designed and developed Dara, a tool for model checking distributed systems.

Software Engineering Intern | 2017
NVIDIA - MODS (Modular Diagnostics) Team
- Implemented memory repair sequences for faulty High Bandwith Memory(HBM).

- Designed, developed and implemented a CUDA based full memory stress test.

Software Engineering Intern   2016
NVIDIA - MODS (Modular Diagnostics) Team
- Implemented a synchronization option for CUDA based linpack tests to synchronize CUDA kernel launches within 30us across multiple GPUs.

Software Developer Intern   2015-2016
Thinkbox Software - Sequoia Team
- Designed, developed and implemented the 3D PDF export option in Sequoia.

Undergraduate Teaching Assistant   2014-2018
University of British Columbia, Department of Computer Science
- 1 semester TA for Introduction to Software Engineering (CPSC 210)   2018
- 1 semester TA for Advanced Operating Systems (CPSC 415)   2017
- 1 semester TA for Intermediate Algorithm Design and Analysis (CPSC 320) 2017
- 1 semester TA for Computer Hardware and Operating Systems (CPSC 313) 2016
- 1 semester TA for Introduction to Computer Systems (CPSC 213)   2015
- 3 semesters TA for Models of Computation (CPSC 121)   2014-2015

AWARDS

2nd Place, FSE'18 SRC	2018
SIGSOFT CAPS Award	2018
UBC International Tuition Award	2018-2019
Work Learn International Undergraduate Research Award	2018
UBC Faculty of Science, International Student Award	2015, 2018
ACM ICPC PacNW Regional Contest Division 2 Champion	2017
UBC Trek Excellence Scholarship	2016-17, 2017-2018
UBC Dean's Honor List	2014, 2015, 2017
UBC Computer Science Student Service Award	2015
GIIS Global Citizen Scholarship	2011-2013

SERVICE

Academic Service   2018
- Sub-Reviewer for Prof. Ivan Beschastnikh
  - ESEM 2018, ESEC/FSE NIER 2018, Elseiver IST 2019
- Sub-Reviewer for William Anthony Mason
  - SIGCSE 2019

SKILLS

Programming Languages: C++, Go, Python, C, Bash, JavaScript, Java, CUDA
Tools: IntelliJ, GDB, Eclipse, Visual Studio, Git, Perforce, Vim, LaTeX

PROJECTS

Distributed Clocks   2018-current
Inter-operable vector clock logging library
- Distributed clocks implements vector clocks in Go, Java, C++ and C
- https://github.com/DistributedClocks

INTERESTS

Computing: Distributed Systems, Operating Systems, Software Engineering
Extra Curricular: Soccer, Languages.