



Vicente Abarca

Personal Information

Age: 39 years old

Phone: (+34) 644 11 87 37

mail: viabfer@gmail.com

Experience +10 years

- 2014 - present **Software Engineering Analyst**, *Celestica*, Poble de Vallbona (Spain).
Product design, engineering services, functional testing and manufacturing process improvements for aerospace, healthcare and communication customer projects.
- 2009 - 2014 **Project manager & Sr. R&D Engineer**, *I3BH - LabHuman*, Valencia (Spain).
In studies focused on the operation of natural interfaces; AR techniques, real time systems, RGB-D cameras, inertial & multitouch electronic devices and modern computer vision algorithms.
- 2007 - 2009 **Jr.R&D Engineer**, *I3BH - LabHuman*, Valencia (Spain).
R&D in multitouch systems based on modern electronic devices and computer vision algorithms.
- 2003 - 2004 **Jr. R&D Technical Electronic Engineer**, *ITE - Instituto tecnológico de la energía*, Valencia (Spain).
R&D in early warning electronic radiation dosimeters and networks analyzers.
- 2001 - 2003 **Jr. Tech. Electronic Engineer - tester**, *ITE - Instituto tecnológico de la energía*, Valencia (Spain).
R&D in lightning protection devices and overvoltage/overcurrent certification tests.

Education

- 2006-2006 **Bioengineering**, *UV*, Valparaíso, Chile.
PFC: Small & portable X-ray medical equipment design.
- 2001-2006 **Master's level degree in Telecommunications, Bioelectronics**, *UPV*, Valencia, Spain.
- 2000-2001 **Master in Telecommunications**, *Högskolan i Gävle*, Gävle, Sweden.
PFC: Temperature controller design for RF transistor over silicon wafer
- 1997-2001 **Bachelor's level degree in Electronics Engineering**, *UPV*, Valencia, Spain.

Technical knowledge

- Software** C/C++, C#, Python, NI Labview, NI TestStand, QT, Visual Basic, Multithread programming, 3D, RA, Computer Vision, OpenGL, OpenCV, OpenNI
- Hardware** Embedded systems, Linux drivers, ARM, PSOCs, SMARTFUSION, 8051, PIC, ATmega, MSP430, Arduino, Raspberry Pi, Toradex, Zigbee, Bluetooth, RF, A/D design
- App** Unity (3D), Eagle, Orcad, SolidWorks, FreeCAD, Icestudio (FPGAs), Visual Studio, PlatformIO

Additional training

- Courses** Linux Device Drivers(40h), SMARTFUSION:ARM Cortex SOC Design(20h), Orcad & Pspice (40h), OpenGL (20h) and self-training (RESTful APIs, Python, Software development, design patterns, FPGAs)

Languages

English Intermediate/high

Vancouver Eurocentres English Course

Further information

<https://www.linkedin.com/in/vicenteabarcafernandez05031979/>

<https://github.com/vabarca/>