

Austin LLVM Meetup

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Meetup Agenda

- Today
 - LLVM Walkthrough How to build
 - Git clone llvm/clang and testing
 - Static Analysis
- Sponsorship
 - Our space is sponsored by Capital Factory
 - Meetup fees are paid by Ilvm.org thank you, Arnaud de Grandmaison!



Meetup Schedule, Space

- 4th Wednesday every month
- We're trying "virtual" for this meeting
 - Thanks for your patience!



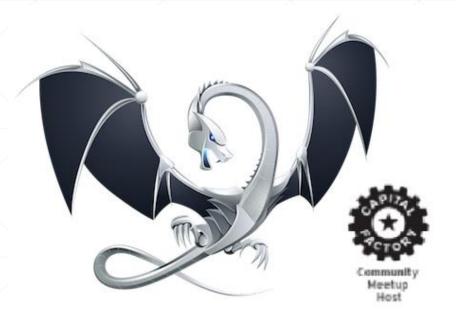
About this tutorial

- LLVM/Clang, Z3 Introduction
- Basics of How to build for the examples shown
- Mini introduction to LLVM IR
- Example usages of Clang
- Static Analysis
- Benefits of CTU and Z3, how to use
- A few known issues with Clang Static Analysis



What is LLVM?

- A collection of reusable compiler and toolchain libraries
- LLVM is not a compiler! Clang is built on LLVM for example
- Compilers using LLVM
 - Clang, Swift, Rust, Haskell
- LLVM's IR Intermediate Representation was one of it's great innovations. I'll touch upon that today
 - LLVM was "Low Level Virtual Machine" now just LLVM
- Great for academic research into technologies related to compilers, researching new compiler passes and improvements



Comparing LLVM and GCC

- Difficult to compare, came from different origins
- Structured differently GCC is monolithic, LLVM/Clang is modular
- GCC is difficult to extend. LLVM/Clang can be improved and extended by improving and extending the interfaces.
- GCC supports more traditional languages such as Ada, Fortran and Go.
- GCC supports less popular architectures and more assembly language features than Clang. GCC is still the only option for compiling the Linux kernel.
- Clang provides many additional useful tools, such as static analysis, clang-format, clang-tidy, refactoring tools, and IDE plugins
- Clang was designed from the very beginning to provide accurate and user friendly error messages.



Demo - Environment, Tools

- Oracle VirtualBox VM, Ubuntu 18.04 on Win10
- 16G memory, 100GB storage, 4 processors
- Bidi sharing enabled, Guest Additions added
- sudo apt install gcc g++ cmake ninja-build python



Z3 – an SMT Solver to Enhance Static Analysis

- Z3 is a cross platform, satisfiability modulo theories (SMT) solver by Microsoft
- Helps solves problems that arise in software verification and analysis. We'll use Z3 in static analysis.
- Bindings for various programming languages include C, C++, Java, and Python among others.
- Pull git clone https://github.com/Z3Prover/z3.git
- Build cd z3; rm –rf build; git clean –nx src; mkdir build; cd build
- Build ... cmake –DCMAKE_INSTALL_PREFIX=<root>/z3-install –
 DCMAKE_EXE_LINKER_FLAGS:STRING=-WI,-R<root>/z3-install/lib –
 DCMAKE_BUILD_TYPE=Release –G Ninja ../
- Build ... ninja; ninja install

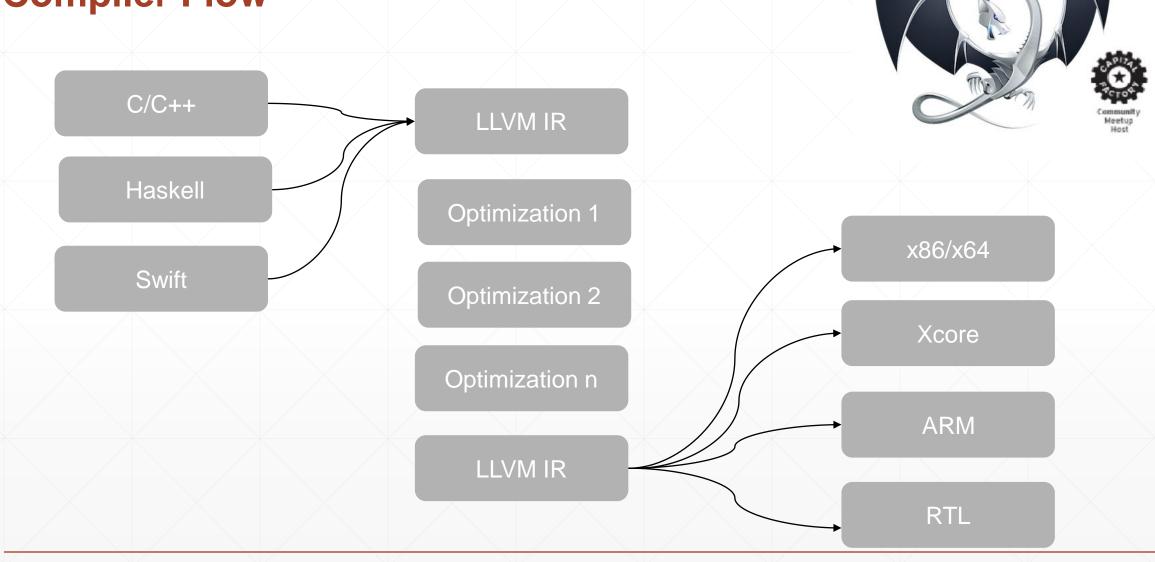


Start "git clone" & build

- git clone https://github.com/llvm/llvm-project.git
 - mkdir Ilvm-install; mkdir Ilvm-build; cd Ilvm-build
- X86
 - cmake -DLLVM_ENABLE_ASSERTIONS=On -DCMAKE_BUILD_TYPE=Release -DLLVM_ENABLE_PROJECTS="clang;clang-tools-extra" -DLLVM_TARGETS_TO_BUILD="X86" -DLLVM_Z3_INSTALL_DIR= <root>/z3-install -DLLVM_ENABLE_Z3_SOLVER=ON -DCMAKE_EXE_LINKER_FLAGS:STRING=-WI,-R<root>/z3-install/lib -DCMAKE_INSTALL_PREFIX=<root>/llvm-install -G Ninja <root>/llvm-project/llvm
 - ninja; ninja install; ninja check-all



Compiler Flow



IR Representation



Bitcode file: *.bc

000101010101010000101010101010 000101010101010010111010010110 000000000000000000101010101010 000101010101010000101010101010 01011101001011000000000000000000

Ilvm-dis



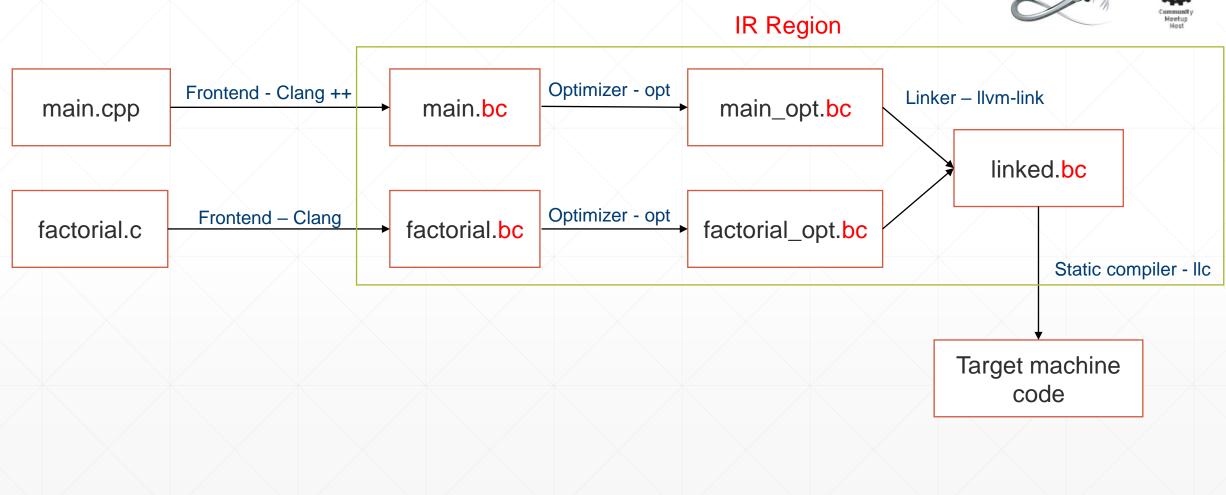
Ilvm-as

Human-readable file: *.11

```
; You can read me!
```

IR and the Compilation Process





A basic main program

Hand-written IR for this program:

```
int factorial(int val);
int main(int argc, char** argv)
{
  return factorial(2) * 7 == 42;
}
```

% Virtual Registers %

Those are "local" variables.

Two flavors of names:

- Unnamed: %<number>
- Named: %<name>

"LLVM IR has infinite registers"

```
declare i32 @factorial(i32)

define i32 @main(i32 %argc, i8** %argv) {
    %1 = call i32 @factorial(i32 2)
    %2 = mul i32 %1, 7
    %3 = icmp eq i32 %2, 42
    %result = zext i1 %3 to i32
    ret i32 %result
}
```

Types everywhere!

Very much a typed language.

```
declare i32 @factorial(i32)

define i32 @main(i32 %argc, i8** %argv) {
    %1 = call i32 @factorial(i32 2)
    %2 = mul i32 %1, 7
    %3 = icmp eq i32 %2, 42
    %result = zext i1 %3 to i32
    ret i32 %result
}
```

The LangRef is your friend

'call' Instruction

```
Syntax: %1 = call i32 @factorial(i32 2)
```

Overview:

The 'call' instruction represents a simple function call.

Arguments:

This instruction requires several arguments:

Basic Blocks

List of non-terminator instructions ending with a <u>terminator</u> instruction:

- Branch "br"
- Return "ret"
- Switch "switch"
- Unreachable "unreachable"
- Exception handling instructions

```
define i32 @factorial(i32 %val) {
    %is_base_case = icmp eq i32 %val, 0
    br i1 %is_base_case, label %base_case, label %recursive_case
    base_case:
    ret i32 1
    recursive_case:
    %1 = add i32 -1, %val
    %2 = call i32 @factorial(i32 %0)
    %3 = mul i32 %val, %1
    ret i32 %2
}
```

Control Flow Graph

```
; Precondition: %val is non-negative.
define i32 @factorial(i32 %val) {
  %is base case = icmp eq i32 %val, 0
  br i1 %is_base_case, label %base_case, label %recursive_case
                                                                         entry
base_case:
                         ; preds = %entry
  ret i32 1
recursive_case:
                         ; preds = %entry
 %1 = add i32 -1, %val
 %2 = call i32 @factorial(i32 %0)
                                                             base_case
                                                                             recursive case
 %3 = mul i32 %val, %1
  ret i32 %2
                                                               CFG for 'ir_factorial' function
```

Automatically generated comments

Compiling a Simple Source file to IR

```
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```

```
1: int main(void) {
2: int a = 32;
3: int b = 10;
4: return a + b;
5: }
```

- \$ clang -S -emit-llvm main.c
- Produces unoptimized IR

```
define dso_local i32 @main() #0 {
entry:
    %retval = alloca i32, align 4
    %a = alloca i32, align 4
    %b = alloca i32, align 4
    store i32 0, i32* %retval, align 4
    store i32 32, i32* %a, align 4
    store i32 10, i32* %b, align 4
    %0 = load i32, i32* %a, align 4
    %1 = load i32, i32* %b, align 4
    %add = add nsw i32 %0, %1
    ret i32 %add
}
```

Compiling a Simple Source file to IR



```
1: int main(void) {
2:    int a = 32;
3:    int b = 10;
4:    return a + b;
5: }
define dso_local i32 @main()
local_unnamed_addr #0 {
   entry:
    ret i32 42
}
```

Constant Folding. The compiler recognized a, b, and a+b as constants and folded the result to 42. The answer to everything!

- \$ clang -S -emit-llvm -O3 main.c
- Produces optimized IR

Finding bugs with the Compiler

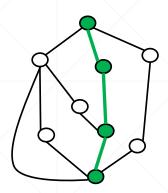


 Static analysis can find deeper bugs through program analysis techniques – like memory leaks, buffer overruns, logic errors.

Program Analysis vs Testing

- Testing usually tests a small number of paths in the program.
- May miss errors
- It's fast, but real coverage can be sparse
- Same is true for other useful testing methods such as Sanitizers
- All used together a useful combination

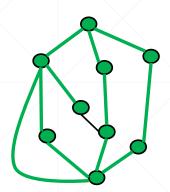




Program Analysis vs Testing

- Program analysis can exhaustively explore all execution paths
- Reports errors as traces, or "chains of reasoning"
- Downside doesn't scale well path explosion
- Mitigation techniques ...
 - Bounded model checking breadth-first search approach
 - Depth-first search for symbolic execution





Clang Static Analyzer (CSA)

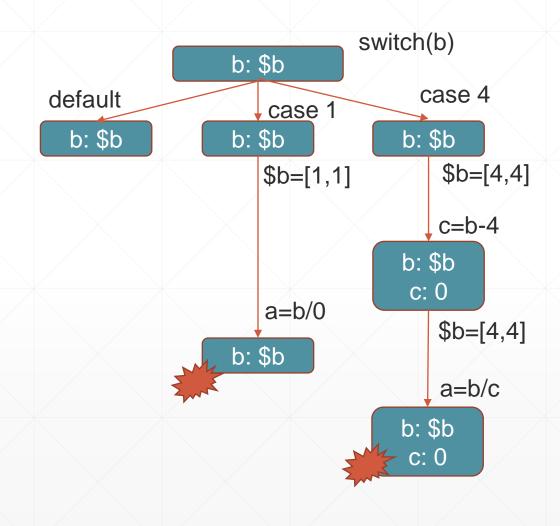


- The CSA is project built on top of clang that performs context-sensitive interprocedural analysis for programs written in the languages supported by clang.
- Designed to be fast to detect common mistakes such as divide by zero, or null pointer dereferences, even in complex projects.
- Speed comes at the expense of precision, so does not handle some arithmetic (e.g. remainder) and bitwise operations. Z3 can compensate for some of these shortcoming.
- Normally, static analysis works in the boundary of a single translation unit. With additional steps and configuration, static analysis can utilize multiple translation units.
- Memory modeling is sufficient for many issues, but also has a few incomplete areas due to the performance/function tradeoffs taken. I'll demonstrate the opportunities for improvement.
- Early discovery of bugs, when cheaper to fix

Clang Static Analyzer – Symbolic Execution

- Finds bugs without running the code
- Path sensitive analysis
- CFGs used to create exploded graphs of simulated control flows

```
void test(int b) {
  int a, c;
  switch (b) {
    case 1: a = b / 0; break;
    case 4: c = b - 4;
    a = b/c; break;
}
```



Using the Clang Static Analyzer



- Basic example
- \$ clang --analyze -Xclang -analyzer-output=html -o <output-dir> file.c
- Runs the analyzer on file.c, outputs an HTML formatted "chain of reasoning" to the output directory.
- cd to <output-dir>, firefox report* &

Clang Static Analyzer – Example 1

```
void f6(int x) {
  int a[4];
  if (x==5) {
    if (a[x] == 123) {}
}
```

Array index out of bounds.

Bug Summary

File: /home/vince/examples/check.c

Warning: line 6, column 18

The left operand of '==' is a garbage value due to array index out of bounds

Annotated Source Code

Press !?! to see keyboard shortcuts

Show analyzer invocation

1 warning generated.

Clang Static Analyzer – Example 2

```
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```

```
1:
2: int foobar() {
3:    int i;
4:    int *p = &i;
5:    return *p;
6: }
```

- 'i' declared without an initial value
- '*p', undefined or garbage value

Bug Summary

File: /home/vince/examples/check2.c

Warning: line 5, column 5

Undefined or garbage value returned to caller

Annotated Source Code

Press !?! to see keyboard shortcuts

Show analyzer invocation

```
Show only relevant lines

int foobar() {
   int i;

   int *p = &i;
   return *p;

   Undefined or garbage value returned to caller
}
```

Clang Static Analyzer – Example 3

```
Community
```

```
2: #include <stdlib.h>
    int process(void *ptr, int cond) {
 5:
        if (cond)
 6:
            free (ptr);
 9: int entry(size t sz, int cond) {
        void *ptr = malloc(sz);
10:
11:
        if (ptr)
12:
            process (ptr, cond);
13:
14:
        return 0;
15: }
```

- Analysis spans functions said to be "interprocedural"
- A Memory leak!

Bug Summary

File: /home/vince/examples/check3.c

Warning: line 14, column 12

Potential leak of memory pointed to by 'ptr'

Annotated Source Code

Press '?' to see keyboard shortcuts

Show analyzer invocation

```
    Show only relevant lines

      #include <stdlib.h>
      int process(void *ptr, int cond) {
           if (cond)
               free(ptr);
      int entry(size t sz, int cond) {
           void *ptr = malloc(sz);

    Memory is allocated →

           if (ptr)
                   ← Assuming 'ptr' is non-null →
               ← Taking true branch →
               process(ptr, cond);
 14
           return 0;

    Potential leak of memory pointed to by 'ptr'

 15 }
```

Using the Clang Static Analyzer

Community Meetup Host

- These examples were single translation unit only.
 - In other words, in the same, single source file "interprocedural"
- What if a function calls another function outside of it's translation unit?
 - Referred to as "Cross translation Unit"
- Examples ...

Cross Translation Unit Analysis

Main.cpp

```
int foo();
int main() {
   return 3/foo();
}
```

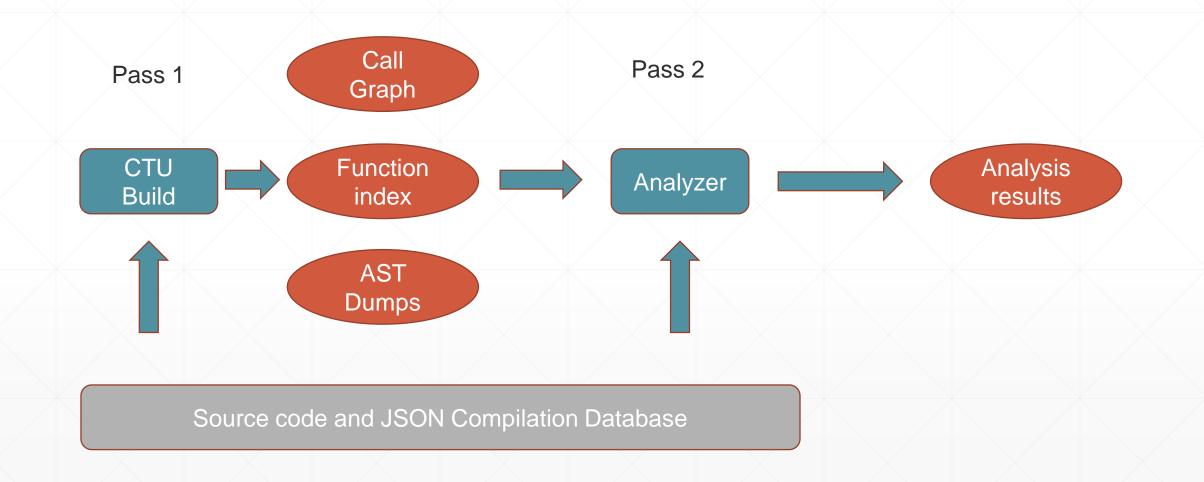
Foo.cpp

```
int foo() {
    return 0;
}
```

foo() is not known to be 0 without CTU

- CTU gives the analyzer a view across translation units
- Avoids false positives caused by lack of information
- Helps the analyzer constrain variables during analysis

How does CTU work?



Manual CTU – compile_commands.json

```
"directory": "<root>/examples/ctu",
   "command": "clang++ -c foo.cpp -o foo.o",
   "file": "foo.cpp"
},
{
   "directory": "<root>/examples/ctu",
   "command": "clang++ -c main.cpp -o main.o",
   "file": "main.cpp"
}
```



- Mappings implicitly use the compile_commands.json file
- Analysis phase uses compile_command.json to locate the source files.

Manual CTU - Demo

```
# Generate the AST (or the PCH)
clang++ -emit-ast -o foo.cpp.ast foo.cpp
# Generate the CTU Index file, holds external defs info
clang-extdef-mapping -p . foo.cpp > externalDefMap.txt
# Fixup for cpp -> ast, use relative paths
sed -i -e "s/.cpp/.cpp.ast/g" externalDefMap.txt
sed -i -e "s|$(pwd)/||g" externalDefMap.txt
# Do the analysis
clang++ --analyze \
    -Xclang -analyzer-config -Xclang experimental-enable-naive-ctu-analysis=true \
    -Xclang -analyzer-config -Xclang ctu-dir=. \
    -Xclang -analyzer-output=plist-multi-file \
    main.cpp
```

Cross Translation Unit Analysis – Example 2

Main.cpp void neg(int *x); Void g(int *x) { if (*x>0) { neg(x); *x is unknown if (*x>0) { *x/0; neg(NULL); } API misuse Foo.cpp void neg(int *) { *x = -(*x); } API misuse

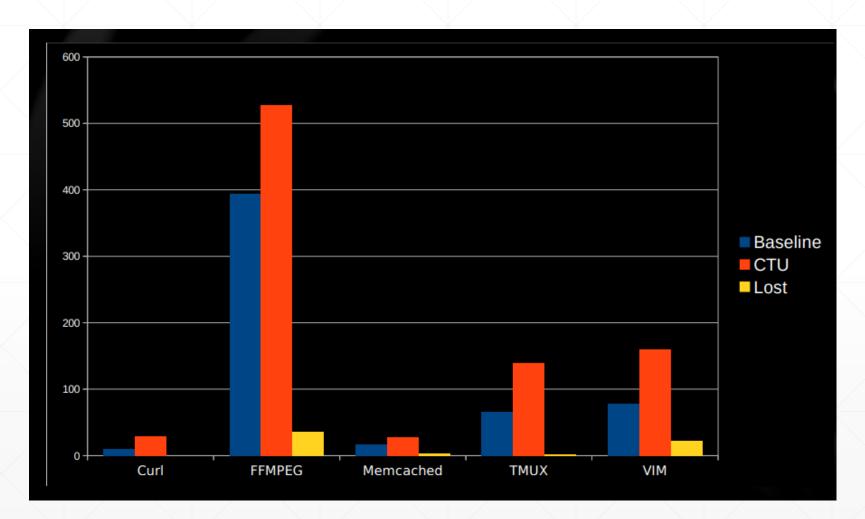
Using Cross Translation Unit Analysis



- scan-build.py within Clang can be used to drive Static Analysis on projects, scarbuild is NOT actively developed for CTU.
 - Don't try it, you'll probably encounter a lot of pain ☺
- Ericsson's CodeChecker tool supports CTU flows
- It's possible to develop your own but why? CodeChecker is open source ©
- One could always try to upstream fixes to scan-build.py

Some statistical benefits of CTU





- 2.4x Average
- 2.1x median
- 5x peak
- Note there are some lost defects when using CTU
- For this reason, w/ and w/o CTU runs are recommended

Refuting some False Positives with Z3



- CSA sometimes detects false positives because of limitations in the CSA constraint manager.
- Speed comes at the expense of precision, so does not handle some arithmetic (e.g. remainder) and bitwise operations. Z3 can compensate for some of these shortcoming.
- See https://github.com/Z3Prover/z3. Clang can be compiled to use Z3. They are separated because of licensing issues.

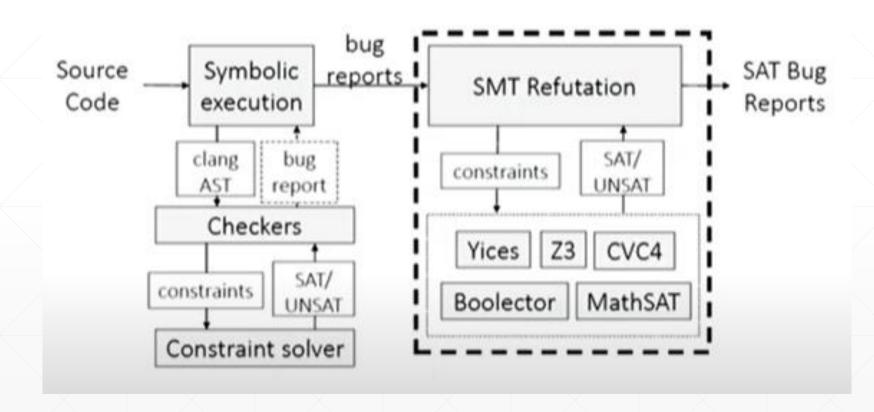
Example of unhandled bitwise operations

```
1: unsigned int func(unsigned int a) {
2:    unsigned int *z = 0;
3:    if ((a & 1) && ((a & 1) ^1))
4:        return *z;
5:    return 0;
6: }
```



This program is safe, albeit brittle

False positives – Z3 Refutation





Benefits of Z3

Project	Analysis time (no refutation)	· \ / - \ / /	Number of bugs (no refutation)	
+	00.35	96.28	19	-
tmux	90.35	' /		\'
redis	328.85	'\ / / / /	86	
openSSL	128.67	119.12	36	1 2 1
twin	205.86	207.87	52	0
git	384.00	356.75	69	11
postgreSQL	1024.68	1074.73	184	5
SQLite3	1016.38	1057.99	82	14
curl	80.68	81.56	39	0
libWebM	41.15	42.10	6	0 1
memCached	89.41	100.22	1 25	0
xerces-C++	408.91	387.17	58	2 /
XNU	3564.91	3464.46	441	49

See https://arxiv.org/pdf/1810.12041.pdf

Why not just replace the CSA solver?



- First SMT backend solver (Z3) implemented in late 2017 by Dominic Chan. It aimed to replace the CSA constraint solver.
- This solver was 20 times slower than the built in solver.
- A refutation approach gives us best of both worlds
 - Speed for common cases
 - A chance for a Z3 solver to refute bugs
- So, this is the approach for now

CSA Memory Modeling Weaknesses



- CSA does a good job addressing many memory modeling problems during static analysis, but does suffer from some technical debt in it's implementation.
- It's difficult to know for certain if this was a mindful design choice, or an oversight at any rate doesn't matter ☺ . The term "technical debt" implies this was an oversight, but performance is sometimes quoted as the reason. Meh ?
- For a list of technical debt in the CSA, see https://clang-analyzer.llvm.org/open_projects.html.
- A list of Bugzilla bugs related to the Clang Static Analyzer
- Some examples ...

CSA Weakness – Example 1

```
1: typedef struct {
 2: int rcode;
 3: } A;
 4: typedef struct {
        int rcode;
 6: } B;
 7: int fred(A *param, int *x) {
        if (param->rcode != 0)
            return ((B*) param) ->rcode;
       *x = 1;
10:
11:
        return 0;
12: }
13: int foo(A* param) {
14:
        int x;
15:
        if (fred(param, &x) != 0) {
16:
            return 0;
17:
18:
        return x;
19: \
```

Casts are not recognized in some cases.

```
Press '?' to see keyboard shortcuts
Show analyzer invocation

    Show only relevant lines

       typedef struct {
            int rcode;
       } A;
       typedef struct {
            int rcode;
       int fred(A *param, int *x) {
            if (param->rcode != 0)
                 3 ← Assuming field 'rcode' is not equal to 0 →
              4 ← Taking true branch →
                 return ((B*) param)->rcode;
                 S ← Returning without writing to '*x' →
  10
            *x = 1;
  11
            return 0;
  12
  13
       int foo(A* param) {
            int x;
             1 'x' declared without an initial value →
  15
            if (fred(param, &x) != 0) {
                 2 ← Calling 'fred' →
                    ← Returning from 'fred' →

    Assuming the condition is false -

                ← Taking false branch →
                return 0;
  17
            return x;
             Undefined or garbage value returned to caller
```

CSA Weakness – Example 2

```
1: struct s1 {
2:    unsigned short u16;
3: };
4: int main(void) {
5:    struct s1 s1var = { 0x1122 };
6:    char *p = &s1var;
7:    int x = 0;
8:    if (p[1])
9:        x++;
10:    return x;
11: }
```

 p[1] is not recognized by the CSA as defined.



Bug Summary

File: /home/vince/examples/false-positives/case1.c

Warning: line 8, column 9

Branch condition evaluates to a garbage value

Annotated Source Code

Press !?! to see keyboard shortcuts

Show analyzer invocation

CSA Weakness – Example 3

```
1: unsigned long getV();
2: void foo() {
3:     unsigned long len = getV();
4:     void *ptrs[len*2];
5:
6:     for (int i=0; i<len*2; i++) {
7:         ptrs[i] = 0;
8:     }
9:
10:     for (int i=0; i<len; i++)
11:         if (ptrs[i] == ptrs[i+len])
12:         return;
13: }</pre>
```

 Evaluation of Symbolic Values in conditionals is not handled in all cases.



```
for (int i=0; i<len*2; i++) {
 1 Loop condition is true. Entering loop body →
    ← Loop condition is true. Entering loop body -
                 ← Assuming the condition is false →
     ← Loop condition is false. Execution continues on line 10 →
    ptrs[i] = 0;
for (int i=0; i<len; i++)
    ← Loop condition is true. Entering loop body →
                 ← Assuming 'i' is < 'len' →
     ← Loop condition is true. Entering loop body →
                    12 ← The value 2 is assigned to 'i' →
 13 ← Loop condition is true. Entering loop body →
    if (ptrs[i] == ptrs[i+len])
           ← Assuming the condition is false →
        ← Taking false branch →
         10 
Assuming the condition is false
     11 ← Taking false branch →
                    ← The left operand of '==' is a garbage value due to array index out of bounds
         return;
```

References

Z3 Refutation in Clang - https://arxiv.org/pdf/1810.12041.pdf

- Implementation of CTU in Clang https://dl.acm.org/doi/pdf/10.1145/3183440.3195041
- https://llvm.org/devmtg/2017-03//assets/slides/cross_translation_unit_analysis_in_clang_ static_analyzer.pdf
- SMT based refutation of spurious bug reports in CSA -https://www.youtube.com/watch?v=WxzC_kprgP0

Links

- https://www.meetup.com/Austin-LLVM-Meetup/
- https://www.capitalfactory.com/ Capital Factory
- https://docs.google.com/document/d/1bqms0I4u9sNdHWfK5c76-iu1Q7DvZTHXIxmq74-8aA/edit LLVM Meetup suggestions
- https://berlincodeofconduct.org/ Berlin Code of Conduct
- https://llvm.org/pubs/2002-12-LattnerMSThesis.pdf



Thank you for attending!

