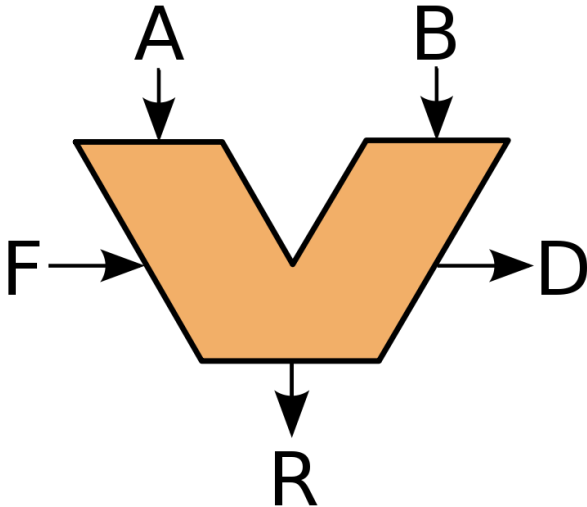


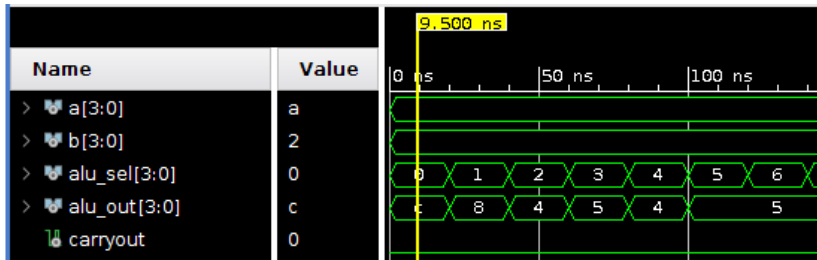
Unidad Aritmética Lógica

Gonzalo Vaca

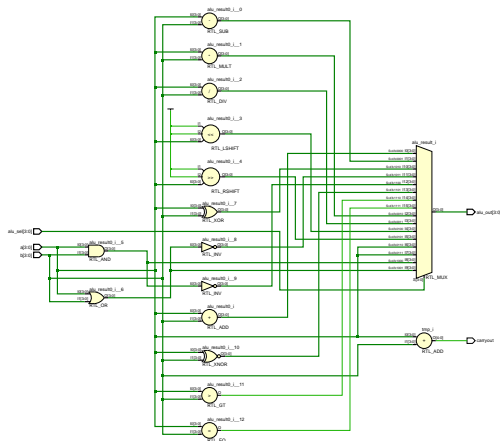
16 de abril de 2022

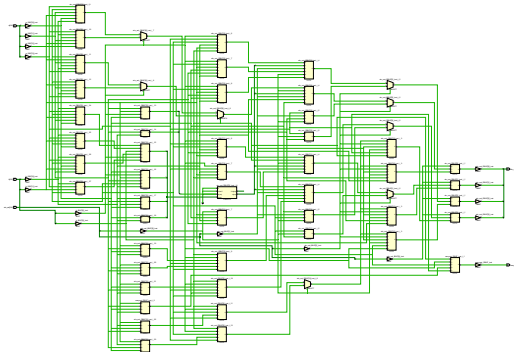


RTL: Register Transfer Level



RTL: Register Transfer Level





Implementación

Utilization

Post-Synthesis

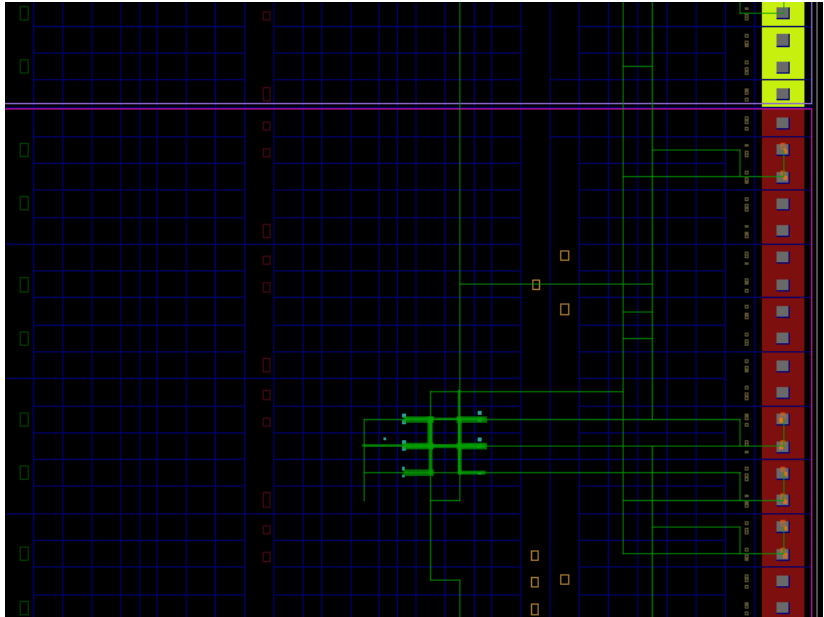
Post-Implementation

Graph

Table

Resource	Utilization	Available	Utilization %
LUT	40	53200	0.08
IO	17	125	13.60

Implementación



Montaje

