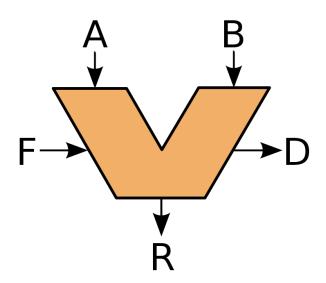
### Unidad Aritmética Lógica

Gonzalo Vaca

16 de abril de 2022

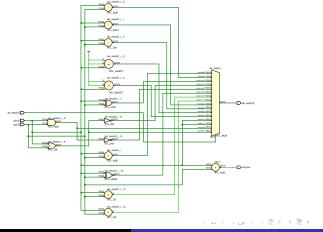




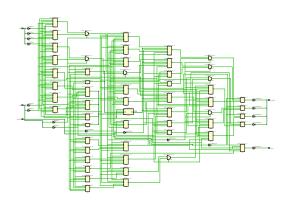
## RTL: Register Transfer Level

		9.500 ns		
Name	Value	0 ns		
> <b>⊌</b> a[3:0]	а	{		
> <b>⊌</b> b[3:0]	2			
> <b>₩</b> alu_sel[3:0]	0	0 1 2 3 4 5 6 2		
> <b>™</b> alu_out[3:0]	С	(		
¹⊌ carryout	0			

### RTL: Register Transfer Level



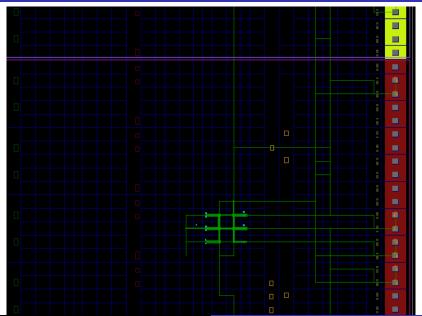
#### Síntesis



## Implementación

Ut	ilization	Post-Synthesis   <b>Post-Implementation</b>					
	Graph   <b>Table</b>						
	Resource	Utilization	Available	Utilization %			
	LUT	40	53200	0.08			
	10	17	125	13.60			

# Implementación



### Montaje

