2. Device-Tree

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1 Device tree

1.1 1. Understanding the device tree

Refer to the page [`u-boot/1. Device Driver/1`]

1.2 2. How the device tree is loaded by Bootloader

During the boot process, the secondary bootloader loads the DTB into memory. The specific method of loading can vary:

- **Directly from Storage**: The bootloader reads the DTB from its storage location into RAM.
- **Packaged with the Kernel**: In some configurations, the DTB may be appended to the kernel image. The bootloader loads the entire package into memory, and the kernel extracts the DTB.
- **User Selection or Automatic Detection**: In systems with multiple possible hardware configurations, the bootloader may present a selection to the user or automatically detect the hardware configuration to choose the correct DTB.

1.3 3. Platform device - Platform driver

- **Static Configuration**: Information about the device (like memory addresses, IRQ numbers, etc) is specified within static data like the Device Tree.
- **No Hardware Enumeration**: These devices are typically directly integrated into the motherboard and do not have an enumeration mechanism unlike PCI or USB devices.

1.3.1 3.1 How to connect a device to a driver

1.3.1.1 3.1.1 Add a device(mydevice@1) in the device tree (`s5p6818.dtsi`)

```
{
        model = "nexell soc";
        compatible = "nexell,s5p6818";
        #address-cells = <0x1>;
        #size-cells = <0x1>;

        aliases {
                serial0 = &serial0;
                serial1 = &serial1;
                serial2 = &serial2;
                serial3 = &serial3;
                serial4 = &serial4;
                serial5 = &serial5;
                i2s0    = &i2s_0;
                i2s1    = &i2s_1;
                i2s2    = &i2s_2;
                spi0    = &spi_0;
                spi1    = &spi_1;
                spi2    = &spi_2;
                i2c0    = &i2c_0;
                i2c1    = &i2c_1;
                i2c2    = &i2c_2;

                pinctrl0 = &pinctrl_0;
        };

        mydevice@1 {
                compatible = "yang,mydevice";
                /* Other necessary properties */
        };
```

1.3.1.2 3.1.2 driver code (misc driver)

The driver code should match the device name(mydevice@1) and 'compatible'(yang,mydevice)

drivers/misc/my_misc.c

```
#include <linux/miscdevice.h>
#include <linux/fs.h>
#include <linux/kernel.h>
#include <linux/module.h>
#include <linux/init.h>

/* Connect the device from the dts to the driver */
#include <linux/moduleparam.h>
#include <linux/platform_device.h>

static struct of_device_id mydevice_match[] = {
    { .compatible = "yang,mydevice",},
    {}
};
//MODULE_DEVICE_TABLE(of, );


static int mydevice_probe(struct platform_device *pdev)
{
    printk("===yang's device[%s][L:%d]", __func__, __LINE__);
    return 0;
}

static struct platform_driver mydevice_driver = {
    .probe = mydevice_probe,
    .driver = {
        .owner = THIS_MODULE,
        .name = "mydevice@1",
        .of_match_table = mydevice_match,
    },
};

static int __init mydevice_debug_init(void)
{
    printk("===yang's device[%s][L:%d]===\n", __func__, __LINE__);
    return platform_driver_register(&mydevice_driver);
}

late_initcall(mydevice_debug_init);

/*
** This function will be called when we open the Misc device file
```

```
*/
static int etx_misc_open(struct inode *inode, struct file *file)
{
    pr_info("EtX misc device open\n");
    return 0;
}

/*
** This function will be called when we close the Misc Device file
*/
static int etx_misc_close(struct inode *inodep, struct file *filp)
{
    pr_info("EtX misc device close\n");
    return 0;
}

/*
** This function will be called when we write the Misc Device file
*/
static ssize_t etx_misc_write(struct file *file, const char __user *buf,
                size_t len, loff_t *ppos)
{
    pr_info("EtX misc device write\n");

    /* We are not doing anything with this data now */

    return len;
}

/*
** This function will be called when we read the Misc Device file
*/
static ssize_t etx_misc_read(struct file *filp, char __user *buf,
                    size_t count, loff_t *f_pos)
{
    pr_info("EtX misc device read\n");

    return 0;
}

//File operation structure
static const struct file_operations fops = {
    .owner          = THIS_MODULE,
    .write          = etx_misc_write,
    .read           = etx_misc_read,
    .open           = etx_misc_open,
    .release        = etx_misc_close,
    .llseek         = no_llseek,
};

//Misc device structure
struct miscdevice etx_misc_device = {
```

```
    .minor = MISC_DYNAMIC_MINOR,
    .name = "simple_etx_misc",
    .fops = &fops,
};

/*
** Misc Init function
*/
static int __init misc_init(void)
{
    int error;

    error = misc_register(&etx_misc_device);
    if (error) {
        pr_err("misc_register failed!!!\n");
        return error;
    }

    pr_info("misc_register init done!!!\n");
    return 0;
}

/*
** Misc exit function
*/
static void __exit misc_exit(void)
{
    misc_deregister(&etx_misc_device);
    pr_info("misc_register exit done!!!\n");
}

module_init(misc_init)
module_exit(misc_exit)

MODULE_LICENSE("GPL");
MODULE_AUTHOR("EmbeTronicX <embetronicx@gmail.com>");
MODULE_DESCRIPTION("A simple device driver - Misc Driver");
MODULE_VERSION("1.29");
```

1.3.1.3 3.1.3 Probe

Check the probe log to see if the device has been connected to the driver.

```
dmesg | grep -i yang
```

```
-# dmesg | grep -i yang
] ===yang's device[mydevice_debug_init][L:47]===
] ===yang's device[mydevice_probe][L:31]
#
```

1.4 4. How to view the device tree in linux

1.4.1 4.1 /proc/device-tree/

```
root@s5p6818:~# ls /proc/device-tree/
#address-cells   chosen          gpio_keys       memory          name            nx-v4l2         psci
#size-cells      compatible      i2c@10          model           nexell-ion@0    oscillator      reserved-memory
aliases          cpus            leds            mydevice@1      nx-devfreq      pmu             soc
```

This directory contains directories and files corresponding to nodes and properties defined in the Device Tree

1.4.2 4.2 /sys/firmware/devicetree/base/

```
root@s5p6818:~# ls /sys/firmware/devicetree/base/
#address-cells   chosen          gpio_keys       memory          name            nx-v4l2         psci
#size-cells      compatible      i2c@10          model           nexell-ion@0    oscillator      reserved-memory
aliases          cpus            leds            mydevice@1      nx-devfreq      pmu             soc
```

This path is like **/proc/device-tree/ but** is often preferred because it's more structured and designed to be easier to navigate programmatically.

1.4.3 4.3 Reading these directories and files

Reading them allows for obtaining information about the system's hardware configuration, such as peripheral addresses, interrupt numbers, and device parameters.

They can be read as described below.

1.4.3.1 4.3.1 Checking the child nodes of a device

```
ls /sys/firmware/devicetree/base/mydevice@1
```

1.4.3.2 4.3.2 Reading the properties

```
cat /sys/firmware/devicetree/base/mydevice@1/name
cat /sys/firmware/devicetree/base/mydevice@1/compatible
```

2 Memory set-up

2.1 1. Disable the default DRAM setup

First, modify common/fdt_support.c so that CONFIG_SYS_SDRAM_SIZE in u-boot `configs/s5p6818_bitminer.h does not automatically set up the memory` .

`fdt_fixup_memory_banks()` overrides the device tree and sets up the memory.

```
int fdt_fixup_memory_banks(void *blob, u64 start[], u64 size[], int banks)
{
    return 0;
```

2.2 2. Memory size

This section shows how to change the memory size by modifying the device tree.

2.2.1 2.1 Before changing the device tree

2.2.1.1 2.1.1 Device tree(s5p6818-bitminer-common.dtsi)

```
memory {
    device_type = "memory";
    reg = <0x40000000 0x0db00000>;
```

2.2.1.2 2.1.2 Check the memory size

```
dmesg | grep -i memory
```

```
Memory: 177728K/224256K available (6292K kernel code, 896K rwdata, 3948K rodata, 492K init, 634K bss, 30144K reserved,
```

2.2.2 2.2 After changing the device tree

2.2.2.1 2.2.1 Device tree(s5p6818-bitminer-common.dtsi)

```
memory {
    device_type = "memory";
    reg = <0x40000000 0xab00000>;
```

2.2.2.2 2.2.2 Check the memory size

```
dmesg | grep -i memory
```

```
Memory: 128576K/175104K available (6292K kernel code, 896K rwdata, 3948K rodata, 492K init, 634K bss, 30144K reserved,
```

2.3 3. Reserved memory

Reserved memory within the Linux kernel refers to portions of memory that are allocated for specific uses or devices and are not available for general-purpose use by the operating system's memory manager.

Below is about how to handle reserved memory using the device tree.

2.3.1 3.1 Before changing the device tree

2.3.1.1 3.1.1 Device tree(s5p6818.dtsi)

```
reserved-memory {
    #address-cells = <1>;
    #size-cells = <1>;
    ranges;
};

mydevice@1 {
        compatible = "yang,mydevice";
        /* Other necessary properties */
};
```

2.3.1.2 3.1.2 Check the memory

```
cat /proc/meminfo
```

```
root@s5p6818:~# cat /proc/meminfo
MemTotal:         194604 kB
```

```
dmesg | grep -i memory
```

```
Memory: 177728K/224256K available (6292K kernel code, 896K rwdata, 3948K rodata, 492K init, 634K bss, 30144K reserved,
```

2.3.2 3.2 After changing the device tree

2.3.2.1 3.2.1 Device tree(s5p6818.dtsi)

```
reserved-memory {
    #address-cells = <1>;
    #size-cells = <1>;
    ranges;
    my_reserved: buffer@0 {
        reg = <0x40000000 0x5000000>;
    };
};

mydevice@1 {
        compatible = "yang,mydevice";
        #memory-region = <&my_reserved>;
        /* Other necessary properties */
};
```

`memory-region` property can be used to allocate the reserved memory to a device.

2.3.2.2 3.2.2 Check if the reserved memory was added

```
cat /proc/meminfo
```

```
root@s5p6818:~# cat /proc/meminfo
MemTotal:         174144 kB
```

```
dmesg | grep -i memory
```

```
Memory: 157268K/224256K available (6292K kernel code, 896K rwdata, 3948K rodata, 492K init, 634K bss, 50604K reserved,
```

2.4 4. CMA(Contiguous Memory Allocator)

CMA allows for the allocation of large contiguous blocks of memory before or after the system has booted. It's particularly useful for devices that need to perform DMA operations requiring contiguous physical memory, such as video frame buffers or hardware that performs direct I/O. CMA helps mitigate the issue of memory fragmentation, making it easier to allocate large contiguous memory regions.

Below is about how to add a CMA memory pool using the device tree.

2.4.1 4.1 Device tree(s5p6818.dtsi)

```
reserved-memory {
    #address-cells = <1>;
    #size-cells = <1>;
    ranges;
    my_reserved: buffer@0 {
        #reg = <0x40000000 0x2000000>;
    };
    linux,cma {
        compatible = "shared-dma-pool";
        reusable;
        size = <0x4000000>;
        linux,cma-default;
    };
};
```

2.4.2 4.2 Check the added CMA memory

```
dmesg | grep -i memory
```

```
Reserved memory: created CMA memory pool at 0x0000000049800000, size 64 MiB
```