UNIT-4

Logico Programming.

Logic Programmy'ng i's a programming paradigm in which the set of sentences are weretten in Logical From. The dogical Programs consist of fact and rule. The logical programming can be define into form of Logical component and contral component

Logical programming = Logic + contral.

where Logic Represents the logic program in ferm of

fact and rule. and control represents how algorithm can be implemented by applying the suly in particular order.

the Concept of Logic Bogsaming is linked up with a language called prolog. prolog home is garerated by PROgraming and in LOGIC.
Swt-Prolog

1?- 10>5. trul 2?- 2+3>11. 3? unite (" Hello"). "Hellow

Logic broggmiey fact & Rede constitution

Anith

muty

list control in broley

Facts and Rules. A Prolog program consist of a number of clauses. each clause is either a fact or a rule. Prolog Interpreter saturate will give result according to the fact and rules.

Fact A fact must start with a bredicale (atom) and end with a full stop. bredicate may be fullowed by one or more argument which are enclosed by parentheses. argument can be constant, number, variables or list. Argument are seperated by columnas.

Syntax of Fact. perd (9291, 9292 -- ardN). argument left. Nome of predicall tests bo Swi-Prolog. man (anard). 11 ? - barent (anard, party). man (arun). true. womar (anuradha). 2? - parent (arun, anusquetha). parent (anand, parth) parent (anusatha, pasti). 37 parent (arun, anand), parent (arun, anusatha). fulse. ->open prolog and select consult. select the file name test 1. pl Rule: Arule can be viewed as an extension of a fact with added conclition that also have to be sectisfied ter it to be true. head: - body. head is a predicule diffinition just like (fact) :- is the neck symbol son dead as "if" Body is one or more goad (Query) example SWI-Bolog. testibl. 1? - father (x, Parth). man (anand). mar (arun). X = angral. woman (anusudha). 2 ? - mother (x, Parth). woman (Tayshree). X = any adha. Parent (anand, basth). Juck parent (anusadha, barth). parent (arun, anuractha).

parent (Jayshee, anuradla).

father (F,C): - man(F), Parent (F,C).

mether (M): mother (m, c): - woman(m), Parent (m, c) & pull.

Unification unification is a derivation of new rule from given by binding of variables. During Unification, any variable encountered is substitueted with the value of an appropriate Constant. This process is called binding

?-
$$f(A_11) = f(2, B)$$
.
 $A = 2$,
 $B = 1$.

?-
$$1*2+3=x+y$$

 $y=3$

is used for unification in prolog. Arithmatic. The operator = the corresponding value. the use of '=' fer binding with

?Bis 6+2 ?A=10+20. B=12 A=10+20. ? x 1'3 mod (7,2) ?- Ais 10+30. A=40

Lists inProlog. A lest i's an ordered sequence of object and list. in prolog list is uniten as its element separated by commas. and enclosed in bracketog. [a, b, c, d]. the list conferm Reacland trail part the first part of list called heard and rest of the lest called tail.

eq. 7- [a,b, c,d] = [A/B].

A = q.

Contral in Bolog: Contral represent how a language computes a kerpone to a query. The contral execution is based on two ribes. on two rules.

1- Goal Order - that means chase left mast subgood. 2. Rule Order - Hat mans select the first Appolo cable rule.

left mast subgasal family.pl. gfather(x, y): - father(x,z), parant (Z, y), parent(X,Y):- father (X,Y) Tree Repsontation of fact. /xfacts*/ father (dashrath, ram) father (raghu, a Ta). aJ9 father (aga, dashrath). dashrath father (ram, luv). father (ram, kush). Jam Kush. execution of family.pl. tor gfather (aTa, ram) the first rule i'd g father (x, y); father (x, 2) Parent (Z, y) ?- gfather(a7a, ram) gfeither (x, y): feither (a74,2), parant (2, y) toul. parent (dosrath, ram): - father (das ruth, ram). ? parant (x lut) 7 parent (x, luv) left most subgard Charegen X= ram. and search fact for their 80 Z= das harath

Concurrent Programming.

The term concurrency can be defined as they expression of the test in term of multiple interaction sub-tuste that can be potentially executed at the same time.

He intraction among processes take two terms.

- either through messages, or shared variable.
- Synchronization: It invalves, Synchronization of one task buth another generally thread execution is invalved in Synchronization.

created by Os. Boceys has its own address space, Aprogram com be broken down into one or more processes.

Thread is a lightwight process which makes were of the address of its pasent process for execution.

Busically Process creates one or more threads to

Concurrent programing livenus properties.

Parallelism in Synchronization. Cuncurrency interleaving hardware

Parallelism in Hardware.

Shored memony

Processor

Processor

Themosy

Ishared memory

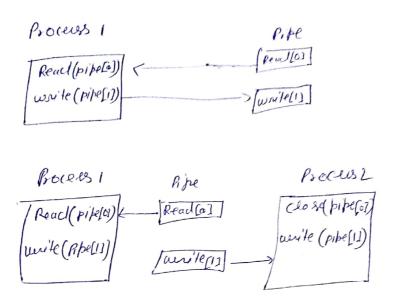
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Thenory

Synchronization Parallelism ruised the problem of synchronization acres to shared nesource. in multiprocessor system shared memory or in distributed System the Communication bet the processes is wring input and output.



Cyncudrency Interleaving:

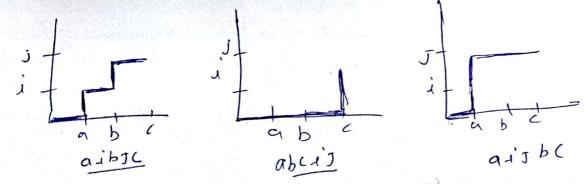
Interlecting of threads is a convenient technical device for studying the concurrent execution of processes. Bourdon the event in the threads the interleaving can be bassible.

eig pl consist three procus a, b and C

PZ Consist fue procing i and J.

The concurrent execution of pland PZ and garerate fullauing

interleaving. os 1Jaabc abcij, al'bgf. ---



brocers during which the bombulations broceed. e.g. deadlock, livelock, fairness.

Examples Dining bhilosopher Broklem: Solution to this problem:

There will Never be a situation of deadlock.

there will never be a situation of resource star cation.

And is a situation in which bhilosophus wants to
eat but does i't.

Deadlock: is a situation in which one process depends upon the resource held by another process.

liveLock: it is a kind of situation in which the execution is continued without any progress

Fairnes: it is a faind of situation in which any broces that wants to execute can execute in a finite amount of time.