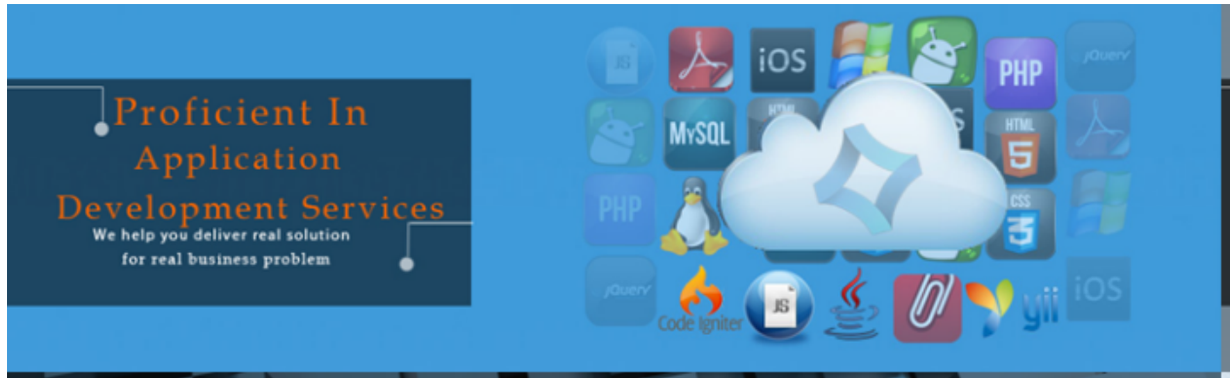
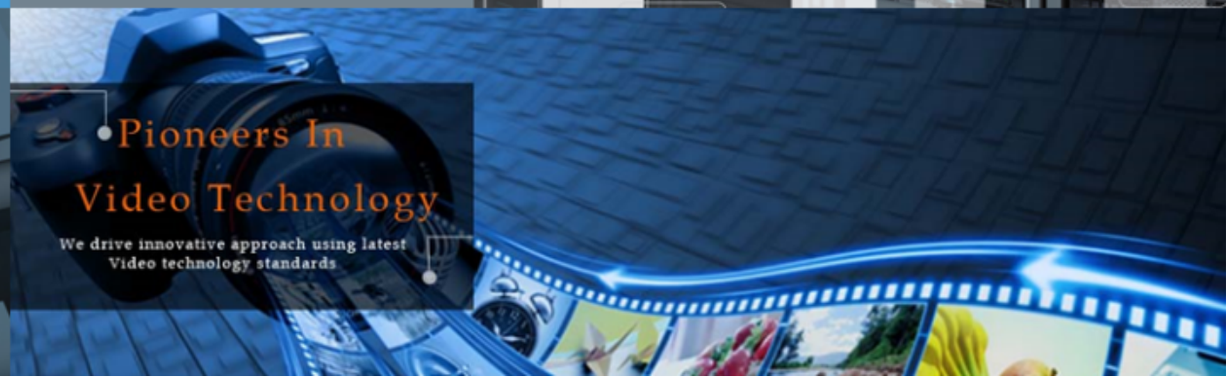




Downlink Report - Test results:

Test purpose :

The test purpose is to verify the DL Test

Test environment :

Normal test conditions.

Channel No	Channel Frequency [GHz]	BS Channel Bandwidth BW [MHz]	Measured EVM (RMS) [%]	EVM Limit [%]	Output Power [dbm]	Limit Low [dBm]	High Low [dBm]	Verdict
1	3.700005	100	1.98	2.5	23.04	21.5	25	PASS
2	3.700005	100	2.01	2.5	23.14	21.5	25	PASS

Channel 1 CRC Table

Channel	Slot	CRC Passed	Bit Length
PDSCH 1	0	True	84240
PDSCH 1	1	True	84240
PDSCH 1	2	True	84240
PDSCH 1	3	True	84240
PDSCH 1	4	True	84240
PDSCH 1	5	True	84240
PDSCH 1	6	True	84240
PDSCH 1	10	True	84240
PDSCH 1	11	True	84240
PDSCH 1	12	True	84240
PDSCH 1	13	True	84240
PDSCH 1	14	True	84240
PDSCH 1	15	True	84240
PDSCH 1	16	True	84240
PDSCH 2	0	True	792
PDSCH 2	1	True	792
PDSCH 2	2	True	792
PDSCH 2	3	True	792
PDSCH 2	4	True	792
PDSCH 2	5	True	792
PDSCH 2	6	True	792
PDSCH 2	10	True	792
PDSCH 2	11	True	792
PDSCH 2	12	True	792
PDSCH 2	13	True	792
PDSCH 2	14	True	792
PDSCH 2	15	True	792
PDSCH 2	16	True	792
PDSCH 3	7	True	35640
PDSCH 3	17	True	35640
PDSCH 4	7	True	252
PDSCH 4	17	True	252
PDCCH 1	0	True	108
PDCCH 1	1	True	108
PDCCH 1	2	True	108

Channel	Slot	CRC Passed	Bit Length
PDCCH 1	3	True	108
PDCCH 1	4	True	108
PDCCH 1	5	True	108
PDCCH 1	6	True	108
PDCCH 1	7	True	108
PDCCH 1	10	True	108
PDCCH 1	11	True	108
PDCCH 1	12	True	108
PDCCH 1	13	True	108
PDCCH 1	14	True	108
PDCCH 1	15	True	108
PDCCH 1	16	True	108
PDCCH 1	17	True	108

CRC PASS : 48 || CRC FAIL : 0

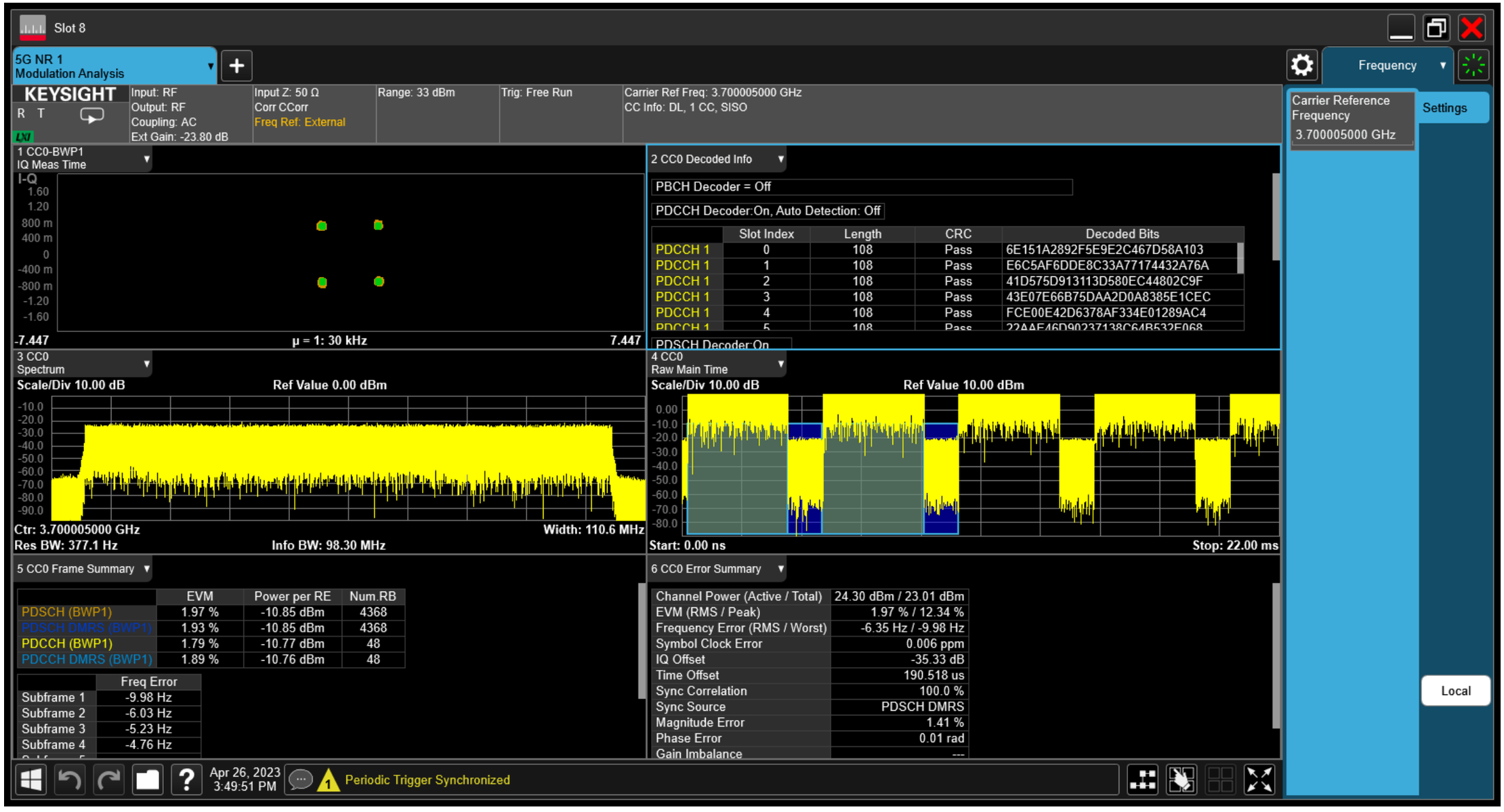
Channel 2 CRC Table

Channel	Slot	CRC Passed	Bit Length
PDSCH 1	0	True	84240
PDSCH 1	1	True	84240
PDSCH 1	2	True	84240
PDSCH 1	3	True	84240
PDSCH 1	4	True	84240
PDSCH 1	5	True	84240
PDSCH 1	6	True	84240
PDSCH 1	10	True	84240
PDSCH 1	11	True	84240
PDSCH 1	12	True	84240
PDSCH 1	13	True	84240
PDSCH 1	14	True	84240
PDSCH 1	15	True	84240
PDSCH 1	16	True	84240
PDSCH 2	0	True	792
PDSCH 2	1	True	792
PDSCH 2	2	True	792
PDSCH 2	3	True	792
PDSCH 2	4	True	792
PDSCH 2	5	True	792
PDSCH 2	6	True	792
PDSCH 2	10	True	792
PDSCH 2	11	True	792
PDSCH 2	12	True	792
PDSCH 2	13	True	792
PDSCH 2	14	True	792
PDSCH 2	15	True	792
PDSCH 2	16	True	792
PDSCH 3	7	True	35640
PDSCH 3	17	True	35640
PDSCH 4	7	True	252
PDSCH 4	17	True	252
PDCCH 1	0	True	108
PDCCH 1	1	True	108
PDCCH 1	2	True	108

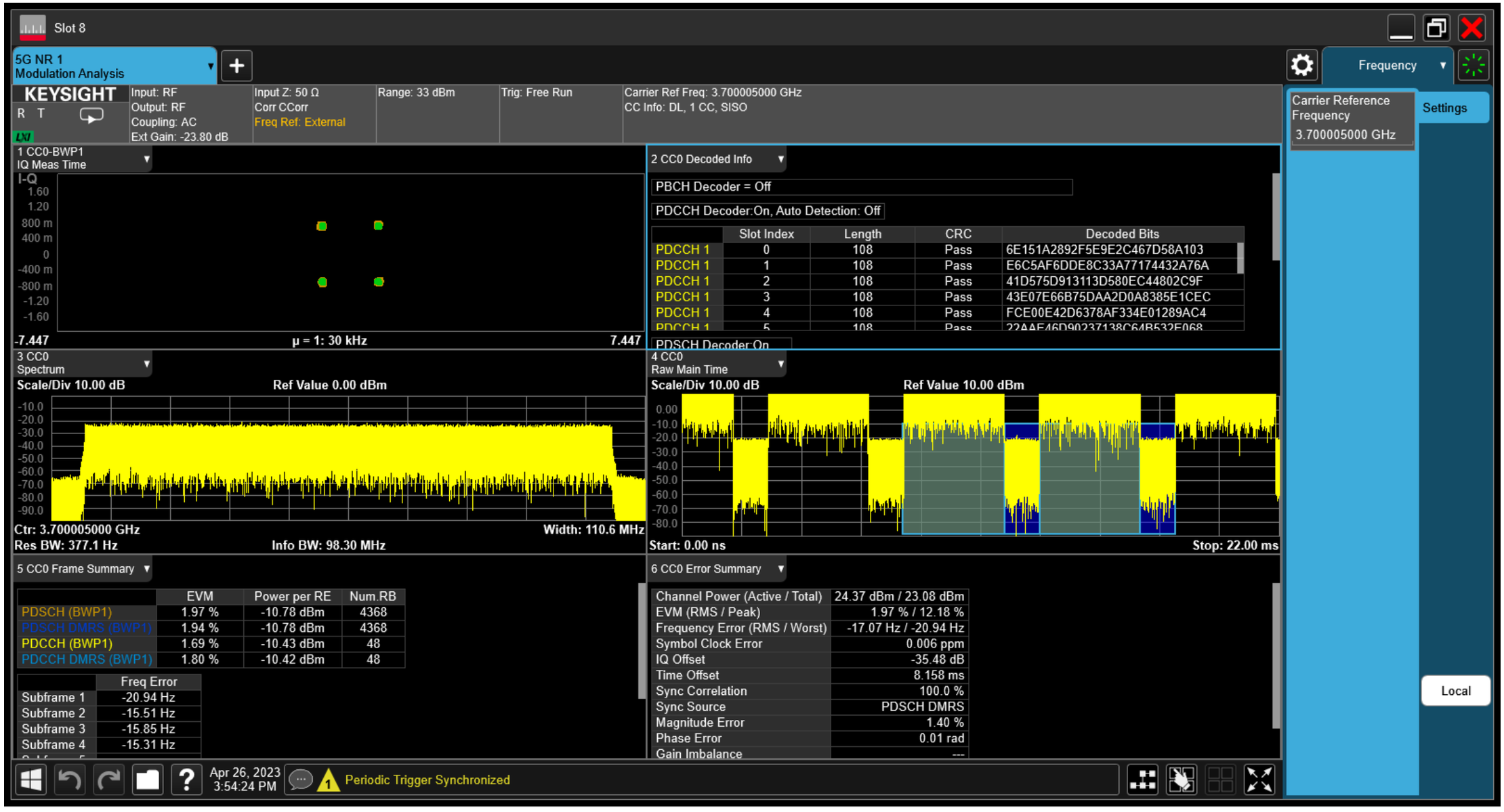
Channel	Slot	CRC Passed	Bit Length
PDCCH 1	3	True	108
PDCCH 1	4	True	108
PDCCH 1	5	True	108
PDCCH 1	6	True	108
PDCCH 1	7	True	108
PDCCH 1	10	True	108
PDCCH 1	11	True	108
PDCCH 1	12	True	108
PDCCH 1	13	True	108
PDCCH 1	14	True	108
PDCCH 1	15	True	108
PDCCH 1	16	True	108
PDCCH 1	17	True	108

CRC PASS : 48 || CRC FAIL : 0

Channel 1



Channel 2



Uplink - Test results:

Test purpose :

The test purpose is to verify the UL Test

Test environment :

Normal test conditions.

Channel No	Channel Frequency [GHz]	BS Channel Bandwidth BW [MHz]	Measured EVM (RMS) [%]	EVM Limit [%]	Verdict