No	Command	Function	Usage
1	atl_read		
2	atl_print		
3	_atl_convertToDag		
4	enum_init	*ModuleComputeInitialSet*	enum_init [-h] <module_name>
5	enum_post	*StateComputePostSet*	enum_post [-h] <state_name>
6	state_print	*StatePrintVariableValues*	state_print [-h] <state_name>
7	enum_search	*ModulePerformEnumerativeSearch*	enum_search [-h] [-d] <module_name>
8	enum_numVar	*EnumNumVarCmd*	
9	enum_var	*EnumVarCmd*	
10	enum_value	*EnumValueCmd*	
11	inv_read		
12	inv_print		
13	inv_check		
14	reinit	*MochaReinitialize*	
15	_mocha_end	*MochaEnd*	
16	atl_check		
17	show_mdls	*MdlShowModuleCmd*	show_mdls [haldg] [module]
18	save	*MdlSaveCmd*	
19	delete	*MdlDeleteCmd*	
20	show_vars	*MdlShowVariableCmd*	show_vars [-vHF|HD|EV|ALL] Module
21	compose	*MdlComposeCmd*	compose Module1 Module2
22	hide	*MdlHideCmd*	hide var1 var2 ... Module
23	ren	*MdlRenameCmd*	ren from_var to_var Module
24	let	*MdlLetCmd*	let Module be Module1
25	isEventVariable	*MdlIsEventVariableCmd*	isEventVariable module variable
26	isPrivateVariable	*MdlIsPrivateVariableCmd*	isPrivateVariable module variable
27	isInterfaceVariable	*MdlIsInterfaceVariableCmd*	isInterfaceVariable module variable
28	isExternalVariable	*MdlIsExternalVariableCmd*	isExternalVariable module variable
29	isHistoryFree	*MdlIsHistoryFreeCmd*	isHistoryFree module variable
30	isModuleUpdate	*MdlIsModuleUpdateCmd*	
31	updateModule	*MdlUpdateModuleCmd*	
32	show_atoms	*MdlShowAtomsCmd*	show_atoms module
33	show_components	*_MdlShowComponents*	
34	traverse	*MdlTraverseCmd*	
35	read_module	*PrsReadModuleCmd*	read_module [-hp] <filename>
36	read_intf	*PrsReadIntfCmd*	read_intf [-hp] <filename>
37	check_refine	*RefCheckNohiddenRefinement*	check_refine [-h] [-o <filename>] [-v] <implModule> <specModule>

38	check_refine_atom	RefCheckCompositionalRefinementOneAtom	check_refine_atom[-e] [-f 0|1|2|<var_name_list>] [-h] [-i <var_name_list>] [-k] [-o <ord_file_name>][-r] [-v] <impl_module> <spec_module> <spec_var_name>
39	check_simulation	RefCheckSimulation	check_simulate [-h] [-v] <implModule> <specModule>
40	ref_test	TestFunction	
41	rtm_trans	CommandBuildTrans	rtm_trans [-v] [-h] <module>
42	rtm_init	CommandComputeInitSet	rtm_init [-v] [-h] <module>
43	rtm_search	CommandComputeReachSet	rtm_search [-v] [-i] [-h] <module>
44	rtm_dynamic_var_ordering	CommandRtmDynamicVarOrdering	
45	rtm_static_order	CommandReadOrder	rtm_static_order [-h] [-f] <file> <module>
46	sim_start		
47	sim_end		
48	sim_choice		
49	sim_select		
50	sim_mode		
51	sim_prev_state_print		
52	sim_info		
53	sl_fsm	SlFSMCmd	sl_fsm [-h | -s <filename>] <module> ...
54	sl_printbdd	SlPrintBddCmd	
55	sl_printvar	SlPrintVarCmd	
56	sl_printsupport	SlPrintSupportCmd	
57	sl_pickminterm	SlPickMintermCmd	sl_pickminterm <outbdd> <inbdd>
58	sl_write_order	SlWriteOrderCmd	
59	sl_free_bdd	SlFreeBddCmd	
60	sl_dump_bdd	SlDumpBddCmd	sl_dump_bdd [-ha] [-f <filename>] <bdd>
61	sl_prime	SlPrimeCmd	sl_prime <outBdd> <inBdd>
62	sl_getCtrlVarIds	SlGetCtrlVarIdsCmd	sl_getCtrlVarIds <outIdArray> <inFsm>
63	sl_eQuantifyIds	SlEQuantifyIdsCmd	sl_eQuantifyIds <outBdd> <inBdd> <inIdArray>
64	sl_getMddSupport	SlGetMddSupportCmd	sl_getMddSupport <outIdArray> <inBDD>
65	sl_createIntArray	SlCreateIntArrayCmd	
66	sl_addToArray	SlAddToArrayCmd	sl_addToArray <inOuttIdArray> <integer>
67	sl_getVarNamesFromIds	SlGetVarNamesFromIdsCmd	sl_getVarNamesFromIdsCmd <outVarArray> <inIdArray>
68	sl_printObj	SlPrintObjCmd	
69	sl_pre_n	SlPreNCmd	sl_pre_n <outBdd> <inFSM> <inBdd> <numSteps>
70	sl_printAwaitDep	SlPrintAwaitDepCmd	
71	sl_printTcAwaitDep	SlPrintTCAwaitDepCmd	
72	sl_freeObj	SlFreeObjCmd	
73	sl_makeStringArray	SlMakeStringArrayCmd	
74	sl_isStateInBdd	SlIsStateInMddCmd	sl_isStateInBDD <inbdd> <inIdArray> <inIntArray>

75	sl_isVVStateInBdd	SlIsVVStateInMddCmd	sl_isVVStateInBDD <inbdd> <varName>=<value> ...
76	sl_makeIntArray	SlMakeIntArrayCmd	
77	sl_makeIdArray	SlMakeIdArrayCmd	
78	sl_getIdFromVarName	SlGetIdFromVarNameCmd	
79	sl_getVarNameFromId	SlGetVarNameFromIdCmd	
80	sl_getPrimedIdFromVarName	SlGetPrimedIdFromVarNameCmd	
81	sl_getPrimedIdFromId	SlGetPrimedIdFromIdCmd	
82	sl_getUnprimedIdFromId	SlGetPrimedIdFromIdCmd	
83	sl_primeIdArray	SlPrimeIdArrayCmd	sl_primeIdArray <outIdArray> <inIdArray>
84	sl_unprimeIdArray	SlUnprimeIdArrayCmd	sl_unprimeIdArray <outIdArray> <inIdArray>
85	sl_toTcl	SlToTclCmd	
86	sl_getPartitionedTr	SlGetPartitionedTRCmd	sl_getPartitionedTr <outBdd> <inFsm>
87	sl_bdd	SlBDDCmd	
88	sl_printawaitvar	SlPrintAwaitVarCmd	sl_printawaitvar <fsm> <var>
89	sl_make_intf	SlMakeIntfCmd	sl_make_intf <outIntf> <IFsm> <OFsm>
90	sl_make_intf_out	SlMakeIntfOutCmd	sl_make_intf_out <outIntf> <OFsm>
91	sl_print_levels	SlPrintLevelsCmd	
92	sl_compose_intf	SlComposeIntfCmd	sl_compose_intf <outIntf> <Intf1> <Intf2>
93	sl_check_intf_ref	SlCheckIntfRefCmd	sl_check_intf_ref <Intf1> <Intf2> <s|v>
94	sl_print_intf	SlPrintIntfCmd	
95	sl_strengthen_intf_inv	SlStrengthenIntfCmd	sl_strengthen_intf_inv <outIntf> <phi> <Intf> <s|v>
96	sl_print_intf_support	SlPrintIntfSupportCmd	
97	sl_copy	SlCopyCmd	sl_copy <outObj> <inObj>
98	sl_checkimpl	SlCheckImplCmd	sl_checkimpl <Bdd1> <Bdd2> (checks Bdd1 -> Bdd2)
99	sl_compose	SlComposeCmd	sl_compose <outFsm> <inFsm1> <inFsm2>
100	sl_wcontr	SlWcontrCmd	sl_wcontr <outBDD> <specBDD> <FSM> <s|v>
101	sl_modwcontr	SlModWcontrCmd	sl_modwcontr <outBDD> <specBDD> <FSMcontrolled> <FSMcontroller> <s|v>
102	sl_lcontr	SlLcontrCmd	sl_lcontr <outBDD> <specBDD> <FSM> <s|v>
103	sl_lcontr_n	SlLcontrNCmd	sl_lcontr_n <outBDD> <specBDD> <FSM> <n_iterations> <s|v>
104	sl_reach	SlReachCmd	sl_reach <outBDD> <FSM> <s|v>
105	sl_reach_histonly	SlReachHistoryCmd	sl_reach_histonly <outBDD> <FSM> <s|v>

106	sl_checkinv	SlCheckInvCmd	sl_checkinv <FSM> <BDD> <s|v>
107	sl_checktrinv	SlCheckTrInvCmd	sl_checktrinv <FSM> <BDD> <s|v>
108	sl_initrand	SlInitRandCmd	
109	sl_checkrandinv	SlCheckRandInvCmd	sl_checkrandinv <FSM> <BDD> <max_iter> <max_size>
110	sl_checksim	SlCheckSimCmd	sl_checksim <FSM1> <FSM2> <s|v>
111	sl_trinv	SlTrInvCmd	sl_trinv <outBDD> <FSM> <s|v>
112	sl_contrreachspec	SlContrReachSpecCmd	sl_contrreachspec <outBDD> <inFSM> <inCtrlBDD> <inSpecBDD> <s|v>
113	sl_contrreachfsm	SlContrReachFsmCmd	sl_contrreachfsm <outBDD> <inFSM> <inCtrlBDD> <inSpecFSM> <s|v>
114	sl_erase	SlEraseCmd	sl_erase <outFSM> <inFSM> <var>...
115	sl_and	SlAndCmd	sl_and <outBdd> <inBdd1> <inBdd2>
116	sl_or	SlOrCmd	sl_or <outBdd> <inBdd1> <inBdd2>
117	sl_not	SlNotCmd	sl_not <outBdd> <inBdd>
118	sl_true	SlTrueCmd	
119	sl_restrict	SlRestrictCmd	sl_restrict <outFsm> <inBdd> <inFsm>
120	sl_getinit	SlGetInitCmd	sl_getinit <outBdd> <inFsm>
121	sl_gettr	SlGetTRCmd	sl_gettr <outBdd> <inFsm>
122	sl_bddsize	SlBddSizeCmd	
123	sl_predsize	SlPredSizeCmd	sl_predsize <inFsm> <inBdd>
124	sl_maketrinv	SlMakeTrInvCmd	sl_maketrinv <outBdd> <inBdd>
125	sl_getfsmvars	SlGetFsmVarsCmd	sl_getfsmvars <FSM> <'erased'/'controlled'/'external'>
126	sl_makevarlist	SlMakeVarListCmd	sl_makevarlist <varlist> <var1> <var2> ...
127	sl_conjoin_and_copy	SlConjoinAndCopyCmd	sl_conjoin_and_copy <outBdd> <inBdd1> <inBdd2>
128	sl_ran_invchk	SlRandomcubeInvChkCmd	sl_ran_invchk <FSM> <max_iterations> <seed> <s|v> <BDD1> <BDD2>...
129	sl_inv_buildMdd	SlInvBuildMdd	sl_inv_buildMdd <module_name> [<inv_name> <inv_name>...] <s|v>
130	sl_tau_plus_and_tau_minus		sl_tau_plus_and_tau_minus <outPlusMdd> <outMinusMdd> <FSM> <one|all> <abstract|notabstract> <s|v> <list of controlled vars of the module to be abstracted , or list of vars to be not abstracted, depending on the <abstract|notabstract> parameter >...
131	sl_mdd_substitute	SlMddSubstitute	sl_mdd_substitute <MDD_in> <MDD_out> <var_num> <old_vars...> <new_vars...>
132	sl_better_upre_comb	SlBetterUPreComb	sl_better_upre_comb <outBDD> <specBDD> <FSM1> <FSM2> <mealy|moore> <abstract|notabstract> <time in seconds> <s|v> <var>...
133	sl_better_upre	SlBetterUPre	sl_better_upre <outBDD> <specBDD> <FSM1> <FSM2> <tauPlusMdd> <tauMinusMdd> <mealy|moore> <abstract|notabstract> <s|v> <var>...
134	sl_mdd_equal	SlMddEqual	sl_mdd_equal <BDD> <BDD>

135	sl_atom_simulate	*SlAtomSimulateCmd*	sl_atom_simulate <FSM> <seed> <max_iterations> <s|v> <BDD1> <BDD2>...
136	sl_sort_and_conjoin _tau		sl_sort_and_conjoin_tau <fsm_name> <maxMddSize> <s|v>
137	sym_trans	*CommandBuildTrans*	sym_trans [-v] [-h] <module>
138	sym_init	*CommandComputeInitSet*	sym_init [-v] [-h] <module>
139	sym_search	*CommandComputeReachSet*	sym_search [-v] [-h] <module>
140	sym_post	*CommandComputePostSet*	sym_post [-v] [-h] <region>
141	sym_union	*CommandComputeUnion*	sym_union <region1> <region2>
142	sym_print	*CommandPrintRegion*	sym_print [-h] <region>
143	sym_dynamic_var_o rdering	*CommandDynamicVarOrdering*	
144	sym_write_order	*CommandWriteOrder*	sym_write_order [-h] [-f] <file> <module>
145	sym_static_order	*CommandReadOrder*	sym_static_order [-h] [-f] <file> <module>
146	show_types	*VarShowTypesCmd*	show_type [-lg] <module>