

RAMAIAH INSTITUTE OF TECHNOLOGY, BANGALORE – 560 054.

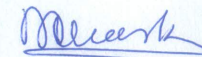
TIME – TABLE

DEPARTMENT: Electronics & Communication Engineering		SEMESTER: V
SECTION: A	TERM: 04th October 2023 To 13st Jan 2024	ROOM NO - LHC 407

TIME DAY	09:00 AM To 09:55 AM	09:55 AM To 10:50 AM	11: 05 AM To 12: 00 PM	12:00 PM To 12:55 PM	12:55 PM To 01: 45 PM	01: 45PM To 02: 40 PM	02: 40 PM To 03: 35 PM	03: 35 PM To 04: 30 PM
Monday	←5 th A ₁ VLSI Lab→VA + SU ←5 th A ₂ MW Lab→ SB + SVC		← CMOS VLSI Lab → DBK+ USP		BREAK	PEC1 (RV/SVC/SMG)	RM &IPR (VA)	
Tuesday	PEC1 (RV/SVC/SMG)	← CS II → (CM)	CCN (CGR)	RM &IPR (VA)		MW (SB)	←5 th A ₂ VLSI Lab→ DBK + MNB ←5 th A ₁ MW Lab→ IR +SVC	
Wednesday	← 5 th A ₁ CCN LAB→ (CGR+)VLSI Lab ← 5 th A ₂ CCN LAB→ (RPP+) AEC Lab		MW (SB)	CMOS VLSI (DBK)		CMOS VLSI (DBK)	← CS II → (CM)	
Thursday	CMOS VLSI (DBK)	CCN (CGR)	← CS II → (CM)	MW (SB)		PEC1 (RV/SVC/SMG)	RM &IPR (VA)	ES USP
Friday	← MW Tuto → (SB+JS)		PEC1 (RV/SVC/SMG)	CCN (CGR)		AEC		
Saturday	RM &IPR (VA)	← CS II → (CM)						

Subject	Subject Code	Faculty (Initials and Name)
Communication System-II	EC51	CHITRA M (CM)
Computer Communication Network	EC52	Dr. C G RAGAVENDRA (CGR)
CMOS VLSI Design	EC53	Dr. DEEPALI KOPPAD B (DBK)
Microwave Devices & antennas	EC54	Dr. SUJATHA B (SB)
Programme Elective Course- 1	ECE551	SVC+RV+SMG
VLSI Lab	ECL56	VA,DBK,MNB,SU
Microwave Lab	ECL57	SB,SVC,IR
Research Methodology & Intellectual Property Rights	ECL58	Dr. V. ANANDI (VA)
Ability Enhancement Course-5	AEC59	
Environmental Studies (Audit course)	HS510	PAVITHA U S (USP)

Date: 29-09-2023


Signature of HoD
 Department of Electronics & Communication Engineering
 M.S. Ramaiah Institute of Technology
 (Autonomous Institute, Affiliated to VTU)
 Bangalore - 560 054

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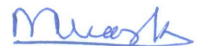
TIME – TABLE

DEPARTMENT: Electronics & Communication Engineering			SEMESTER: V
SECTION: B	TERM: 04th October 2023 To 13th Jan 2024	ROOM NO - LHC 408	

<div>TIME</div> <div>DAY</div>	09:00 AM To 09:55 AM	09:55 AM To 10:50 AM	11: 05 AM To 12: 00 PM	12:00 PM To 12:55 PM	12:55 PM To 01: 45 PM	01: 45PM To 02: 40 PM	02: 40 PM To 03: 35 PM	03: 35 PM To 04: 30 PM
Monday	CS II (CGR)	CMOS VLSI (RS)	← 5 th B ₁ CCN LAB→ (FF+) VLSI Lab ← 5 th B ₂ CCN LAB→ (MM+) AEC Lab		BREAK	PEC1 (RV/SVC/SMG)	CCN (FF)	CS II (CGR)
Tuesday	PEC1 (RV/SVC/SMG)	MW (VNP)	←5 th B ₁ VLSI Lab→USP + SU ←5 th B ₂ MW Lab→FF + SVC			ES RH	CMOS VLSI (RS)	RM & IPR (MVK)
Wednesday	MW (VNP)	CMOS VLSI (RS)	CCN (FF)	CS II (CGR)		RM & IPR (MVK)		
Thursday	CCN (FF)	RM & IPR (MVK)	← MW Tuto → (VNP+RV)			PEC1 (RV/SVC/SMG)	←5 th B ₂ VLSI Lab→RS +SU ←5 th B ₁ MW Lab→JS + RPP	
Friday	RM & IPR (MVK)	CS II (CGR)	PEC1 (RV/SVC/SMG)	MW (VNP)		AEC		
Saturday	← CMOS VLSI Tuto → (RS+MNB)							

Subject	Subject Code	Faculty (Initials and Name)
Communication System-II	EC51	Dr. C G RAGAVENDRA (CGR)
Computer Communication Network	EC52	FLORY FRANCIS (FF)
CMOS VLSI Design	EC53	Dr. S RAGHU RAM (RS)
Microwave Devices & Antennas	EC54	V NUTHAN PRASAD (VNP)
Programme Elective Course- 1	ECE551	SVC+RV+SMG
VLSI Lab	ECL56	RS, USP, SU
Microwave Lab	ECL57	SVC, FF, JS, RPP
Research Methodology & Intellectual Property Rights	ECL58	Dr. MAYA V KARKI (MVK)
Ability Enhancement Course-5	AEC59	
Environmental Studies (Audit course)	HS510	Dr. ROOPA HEGDE (RH)

Date: 29-09-2023



Signature of HoD

Professor & Head

Department of Electronics & Communication Engg
M.S. Ramaiah Institute of Technology
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Bangalore - 560 054

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TIME – TABLE

DEPARTMENT: Electronics & Communication Engineering				SEMESTER: V	
SECTION: C		TERM: 04 th October 2023 To 13 th Jan 2024		ROOM NO.: LHC-409	

<div>TIME</div> <div>DAY</div>	09:00 AM To 09:55 AM	09:55 AM To 10:50 AM	11: 05 AM To 12: 00 PM	12:00 PM To 12:55 PM	12:55 PM To 01: 45 PM	01: 45PM To 02: 40 PM	02: 40 PM To 03: 35 PM	03: 35 PM To 04: 30 PM
Monday	RM & IPR (RH)	CS II (TDS)	← MW Tuto → (IR+RPP)		BREAK	PEC1 (RV/SVC/SMG)	←5 th C ₁ VLSI Lab→MNB+USP ←5 th C ₂ MW Lab→VNP+SVC	
Tuesday	PEC1 (RV/SVC/SMG)	MW (IR)	RM & IPR (RH)	CS II (TDS)		CMOS VLSI (MNB)	CCN (MM)	
Wednesday	RM & IPR (RH)	CCN (MM)	← CMOS VLSI Tuto → (MNB+SU)			← 5 th C ₁ CCN LAB→ (CGR)VLSI Lab ← 5 th C ₂ CCN LAB→ (MM) AEC Lab		
Thursday	←5 th C ₁ MW Lab→IR+JS ←5 th C ₂ VLSI Lab→VA+USP		CMOS VLSI (MNB)	CCN (MM)		PEC1 (RV/SVC/SMG)	RM & IPR (RH)	
Friday	CS II (TDS)	MW (IR)	PEC1 (RV/SVC/SMG)	CMOS VLSI (MNB)		AEC		
Saturday	MW (IR)	CS II (TDS)	ES SU					

Subject	Subject Code	Faculty (Initials and Name)
Communication System-II	EC51	Dr. T D SENTHIL KUMAR (TDS)
Computer Communication Network	EC52	Dr. MAMTHA MOHAN (MM)
CMOS VLSI Design	EC53	Dr. M. NAGABUSHANAM (MNB)
Microwave Devices & Antennas	EC54	Dr. S IMACULATE ROSALINE (IR)
Programme Elective Course- 1	ECE551	SVC+RV+SMG
VLSI Lab	ECL56	VA,MNB,USP
Microwave Lab	ECL57	SVC,VNP,JS,IR
Research Methodology & Intellectual Property Rights	ECL58	Dr. ROOPA HEGDE
Ability Enhancement Course-5	AEC59	
Environmental Studies (Audit course)	HS510	Dr. SHIPRA UPADHYAY (SU)

Date: 29-09-2023


 Signature of HoD