

Vaibhav Murali

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OBJECTIVE	Interested in the field of Instrumentation and Electronics Engineering			
EDUCATION	University of Southern California (USC)	CGPA: 3.6/4.0		
	Master of Science (M.S.), Biomedical Engineering (Electrical Emphasis)	May 2019		
	SSN College of Engineering, Anna University	CGPA: 8.01/10.0		
	Bachelor of Engineering (B.E.), Biomedical Engineering	June 2017		
TECHNICAL SKILLS	Languages	C, C++, Python, Bash Programming		
	Software	Arduino IDE, Atmel Studio, MATLAB, LABVIEW, LT-Spice, Eagle, Cadence Virtuoso		
	Hardware	Oscilloscopes, Signal Generator, Soldering, DAC, ADC, DMM, Electrical Safety Analyzers		
	Platform	Arduino, Intel 8051, Cadence Allegro, TI MSP 430, Solidworks, PIC, Raspberry Pi, BioRadio		
EXPERIENCE	Electronics Engineer	June 2019		
	NOWDx Instrument Division (NID)	Los Angeles, CA		
	<ul style="list-style-type: none">• Design of PCBs (Rigid & Flex, Multilayer PCBs) using Eagle & Cadence Allegro• Creating test environment by making PCBs in-house using through-hole & SMD components• Verification and validation of PCBs using Python & Bash programming• Debugging electronic circuits using DMM & oscilloscopes• Writing documents according to 21CFR820 standard.			
	Engineering Intern	April 2019		
	NOWDx Instrument Division (NID)	Los Angeles, CA		
	<ul style="list-style-type: none">• Responsible for testing of PCBs using python & bash programming. Also, responsible for collecting data & reporting issues to project manager			
	Graduate Teaching Assistant	August 2018		
	University of Southern California	Los Angeles, CA		
	<ul style="list-style-type: none">• Assisted in setting up, monitoring, grading exams & laboratory experiments in instrumentation laboratory and signal processing laboratory. Also, taken classes for engineering graduate students			
	Laboratory Student- Digital MOS VLSI	August 2018		
University of Southern California	Los Angeles, CA			
<ul style="list-style-type: none">• Design of circuits involving area, delay & power minimisation. Includes design, layout, extraction, simulation & automatic synthesis				
PROJECTS	Cast Simulator			
	<ul style="list-style-type: none">• Designing a model arm embedded with temperature and pressure sensors to provide real-time feedback to surgeons• Working in collaboration with Children’s Hospital Los Angeles (CHLA)			
	Design of Artificial Neuron			
	<ul style="list-style-type: none">• Implemented Mealy Machine circuit using Cadence Virtuoso• Involved flipflops & compound gates to replicate the firing of neurons			
	Laboratory Model of a Low-Cost Dialysis Machine			
	<ul style="list-style-type: none">• Headed a team of three to model a low cost dialysis machine using refurbished materials & cost effective electronic components• Engineered a machine that performs basic operations such as monitoring pressure, temperature & detecting air bubbles present inside blood drawn from patient			
	Design of ultrasound airflow transducer			
	<ul style="list-style-type: none">• Developed an ultrasound transducer model in LT SPICE and simulated it• Replicated model for three flow rates and observed linear relationship of volume over time			
	COURSEWORK	<ul style="list-style-type: none">• Graduate: MOS VLSI Circuit design, BIO-MEMS and Nanotechnology, Applied Electrophysiology, Bioinstrumentation, Ultrasonic Imaging, Signals & Systems• Undergraduate: Bio-Optics, Digital Image Processing, Analog and Digital Integrated Circuits, Neural Networks, OOPS & Data Structures, Biomechanics, Sensors & Measurements		
WORK AUTHORIZATION	<ul style="list-style-type: none">• Eligible to work in the United States of America under Optional Practical Training (OPT)• Would require H1B visa sponsorship			