

# Lower Bounds and Separations for Torus Polynomials

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# Main Goal

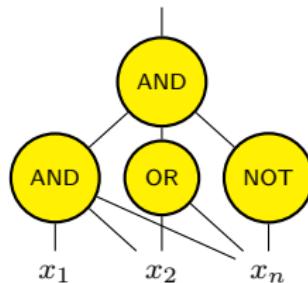
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Resolve Barrington's conjecture on constant-depth circuits.

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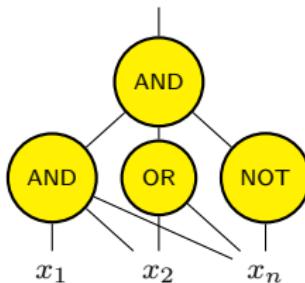
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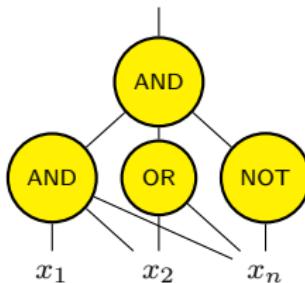


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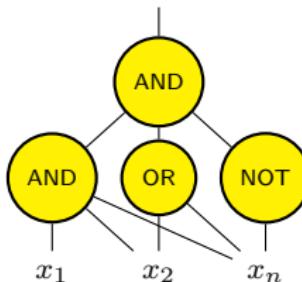
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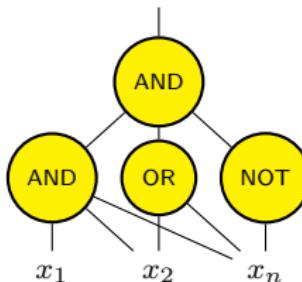
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$\text{AC}^0$ : Constant-depth polynomial size with AND, OR, NOT gates.

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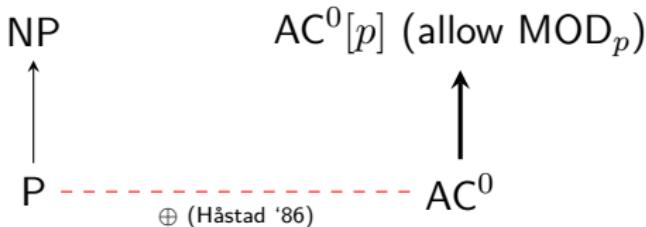


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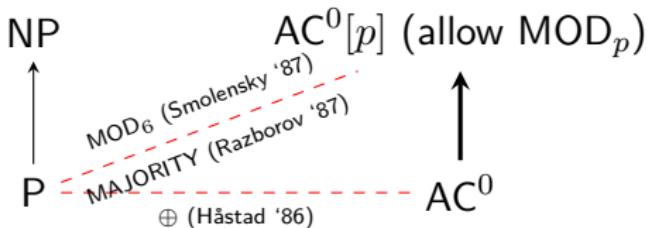
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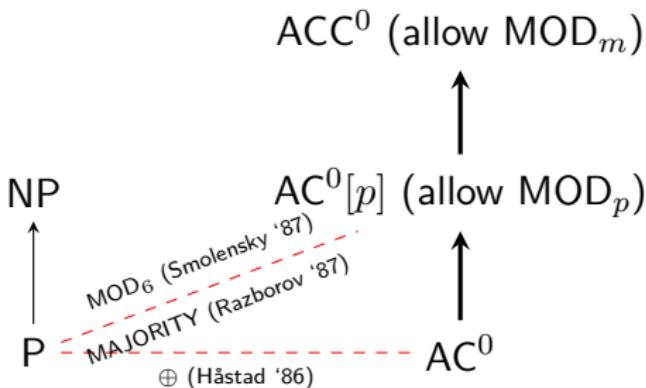
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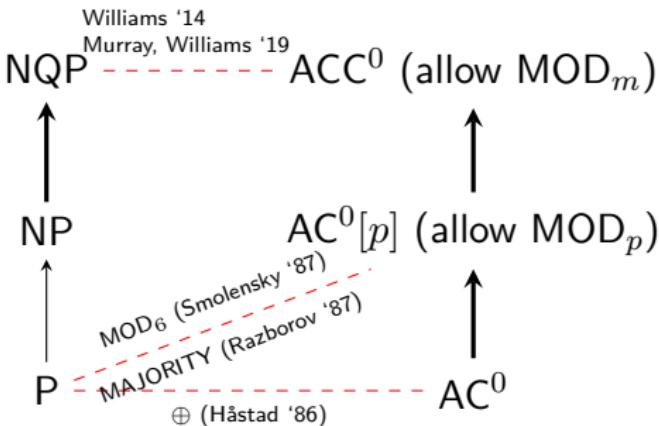
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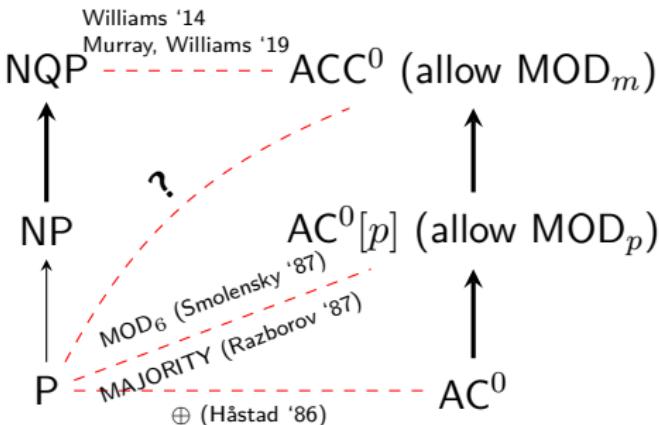
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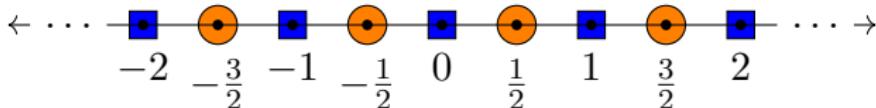
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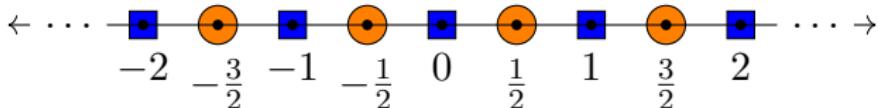
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Trivial upper bound: degree  $n$  for any  $f$ .

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- ▶ Lower bound iff programs are infeasible iff duals are feasible.

# The Family of Duals

- For each  $Z$ , find  $\gamma \in \text{nullspace}(M(n, d))$ , such that :

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- Allows for incremental progress.

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- ▶ New nullspace vectors supported on a single Hamming layer.
  - ▶ Asymmetric construction, unlike previously known.

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## Future Directions

- ▶ Continue the program to find feasible solutions for more  $Z$ s.
  - ▶ Characterize “solved”  $Z$ s using known solutions.
  - ▶ Find more possible solutions.
- ▶ Bridge the lower-upper bound gap for AND.
  - ▶ Current proof uses only one solution.
  - ▶ Use multiple solutions for stronger lower bound.
- ▶ Error-degree trade-off for symmetric torus polynomials approximating AND.
  - ▶ Use lattice theory.

Thank you

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