

VAGICHERLA MANIKANTA

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Career Objective

I am a passionate electronics engineering student with a strong foundation in digital electronics, Verilog HDL, and VLSI design. I have completed a successful internship in Sri Shasha Prayathi technologies, where I gained hands-on experience in FIR filter design using Verilog. I am eager to apply my skills and knowledge in a challenging role to contribute to the advancement of the field.

Educational Qualification

Qualification	Institution	Year of Passing	GPA
B. Tech (ECE)	Bapatla Engineering College	Pursuing final year	7.9
Intermediate	Sri Gayathri Vidhya Parishad Junior College	2020	9.8
10 th Class	Z P High School (Boys)	2018	8.8

Technical Skills

- Programming languages: C, python
- Web Technologies:HTML5, CSS3
- Hardware Languages: Verilog HDL
- Core Skills: Digital Electronics, VLSI Design Flow , Basic Electronics.

Certification courses:

- HTML5 course completion Certificate from Infosys Springboard. - 7th oct 2023
- IOT course completion Certificate from Infosys Springboard. - 26th sept 2023
- CSS3 course completion Certificate from Infosys Springboard. - 2nd Dec 2023

Internship

SkillDzire, Hyderabad

- 45 Days of Internship Training on IOT.

June 2022

Sri Shasha Prayathi Technologies Pvt.lmtd, Mangalore

- 45 Days of Internship Training on VLSI Signal Processing.

May 2023

Oasis Infobyte

- 30 Days AICTE OIB-SIP Internship in Web Development and Designing.

Nov 2023

GitHub Link: <https://github.com/vaicherlamani/OIBSIP>

Minor Projects:

- I had completed project on **FIR Filter Design** using Verilog HdL as a part of Internship on vlsi signal Processing.
- Designing a Combinational Circuits Like Adders, subtractors, Encoders, Decoders, Mux and Demux in Digital Electronics using Verilog HDL in Xilinx Vivado.
- Sequential Circuits Like Flipflops, Counters, Registers, Shift Registers and ALU using Verilog HDL
- I had Completed projects like **Landing Page, Portfolio, Temperature Converter** Websites are designed in Oasis Infobyte Internship.

Declaration

I do hereby declare that all the above information is true to the best of my knowledge and belief.

Signature: V.Manikanta