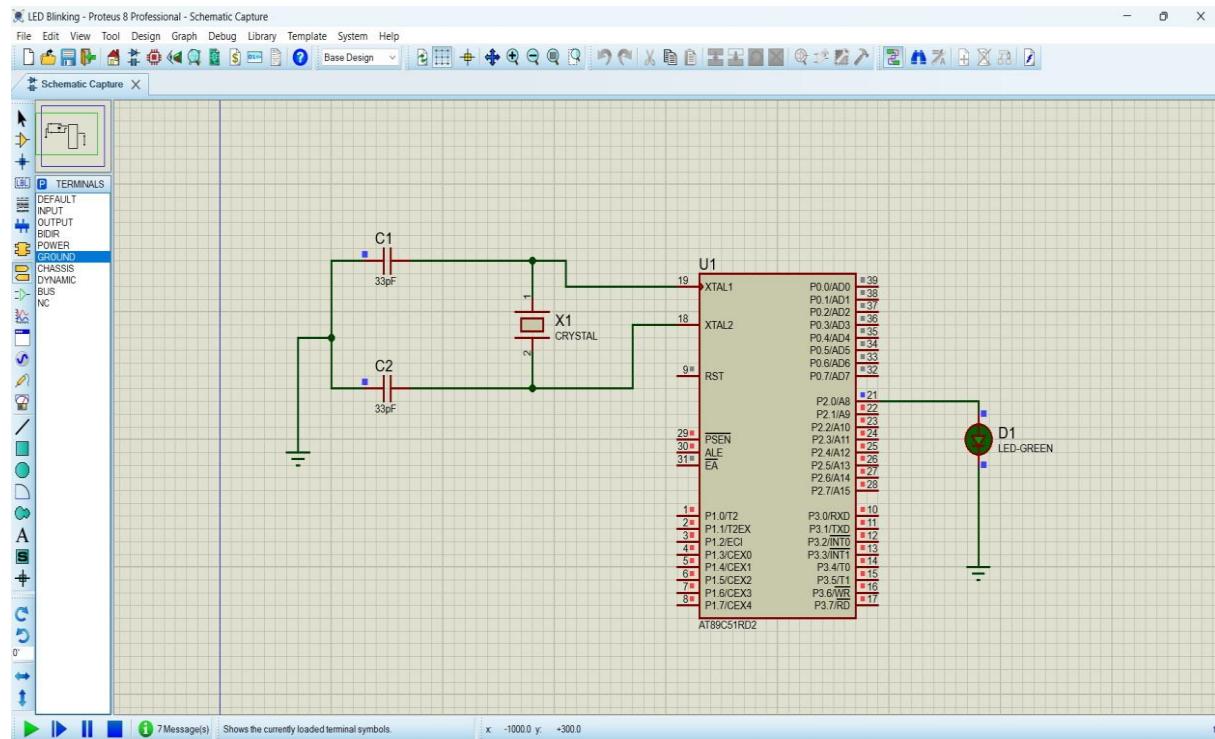


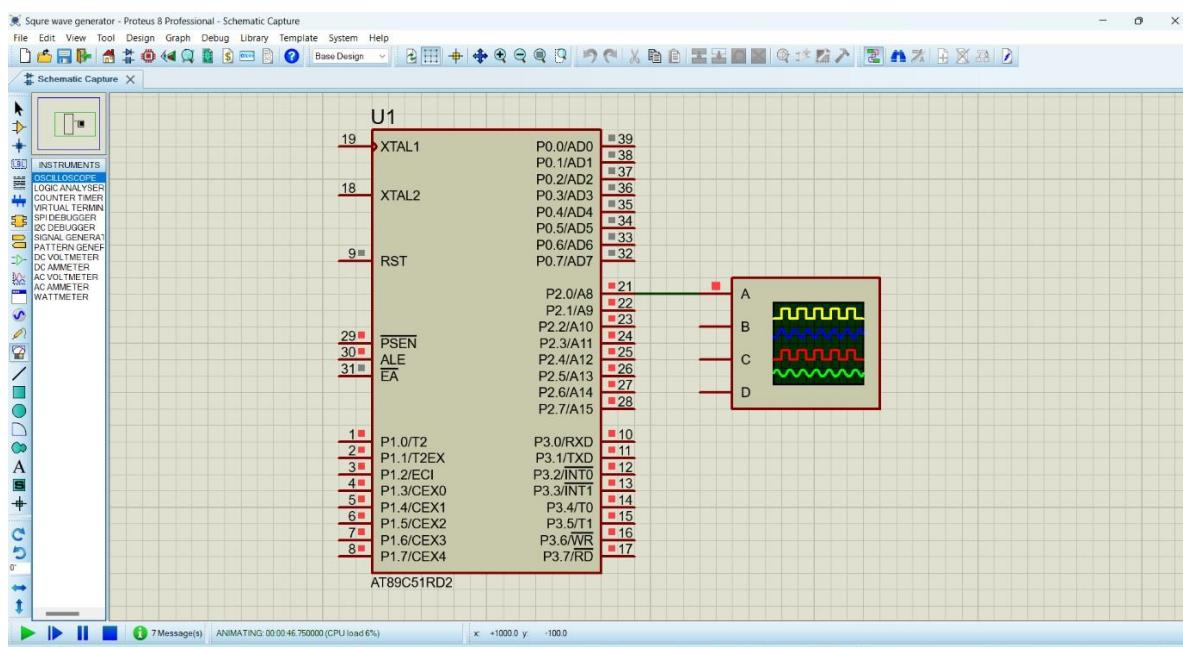
EX-2

DIAGRAM:

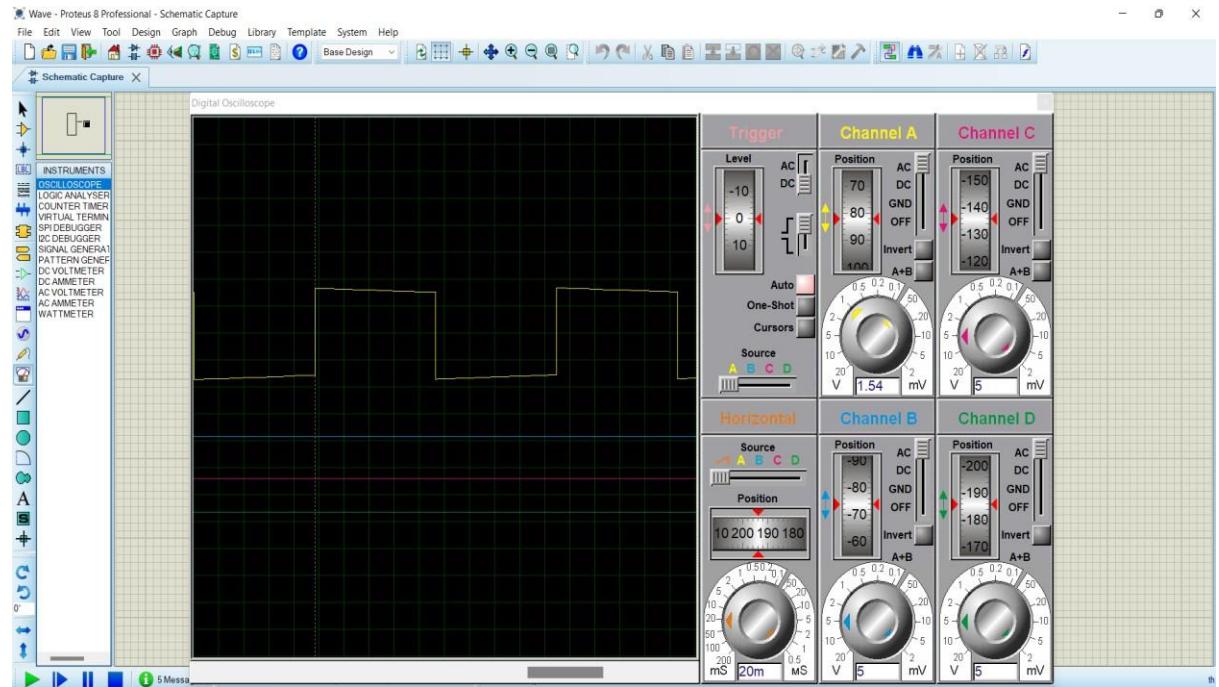


Exp No: 3

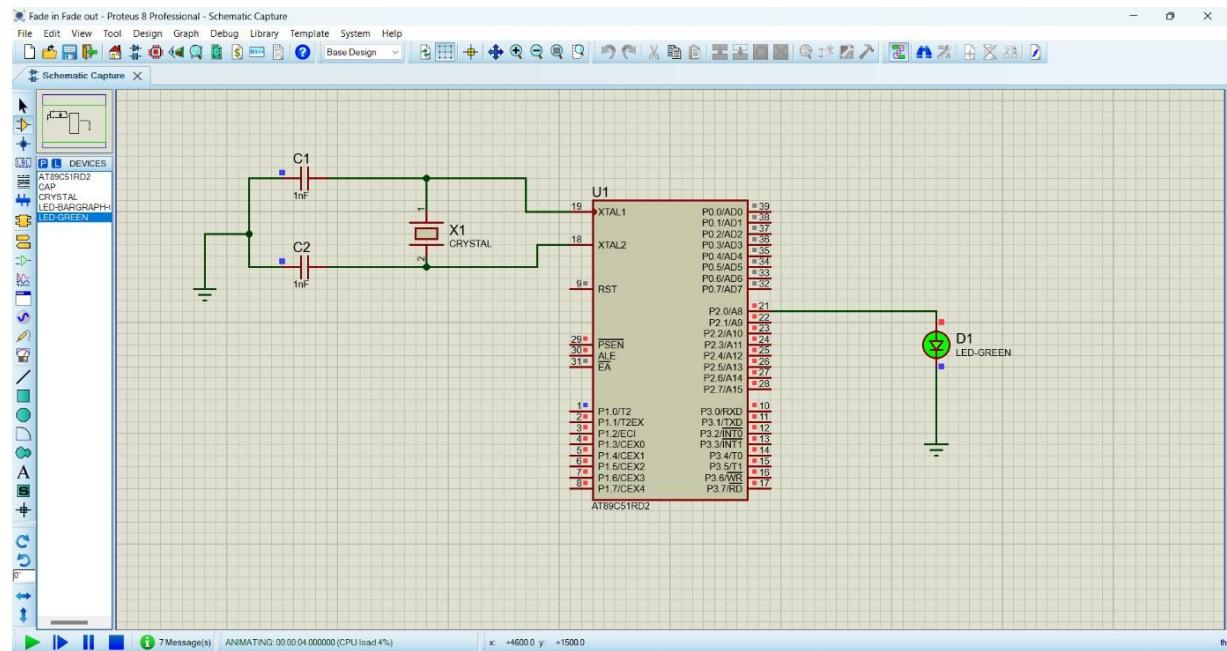
CIRCUIT DIAGRAM:



OUTPUT

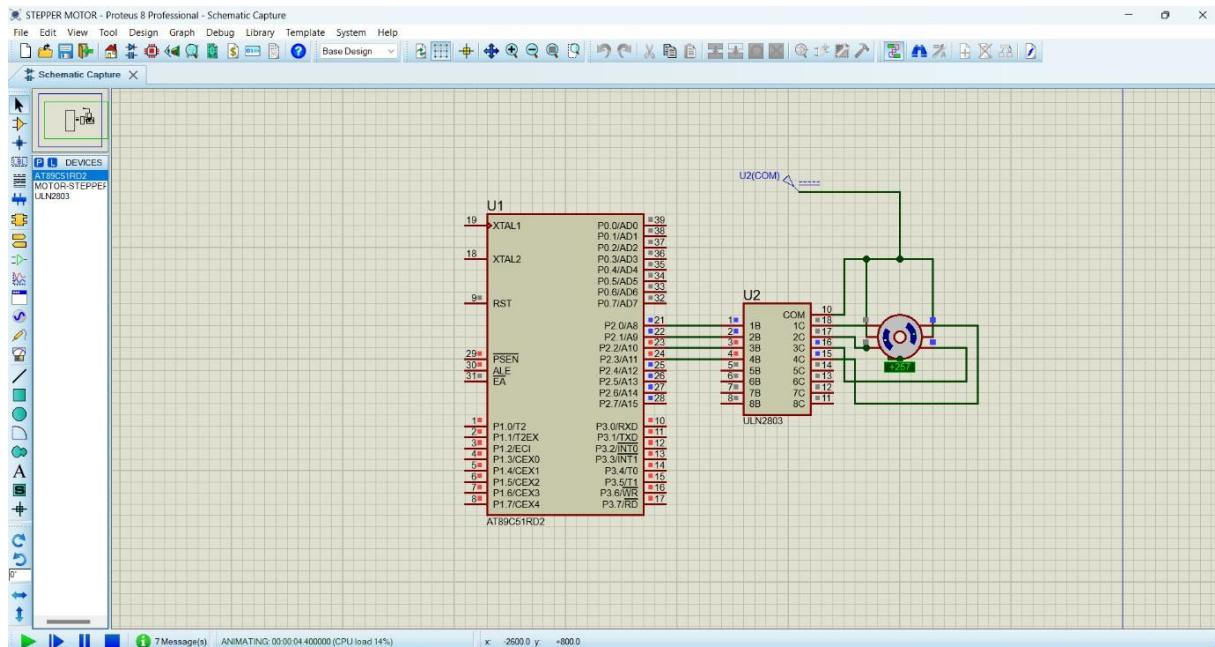


EX-4



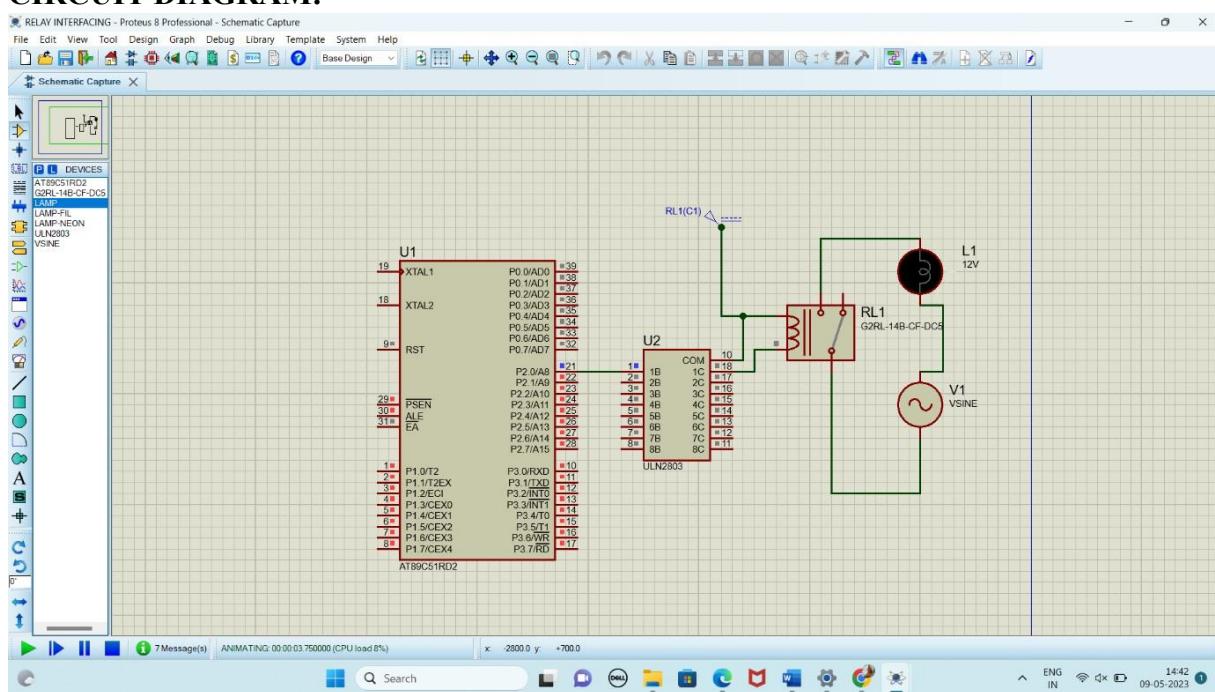
Exp No: 5

CIRCUIT DIAGRAM:



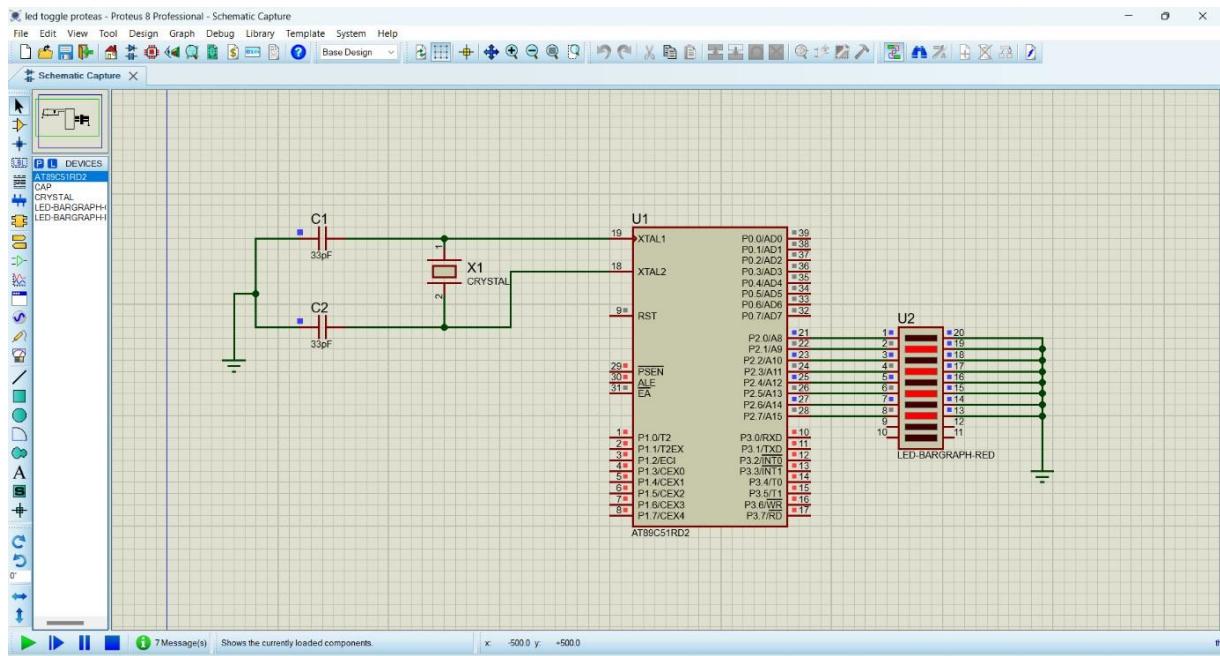
Exp No: 6

CIRCUIT DIAGRAM:

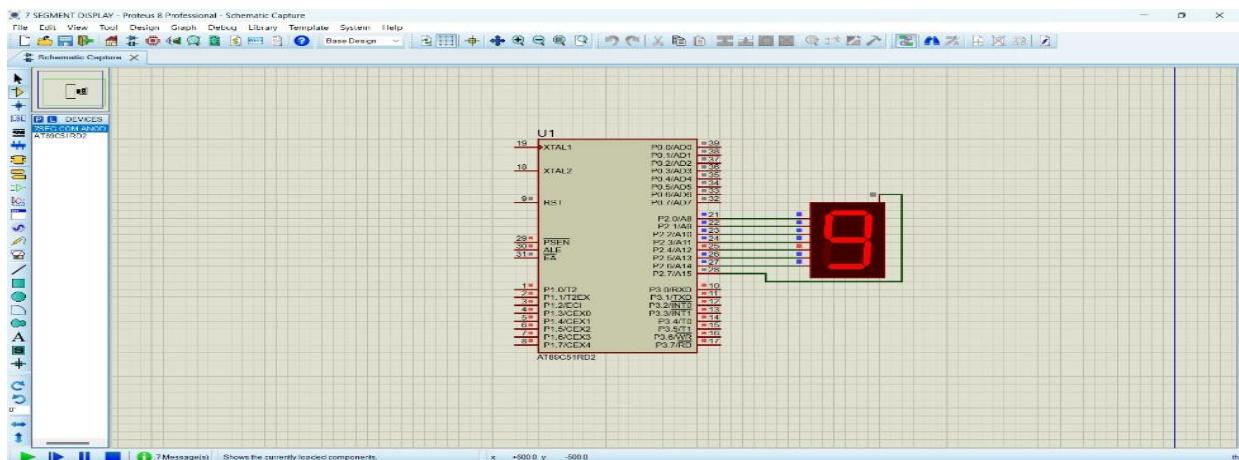


Exp No: 7

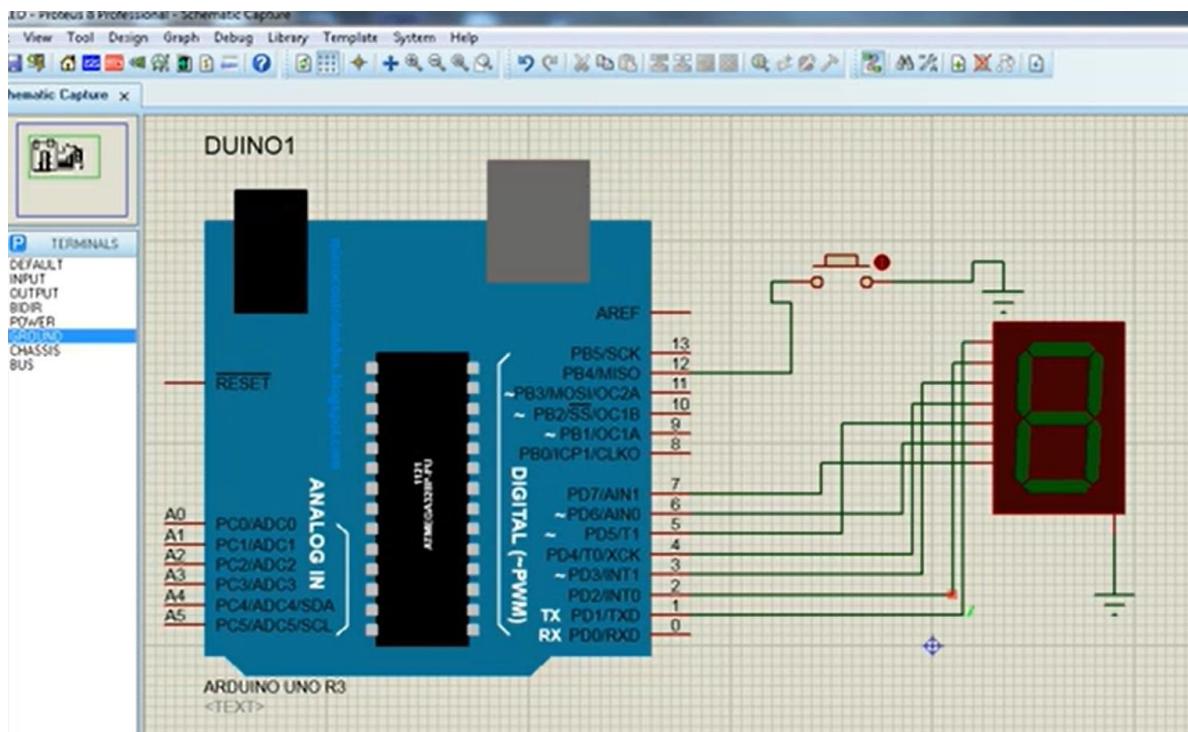
CIRCUIT DIAGRAM:



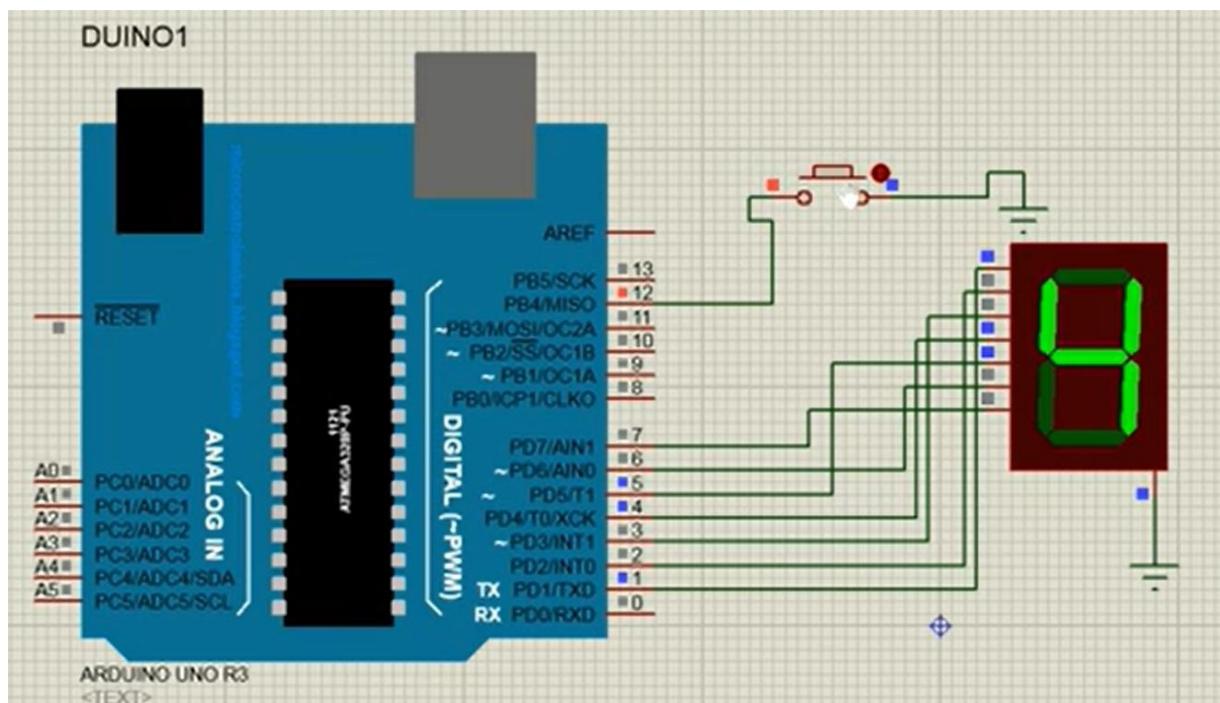
EX-8



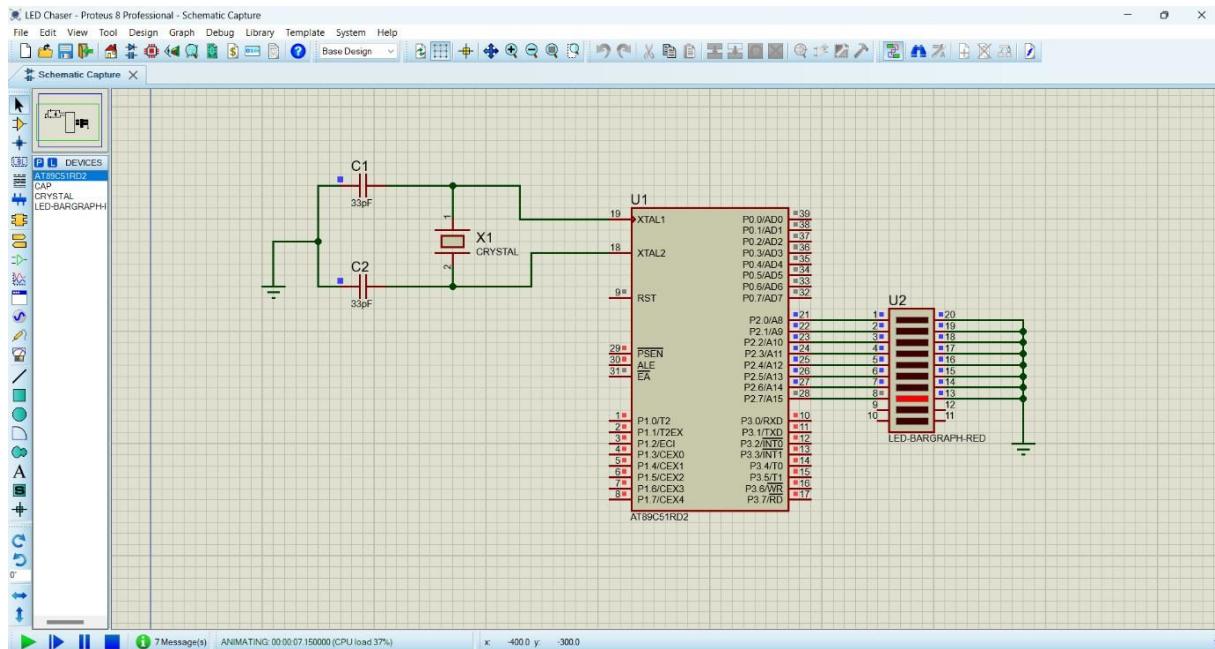
EX-10
CIRCUIT DIAGRAM:



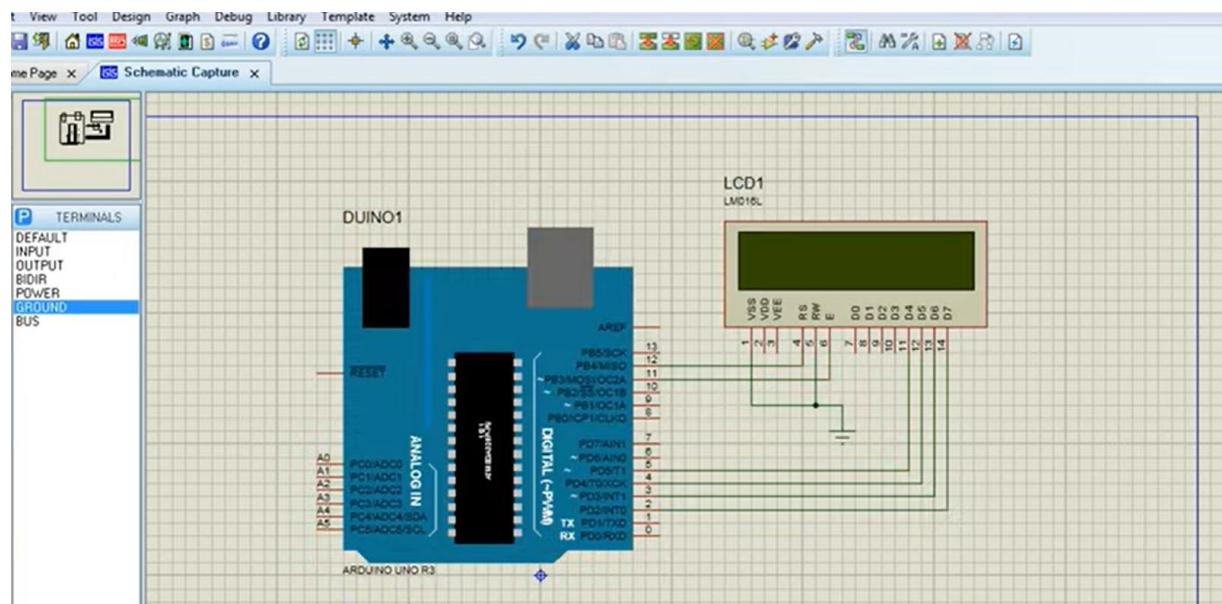
OUTPUT:



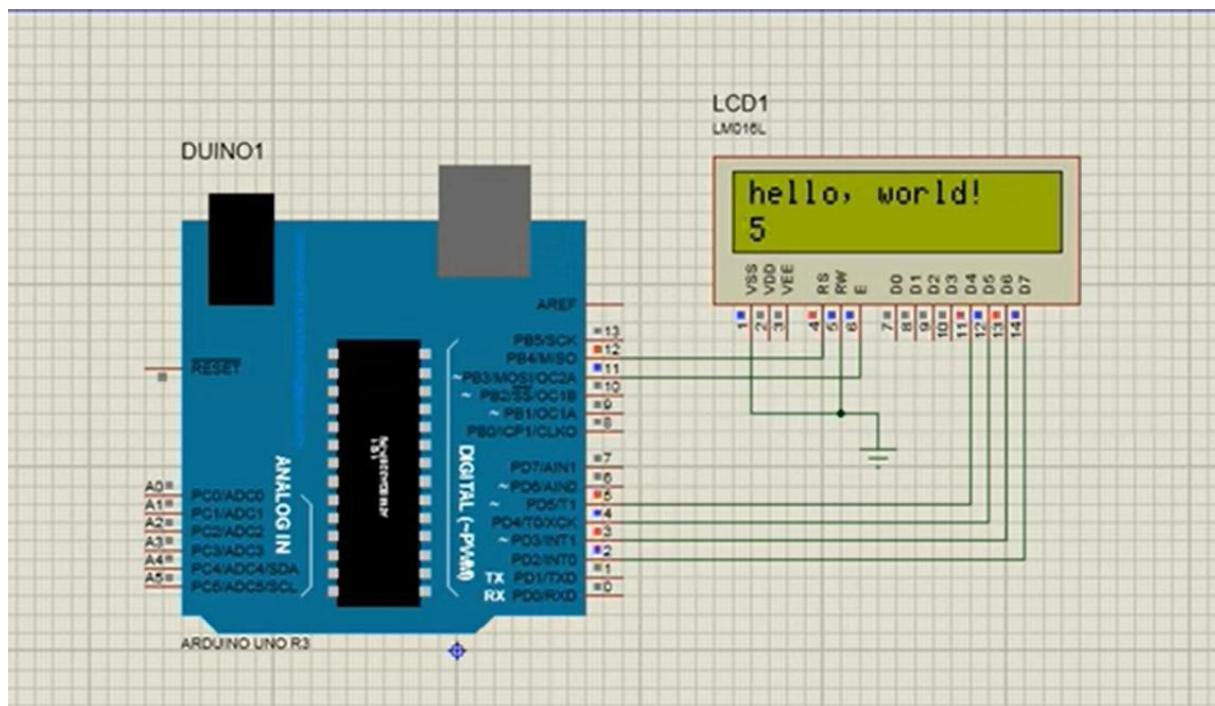
Exp No: 9



EX-11 CIRCUIT DIAGRAM:

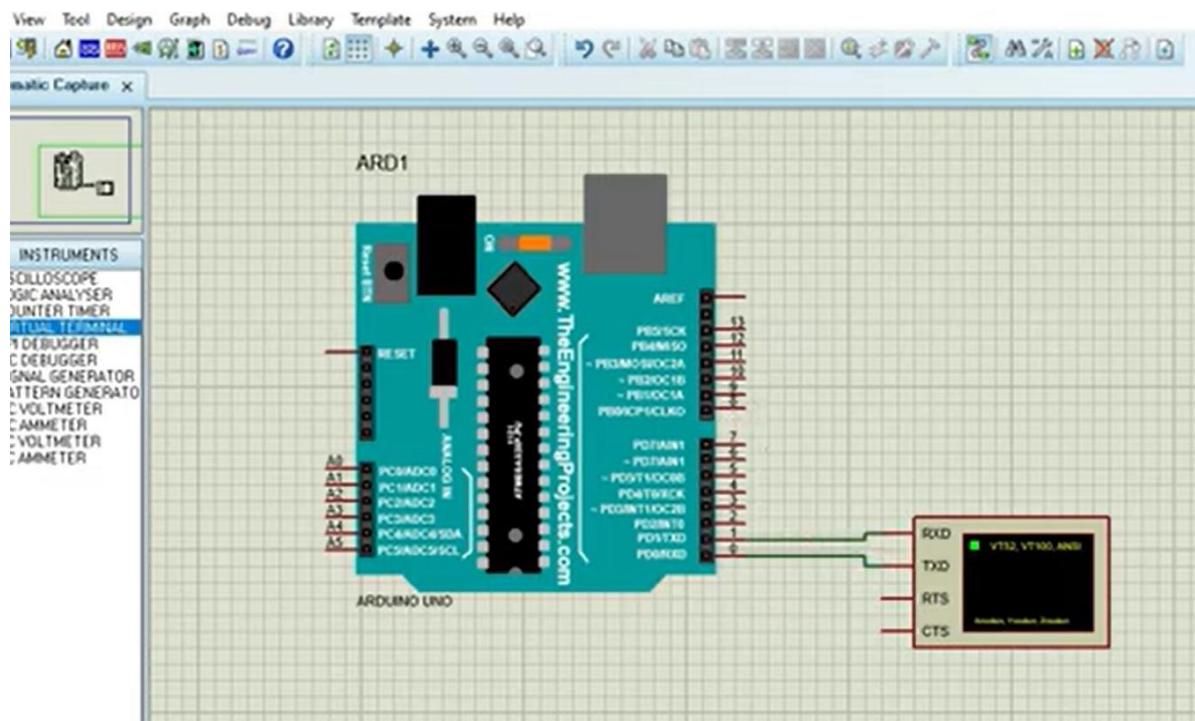


OUTPUT:

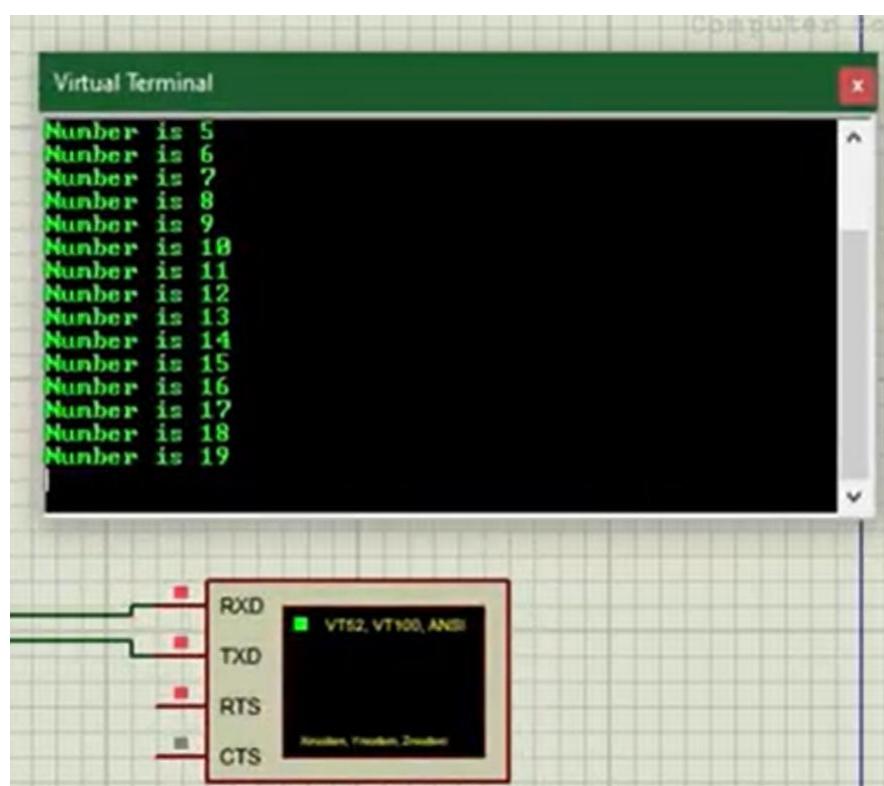


Exp No: 12

CIRCUIT DIAGRAM:

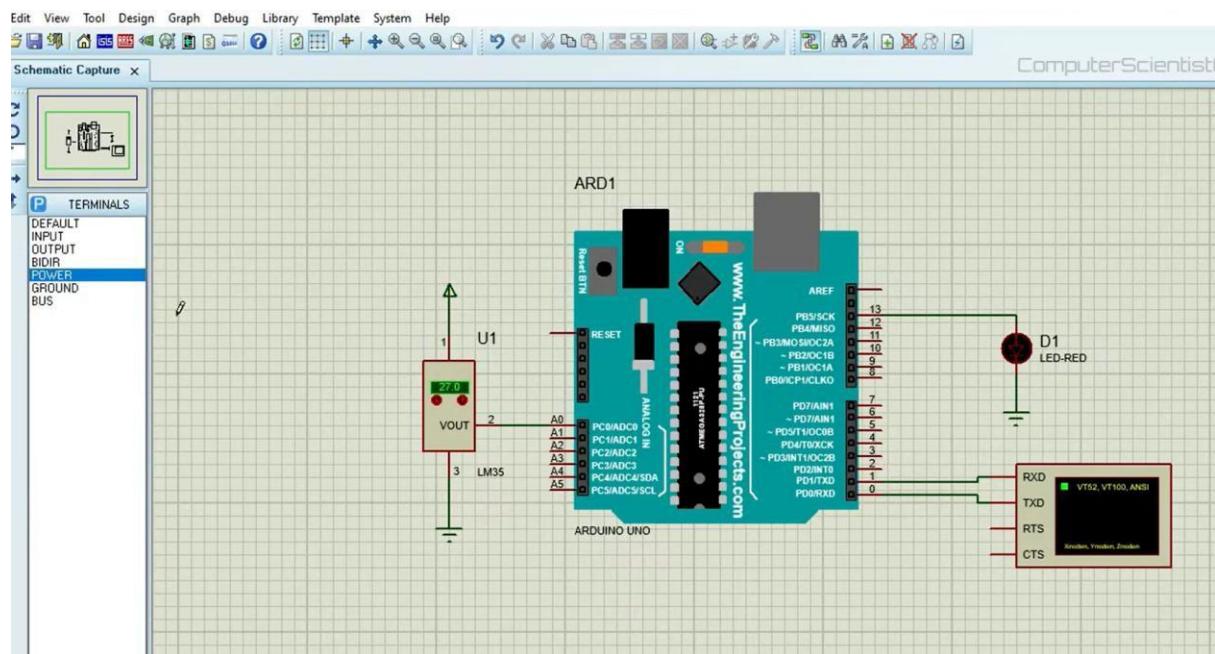


OUTPUT:

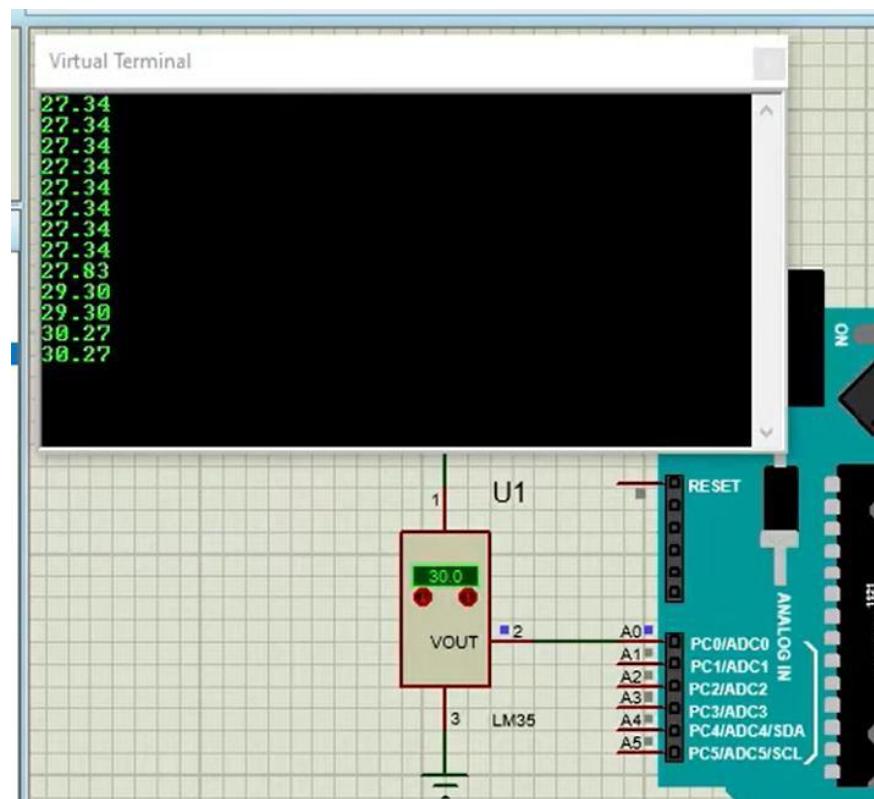


Exp No: 13

CIRCUIT DIAGRAM:

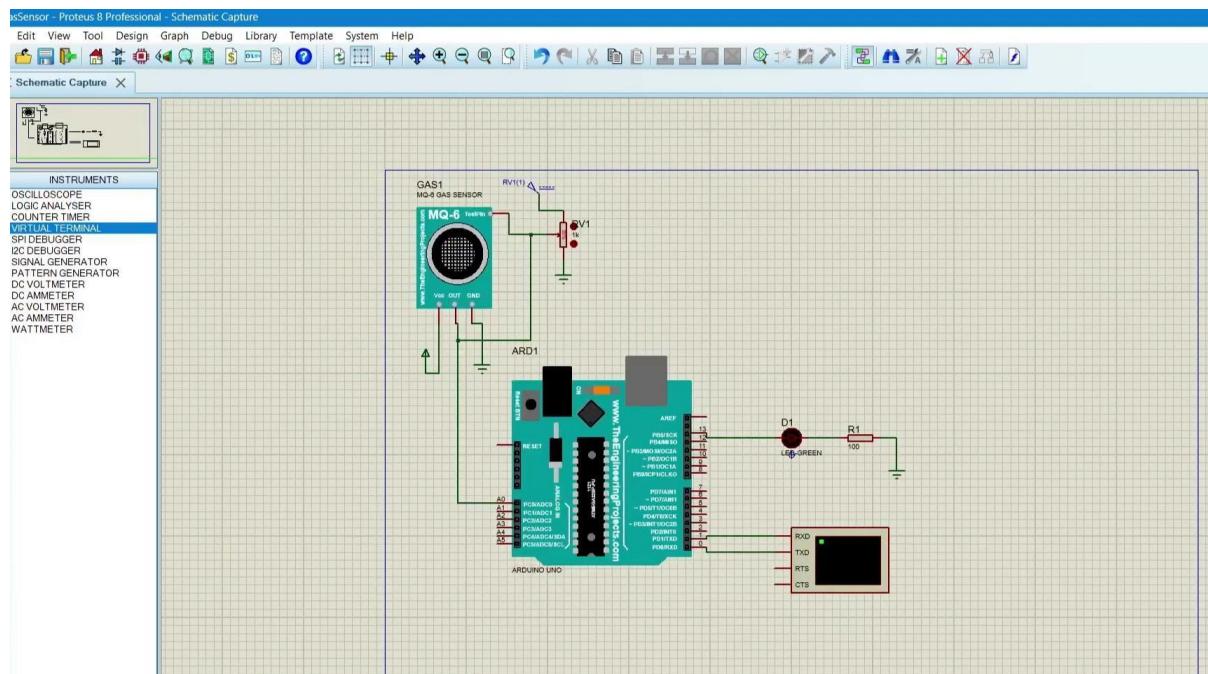


OUTPUT:

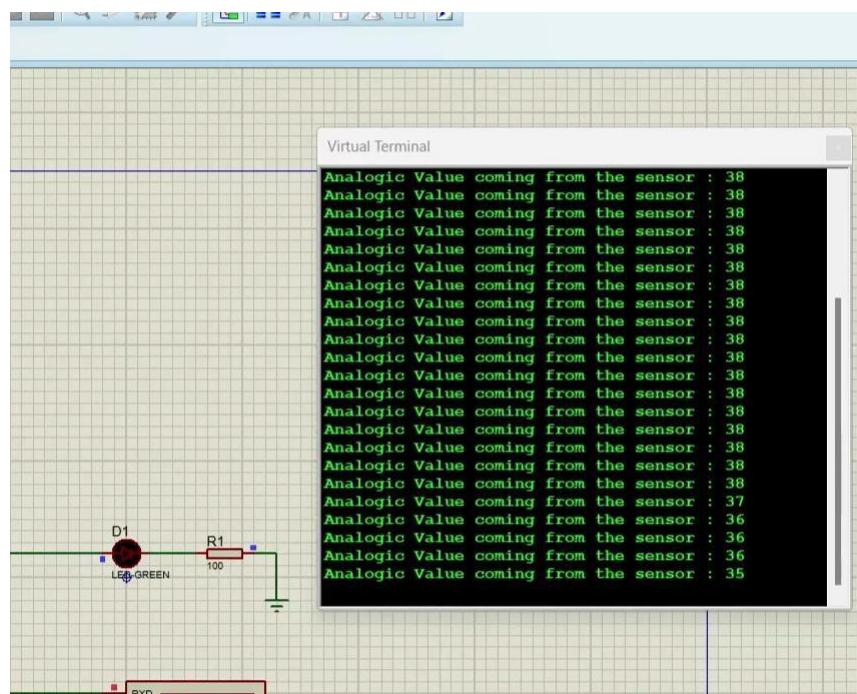


Exp No: 14

CIRCUIT DIAGRAM:

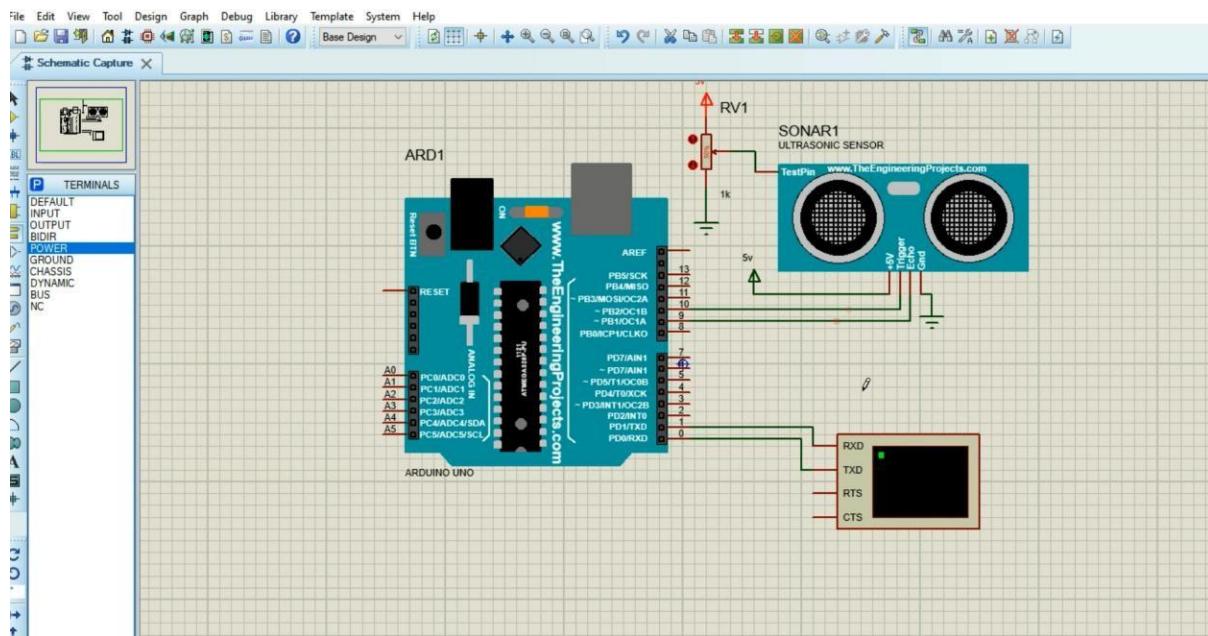


OUTPUT:

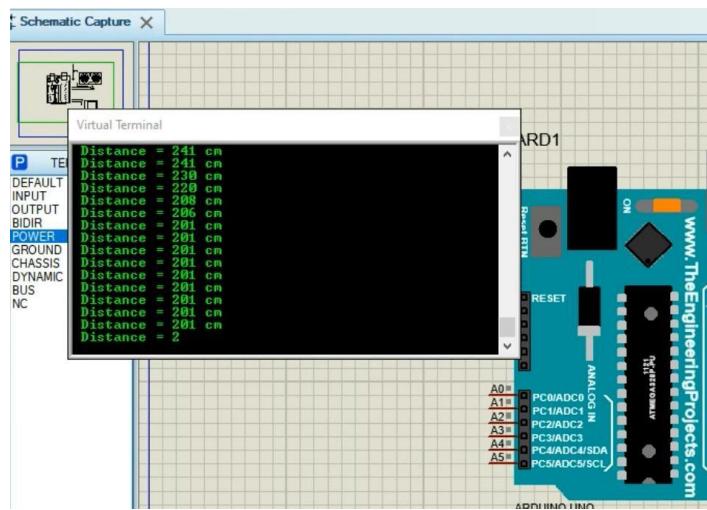


Exp No: 15

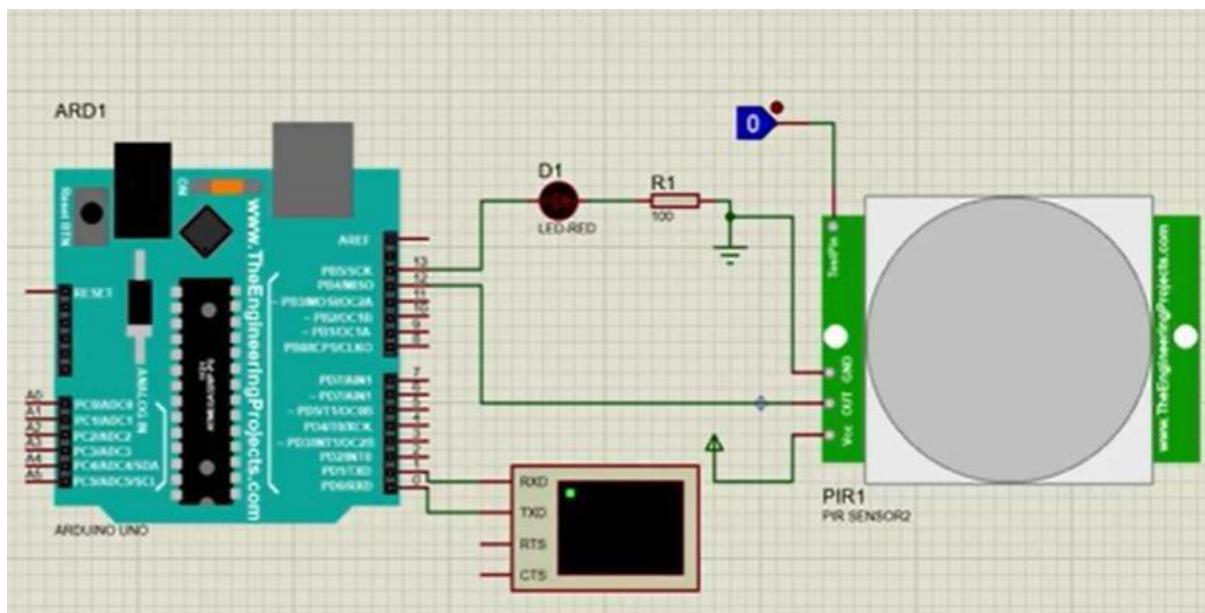
CIRCUIT DIAGRAM:



OUTPUT:



Exp No: 16
CIRCUIT DIAGRAM:



EX-22

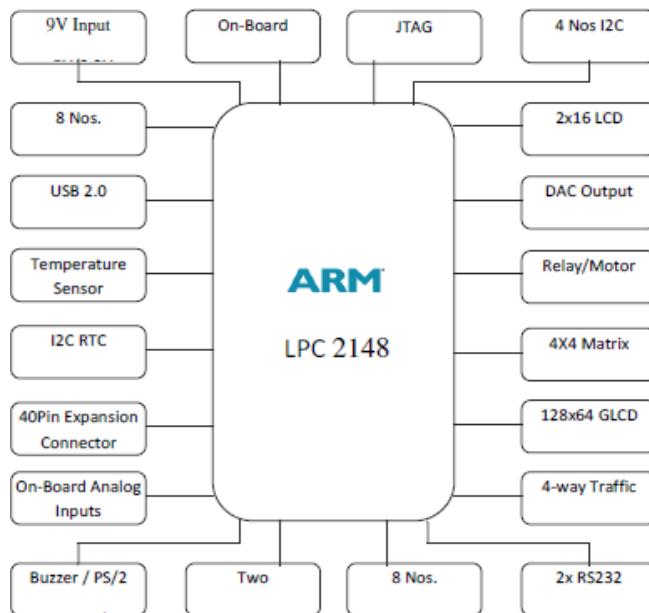


Figure - 3.1: ARM Processor

Pin Configuration

