

https://circuitverse.org/simulator 1/5

// Write Some Verilog Code Here! Circuit Elements Search.. Input 1 **†** <u>‡</u>

Output Gates

Decoders & Plexers

Sequential Elements

Annotation

Misc

Untitled

Project

- New Project
- Save Online Save Offine
- Open Offine
- Clear Project Recover Project

Circuits

12/2/24, 7:44 AM

- New Circuit +
- New Verilog Module

Insert SubCircuit

Tools

- Combinational Analysis Hex-Bin-Dec Converter
- Download Image
- Theme
- Export Verilog

Help

- Tutorial
- User Manual

Quick Menu

- Learn Digital Logic
- Discussion Forum

Sign In

Circuit Elements Search.. Input Output Gates Decoders & Plexers Sequential Elements Annotation Misc Properties Timediagram 1 cycle = 1000

Units

Save Online	Save Offine	Open Offine	Dowload Image	Copy Selected	Paste Selected	Selection Iool	Report Issue	
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Layout Elements								
Timing Diagr	am							
Testbench								
Contention Error: One or more bus contentions in the circuit (check highlighted nodes)								
Verilog Module								
Reset Code Save Code								
This is an experimental module. The code is not saved unless the "Save Code" button is clicked.								
Apply Themes								
11.7								
Select a them	e:							
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Direction Up Up

Combinational Analysis Shift + A

12/2/24, 7:44 AM

Save Cancel

Direction Down Down Direction Left Left Direction Right Right Insert Label Ctrl + L Label Direction Up Alt + Up Label Direction Down Alt + Down Label Direction Left Alt + Left Label Direction Right Alt + Right Move Element Up Shift + Up Move Element Down Shift + Down Move Element Left Shift + Left Move Element Right Shift + Right Hotkey Preference F8 Open Documentation F1 Layout __ Width __ _ Height _ Reset all nodes: _ Title Title Enabled: 🔽

Counter: a binary counter from zero to a given maximum value

Looks like there are no other circuits which doesn't have this circuit as a dependency. Create a new one!

New Circuit +
Report an issue

https://circuitverse.org/simulator

Report an issue

Bug reporting from here will be back soon, until then help make CircuitVerse better by reporting simulator issues to us on Slack.

Click Here to report an issue

Thank you for your time.