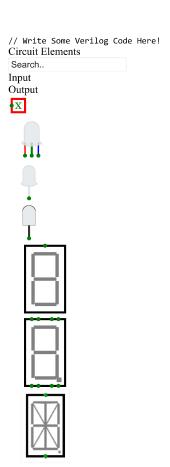


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Gates

Decoders & Plexers

Sequential Elements

Annotation

Misc

Untitled

Project

- New Project
- Save Online Save Offine
- Open Offine
- Clear Project
- Recover Project

Circuits

- New Circuit +
- New Verilog Module
- Insert SubCircuit

Tools

- Combinational Analysis
- Hex-Bin-Dec Converter
- Download Image
- Theme
- Export Verilog

Help

- Tutorial
- User Manual

1 cycle = 1000

- Learn Digital Logic
- Discussion Forum

Sign In

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Units

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Layout Eleme		pe mode and r	erresir to access tr	ic simulator			
Timing Diagra							
Testbench							
Verilog Modu	le						
Reset Code	Save Code						
This is an exp	erimental mod	dule. The code	is not saved unles	s the "Save Code	e" button is clicke	ed.	
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12/2/24, 8:01 AM Clock Time (ms): 500 Clock Enabled: <a>V Lite Mode: Edit Layout Delete Circuit Press Desire Key Combination & press Enter or press ESC to cancel. Command Keymapping New Circuit Shift + N Save Online Ctrl + S Save Offline Ctrl + Alt + S Download as Image Ctrl + D Open Offline Ctrl + O Insert Sub-circuit Shift + C Combinational Analysis Shift + A Direction Up Up Direction Down Down Direction Left Left Direction Right Right Insert Label Ctrl + L Label Direction Up Alt + Up Label Direction Down Alt + Down Label Direction Left Alt + Left Label Direction Right Alt + Right Move Element Up Shift + Up Move Element Down Shift + Down Move Element Left Shift + Left Move Element Right Shift + Right Hotkey Preference F8 Open Documentation F1 Layout _ Width _ Height _

Save Cancel

• Paste

Reset all nodes:

Title Enabled: <

Title

- Copy
- Cut
- Delete
- Undo
- New Circuit
- Insert SubCircuit
- Center Focus

Digital Led ToolTip: Digital LED glows high when input is High(1).

Looks like there are no other circuits which doesn't have this circuit as a dependency. Create a new one!

New Circuit +
Report an issue



Bug reporting from here will be back soon, until then help make CircuitVerse better by reporting simulator issues to us on Slack.

Click Here to report an issue

Thank you for your time.

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