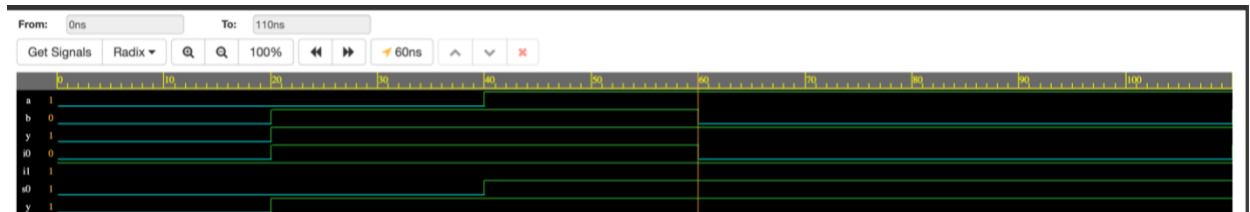


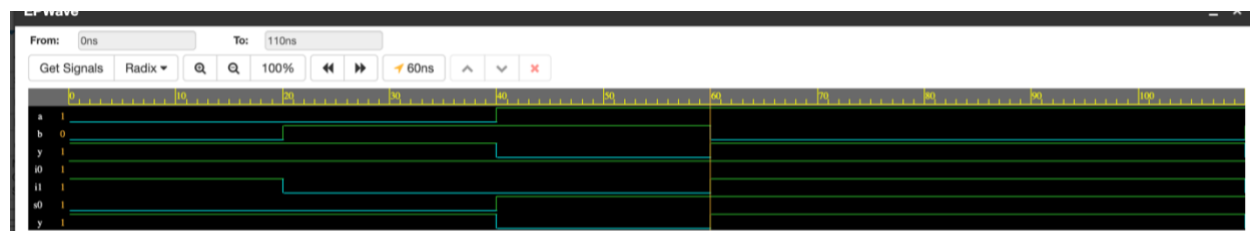
AND gate



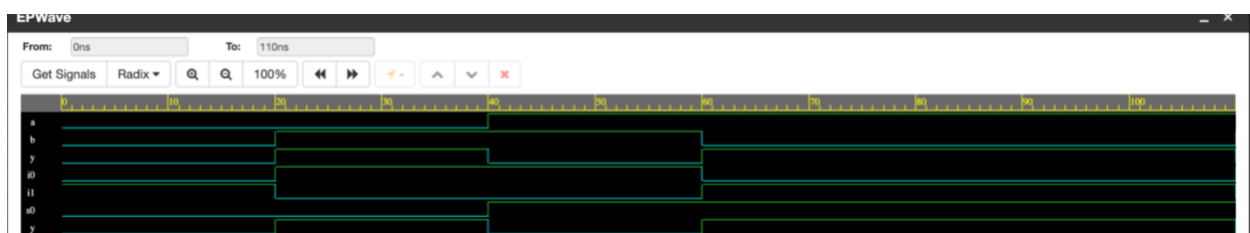
Or gate



NOR gate



NAND gate



XOR gate



XNOR gate