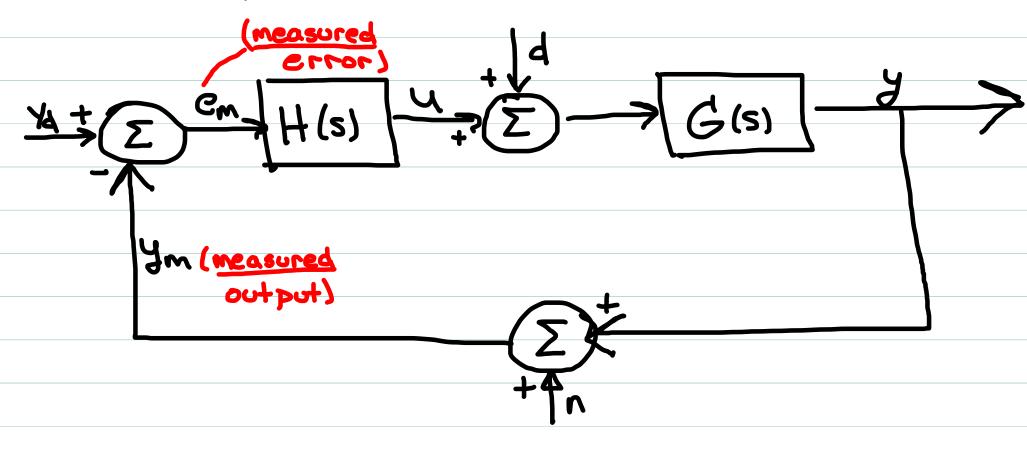
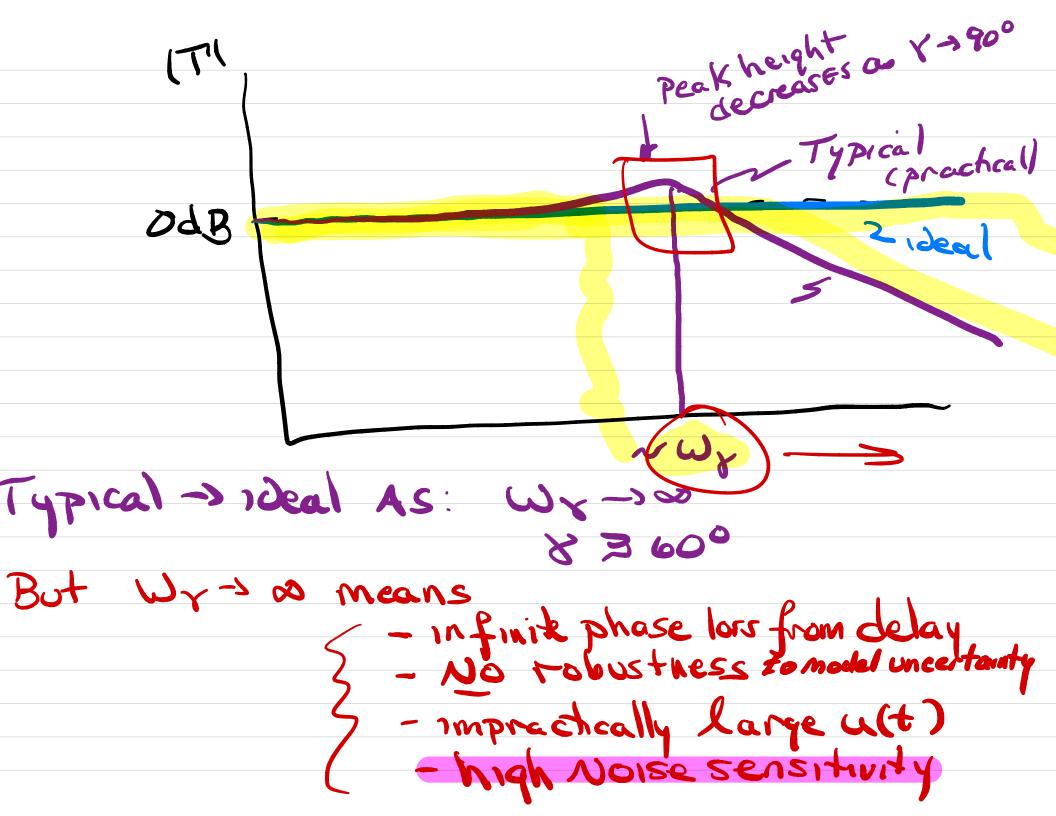
# Effect of sensor noise



ideally (for tracking) Y= => 1 deally, T(s)=1) 0=1(wj)Tj (= XT(jw) = 0 deg for all w>0



and hence:  $E = Y_d - Y$  satisfies:  $E = (1-T)Y_d - S_i D + TN$ New term!

or:  $E = SY_d - S_i D + TN$ Tracking error

error due
to disturbance

Note: TF from noise to Y is same as TF from You to Y (both are T'(s))

Implication: => feedback loop tries to "track the noise"

Equivalently: => noise is indistinguishable from "Signal"

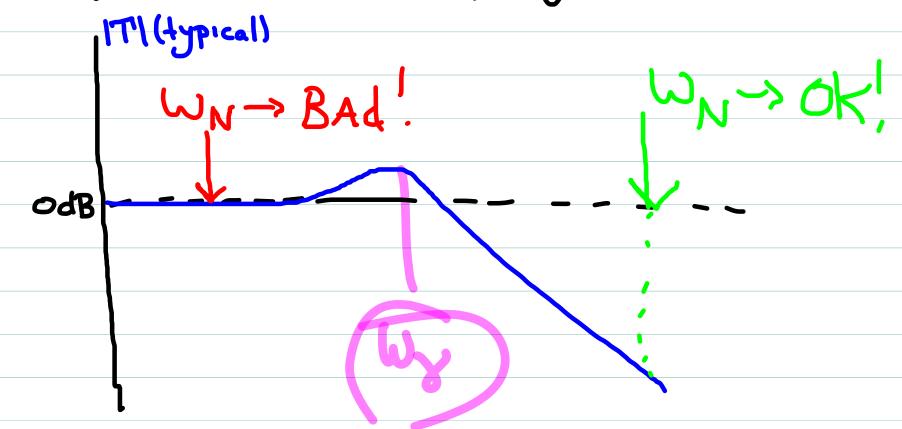
y(+) loop is trying to control!

#### Impact of Noise

Assume for simplicity noise is "tonal": n(t) = Nsin(WNt) (it isn't really, but useful starting point!)

Then Added error is upper bounded by NITIjwnll

=> Need IT(jw) | small at noise frequency wn!

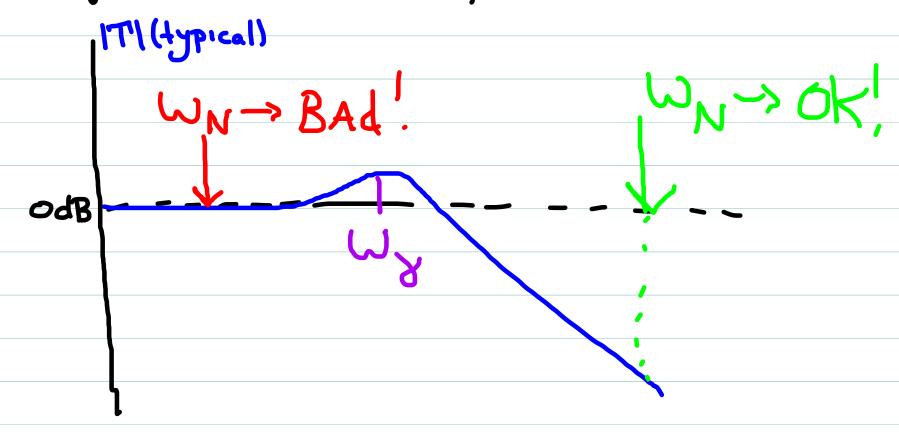


### Impact of Noise

Assume for simplicity noise is "tonal": n(t) = Nsin(WNt) (it isn't really, but useful starting point!)

Then Added error is upper bounded by NITIjwnII

=> Need IT(jw) | small at noise frequencies!



#### Design Implications, I

=> Need Wy << WN

=> Constrains Wy / bandwidth

=> Conversely, designs with larger wy will show worse performance due to increased noise impact!

Essentially, we need to make sure there is adequate separation between the frequencies we are trying to track (bandwidth), and the frequency of the noise.

=> Works against our desire for large wy (fast settling)

# Another perspective:

With Noise, controller implementation equation is:

$$u(t) = C_0 e_{m}(t) + \sum C_K X_K(t)$$

$$\dot{x}_{K}(t) = \alpha_{K} x_{K}(t) + \underline{e_{m}(t)}$$
 [  $\alpha_{K} poles of H(s)$ ]

Noise impacts ult):

XX(t) diff'l eg'ns have a "filtering" property (reduce magnitude of noise effects)

=> Designs with Co = \$ have superior noise resistance

# Design Implications, I

Co = \$\phi \ins \text{H(s) has more poles than zeros

=> Designs with this property have better noise resistance!

=> Works against our need to increase phase margin

Most "Advanced" controller designs have I more pole than zeros to ensure good noise filtering.

However, superior transient performance is achievable with  $C_0 \neq \phi$  provided noise is not a significant issue.

$$\dot{\chi}_{K}(t) = \alpha_{K} \chi_{K}(t) + e_{m} = \alpha_{K} \chi_{K}(t) + e_{k} t - \frac{1}{N + e_{k}} \chi$$

Noise is attenuated in xx(+) if lax << WN.

Compensator Pole

noise frequency

# Design implication, III

for good Noise rejection, compensator poles should be significantly Lower frequency than the Noise

=> Avoid excessively high frequency poles in H(s) (ie. poles very for from imag Axis).

=> Another advantage of "minimum B" lead comp design:

By minimizing B (ratio of pole location to Zero location in H(s)), we are bringing the pole As close to imag Axis As possible while still providing necessary up at desired wy.

# Why it's bad to differentiate y1+).

One is tempted to implement a H(s) with only a zero (or more generally with 1 more zero than pole) by numerically differentiating y(t) = \( \frac{1}{3}(t) \)

This would be needed since, as we've seen, such compensators will result in ult) having a term proportional to c(t) [hence y(t)]

But with worse, we're really diffing ym(+)= y(+)+n(+).

Let z(t) = dt ym(t) Be an estimate of j(t)

Bode mag for H(s)=5 jul 420

Differentiation amplifies the effect of noise

explicitly: if again NH= Esin(Wyt), Wy>>I

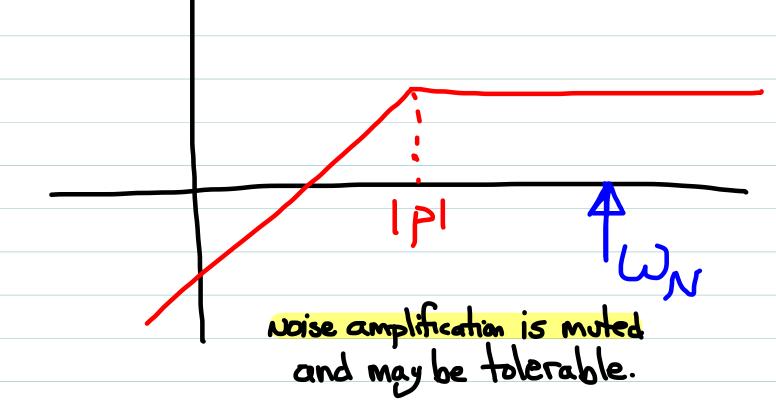
Z(t) = LE [y(t) + n(t)] = y(t) + EWN cos(WNt)

Not small!

(potentially larger than y)

Note that if we added a pole to our derivative estimation scheme

$$Z(s) = \left[\frac{s}{s-p}\right] Y_m(s)$$



If we used this stratery to replace the derivative information needed for implementation an ideal zero:

Then:

which is a lead compensator (for typical case PCZ).

So really, a lead compensator is effectively a "practical" implementation of an ideal zero, which acknowledges the imperfect nature of the measurement process.

Alt: a lead comp is a PD, with velocity measurements
replaced by a low pass filtered estimate of
velocity.

The most basic (and essential) task of the control engineer — achieving a Stable closed-loop system with Nominal pertormance characteristics — is straight-forward to approach.

However, it is tricky to also incorporate and balance the competing constraints of

- Implementation constraints (relative degree of H(s))
- Tracking accuracy
- Disturbance rejection
- Noise rejection
- Model uncertainty
- Sensor/Actuator/Computation delays Actuator Limits/Control Saturation
- Power/weight/cost demands

The "best" design is one which achieves an acceptable trade-off among these competing factors.

There is no "one true design" which makes the "ideal" tradeoff — so don't waste time looking for it!

Find something that works acceptably well, and move on

# Major, Common families of Compensators

Note: implementable if both y(4) and y(4) me assured directly)

3) 
$$H(s) = K_p + \frac{K_T}{s} = K\left[\frac{S-2}{s}\right] \left(K=K_p, 2=-\frac{K_T}{k_p}\right)$$

=> 
$$u(t) = K_{P}c(t) + K_{I}x_{I}(t)$$
  
 $\dot{x}_{I}(t) = e(t)$ 

4) 
$$H(s) = K_p + K_p s + \frac{k_T}{s} = K \left[ \frac{(s-2.)(s-2.)}{s} \right]$$
 $(K = K_p; \frac{2.}{2}, \frac{2.}{2}, roots of K_p s^2 + K_p s + K_T)$ 
 $=> U(t) = K_p e(t) + K_p \dot{e}(t) + K_T \int_0^t e(t) d\tau$ 

"Prop/Int/Deniu (PID) control"

Notes: a.) Very popular. Special purpose Chips which do this computation are commonly available b.) 1)-3) above are special cases of this More general form.

C.) Provides 2 zeros to help meet margin/xover requirements, and pole at origin to help with tracking/distrejection requirements.

d.) Like PD, requires direct measurement of y(+)

Of course, a designer is free to choose H(s) as desired. These are common "go to" starting points which can be modified or added to as needed.