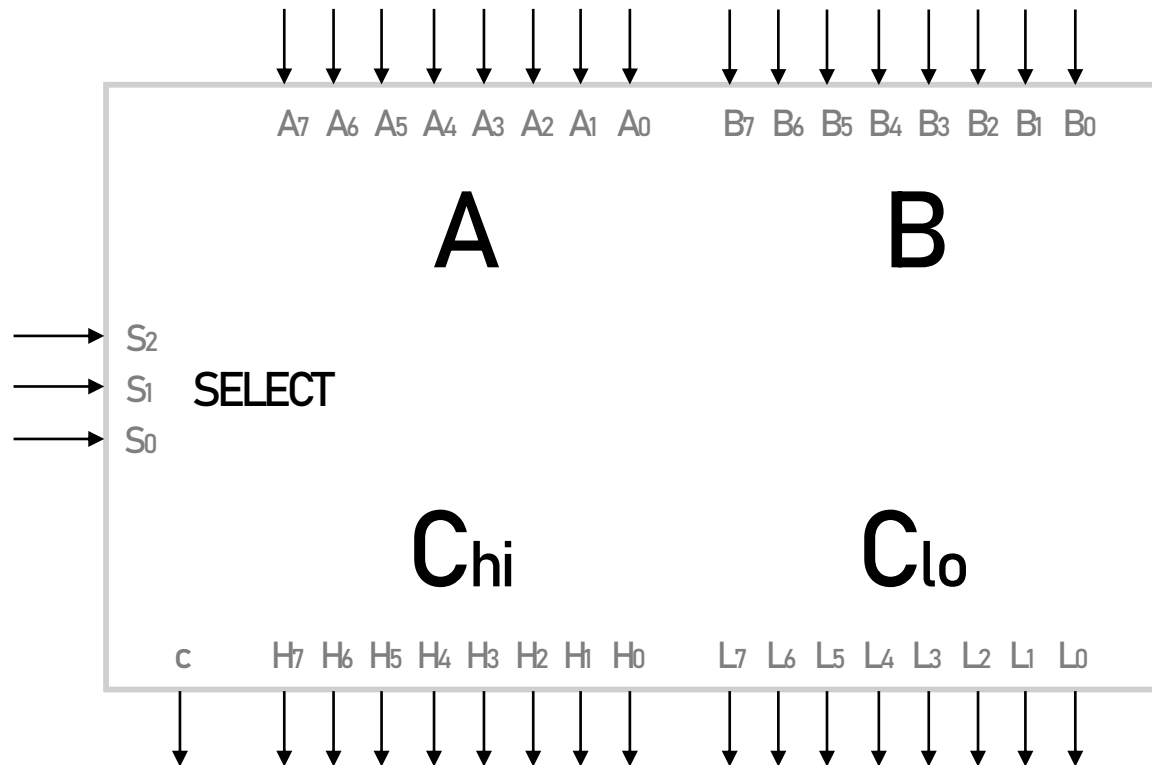
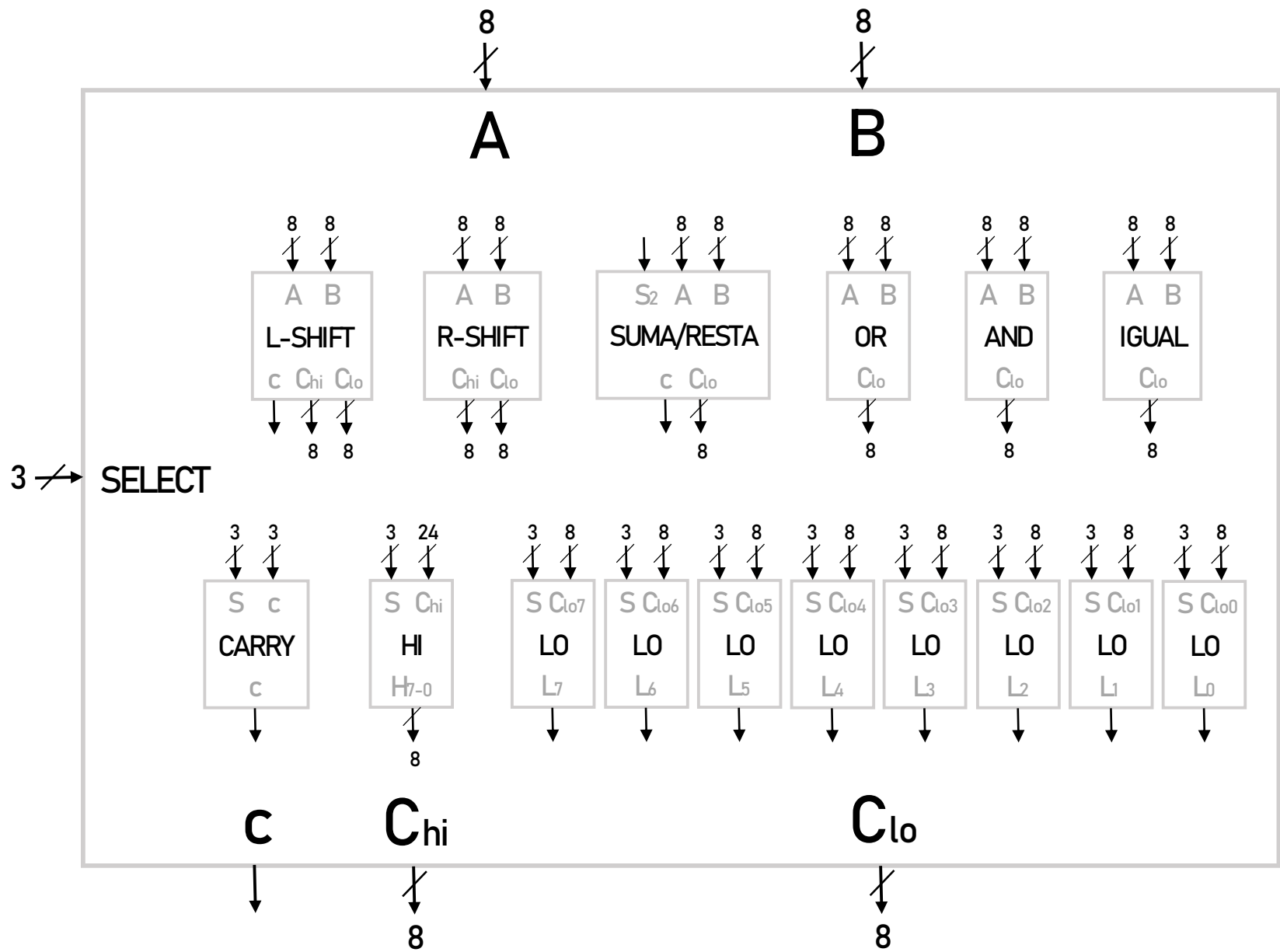
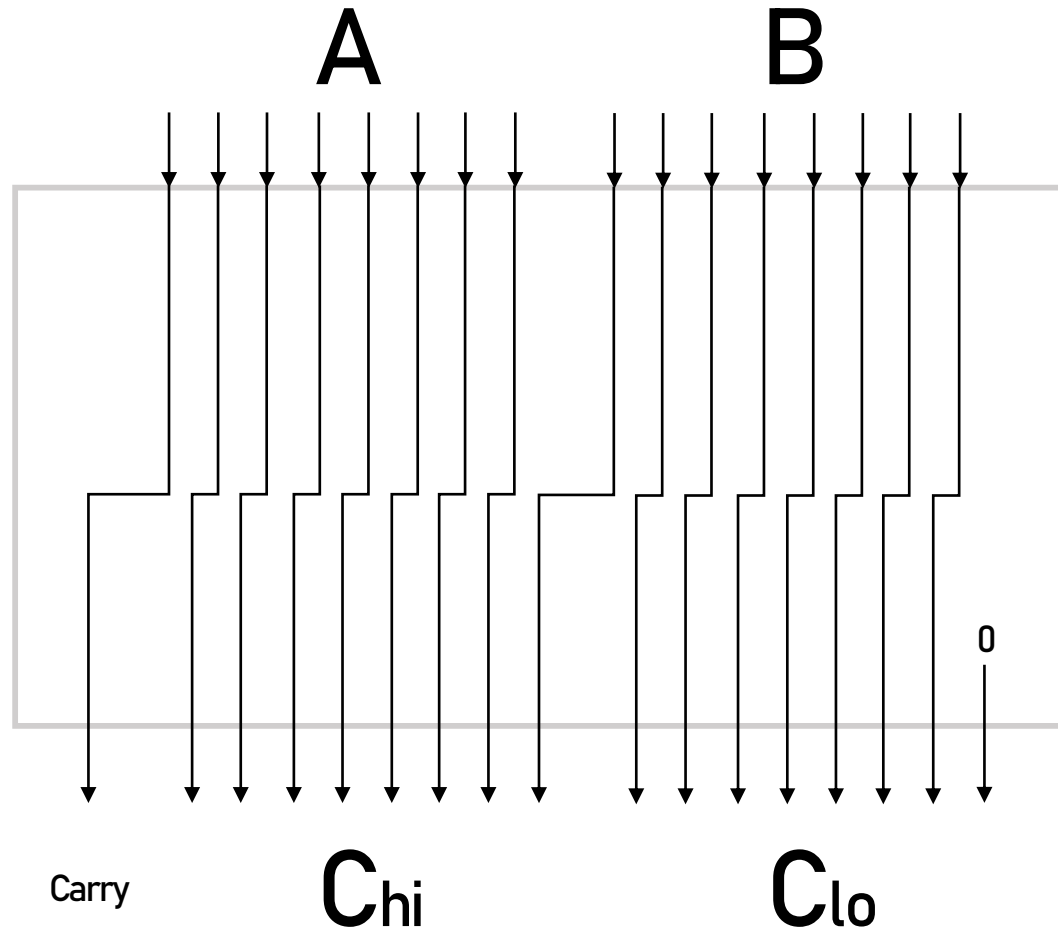


DISEÑO CPU

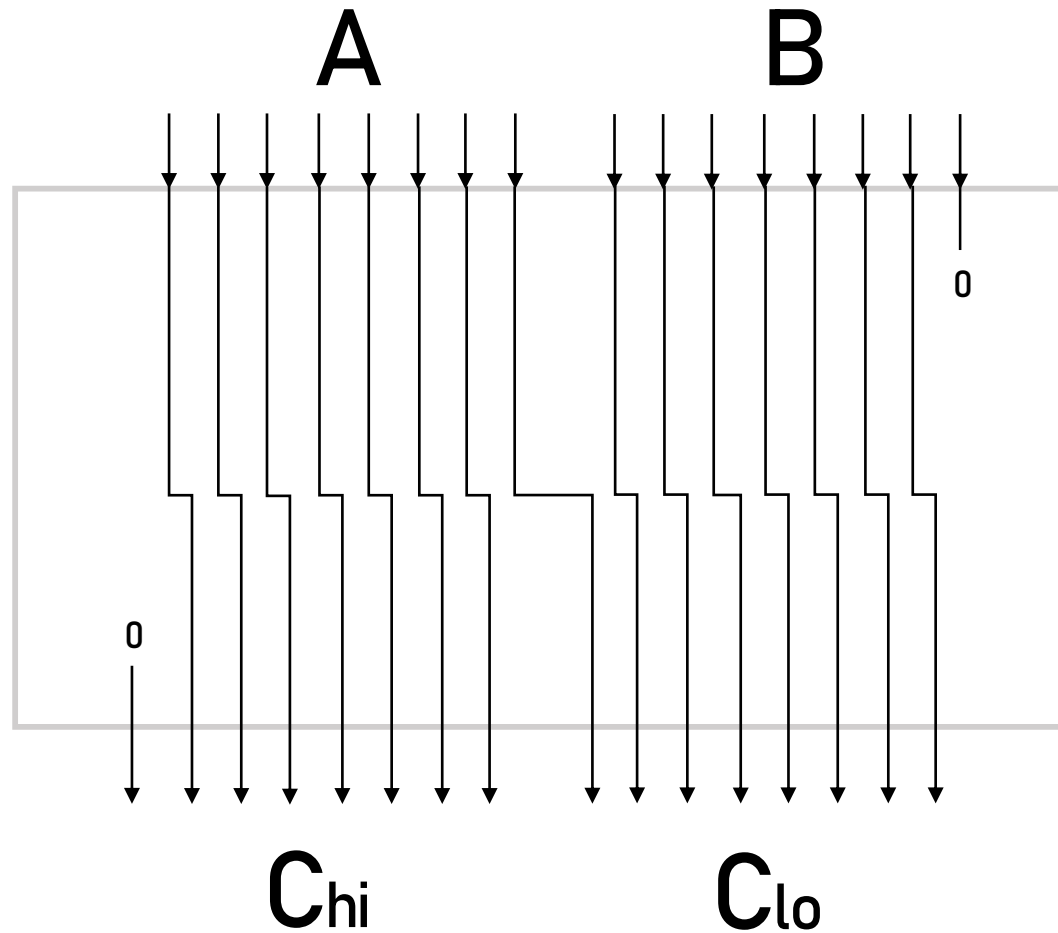




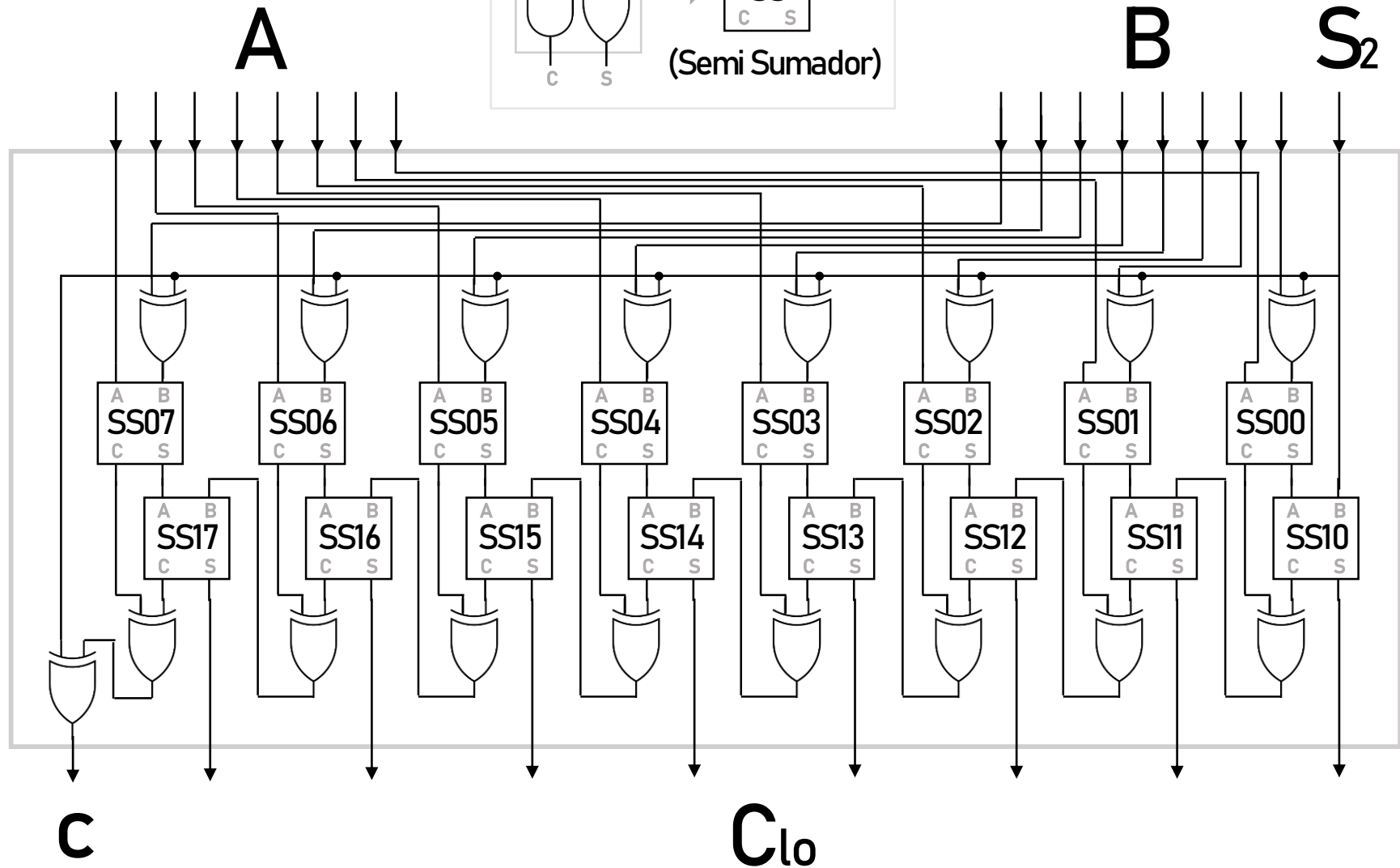
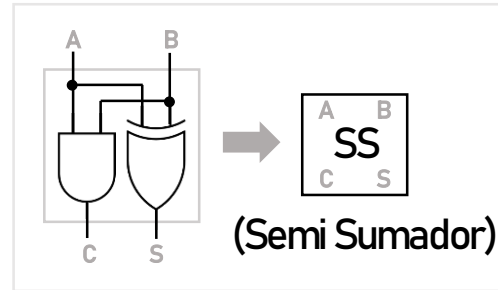
L-SHIFT



R-SHIFT



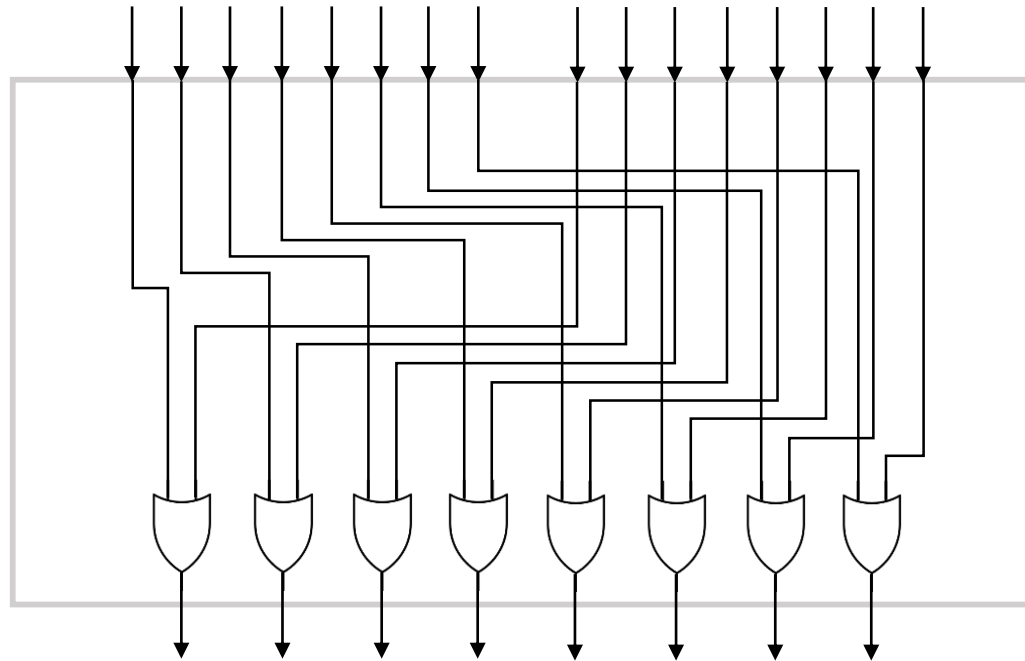
SUMA/RESTA



OR

A

B

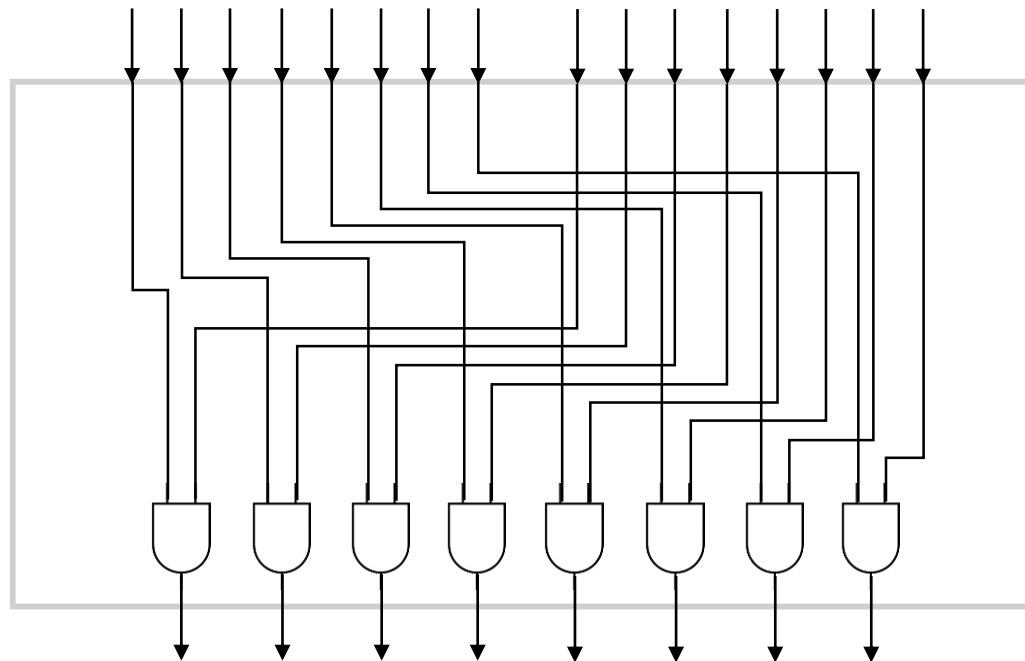


C_{lo}

AND

A

B

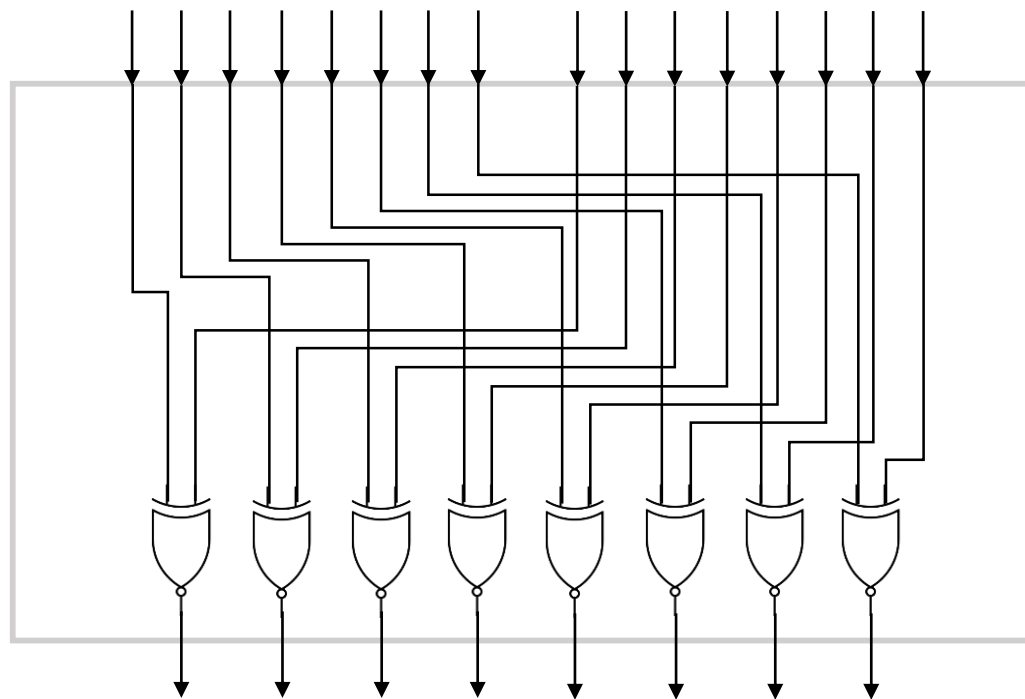


C_{lo}

IGUAL

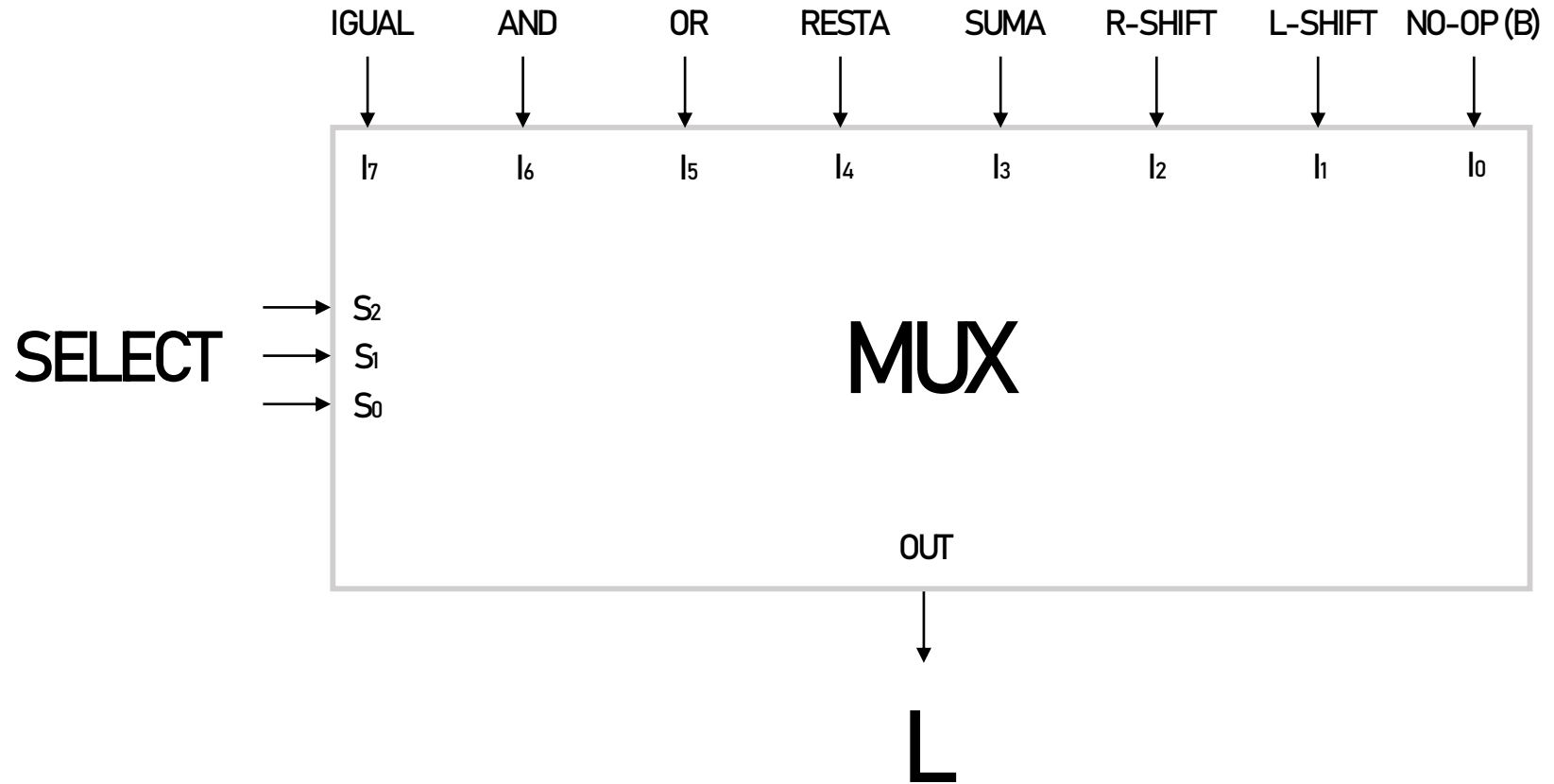
A

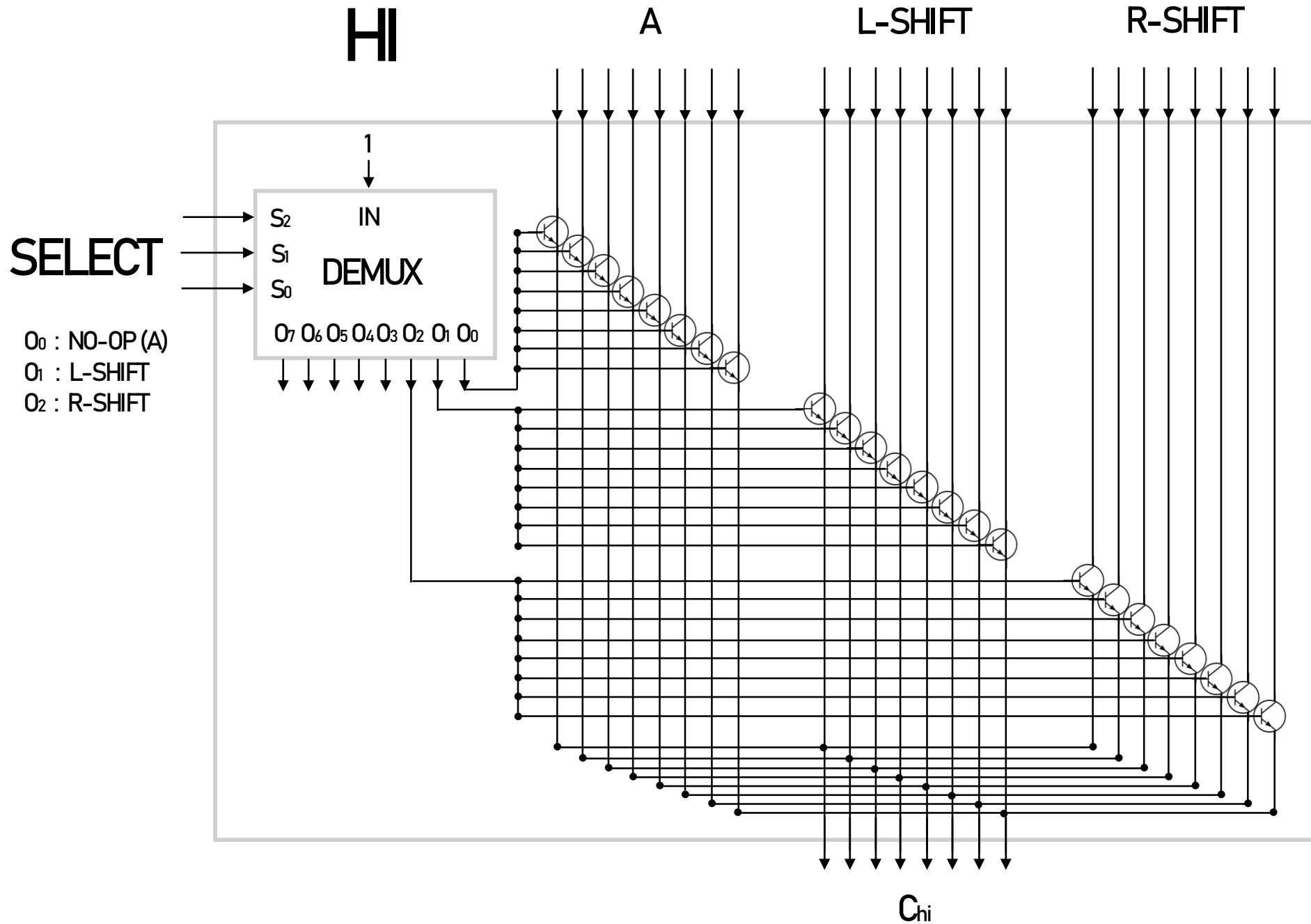
B



C_{lo}

L0





CARRY

