

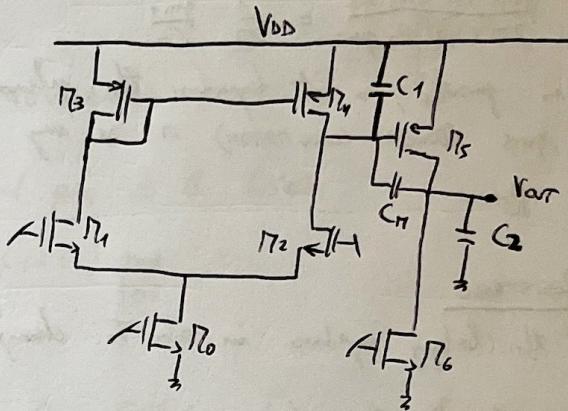
COMPENSATION (Two-STAGE)

MILLER

Split the two poles due to the two high impedance nodes by placing a C_1 bridging the gate-drain nodes of a transistor.

The three capacitors are dependent (they form a loop) so we expect only two poles in the T.F.:

$$b_1 = C_1 \cdot R_1^0 + C_2 R_2^0 + G_1 R_1^0$$



$$\left\{ \begin{array}{l} R_1^0 = R_{out,1} = R_1 \\ R_2^0 = R_{out,2} = R_2 \end{array} \right.$$

$$\left\{ \begin{array}{l} R_{in}^0 = R_1 + g_{ms} R_2 R_1 + R_2 \quad (\text{Miller effect}) \\ b_1 = C_1 R_{out,1} + R_2 (C_L + C_1) + (1 + g_{ms} R_2) C_1 R_1 = \tilde{\epsilon}_1 + \tilde{\epsilon}_2 + \tilde{\epsilon}_3 \end{array} \right.$$

$$\Rightarrow b_1 = C_1 R_{out,1} + R_2 (C_L + C_1) + (1 + g_{ms} R_2) C_1 R_1 = \tilde{\epsilon}_1 + \tilde{\epsilon}_2 + \tilde{\epsilon}_3$$

$$b_2 = C_1 G_1 R_1^0 R_2^0 + C_1 G_1 R_1^0 R_{in}^0 + G_2 G_1 R_2^0 R_{in}^0$$

$$\left\{ \begin{array}{l} R_1^0 = R_2 \\ \cancel{R_{in}^0} = R_2 \\ R_{in}^2 = R_1 \end{array} \right.$$

$$\Rightarrow b_2 = G_1 R_2 (C_2 + G_1) + R_1 R_2 C_2 G_1 = \tilde{\epsilon}_1 \tilde{\epsilon}_2 + \tilde{\epsilon}_4$$

$$b_3 = C_1 C_2 G_1 R_1^0 R_2^0 R_{in}^{1,2} = 0 \quad (\text{as expected})$$

We expect also a zero when

$$2C_{in}V_{ds} = g_{ms}V_S$$

$$z = \frac{g_{ms}}{C_{in}} \quad (\text{POSITIVE})$$

If the roots of the denominators are split of more than a decade, we get

$$P_L \approx -\frac{1}{b_1} \approx -\frac{1}{R_1 g_{ms} R_2 G_1}$$

$$P_H \approx -\frac{b_1}{b_2} \approx -\frac{R_1 g_{ms} R_2 G_1}{R_1 R_2 C_1 (C_2 + G_1) + R_1 R_2 C_2 G_1} = -\frac{g_{ms} C_1}{C_1 G_2 + C_1 G_1 + C_2 G_1}$$

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For large C_1 values we get

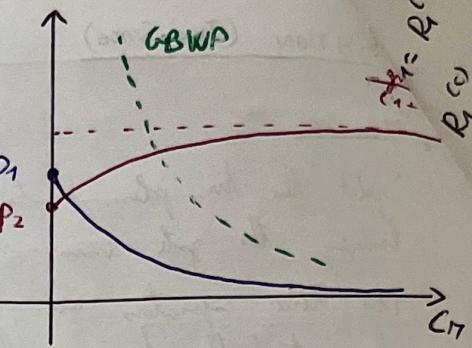
$$P_H \approx -\frac{g_{ms}}{C_1 + C_2}$$

and P_L moves to lower frequencies.

Finally, we have

$$\text{LBWDP} = \frac{g_{ms} R_1 g_{ms} R_2}{2\pi (C_1 P_L R_2 g_{ms})} = \frac{1}{2\pi} \frac{g_{ms}}{C_1}$$

The zero is positive, so it degrades the phase margin! We must shift it to HF by increasing g_{ms} (POWER CONSUMPTION) or we may increase C_1 in order to move P_L away from P_H .



NULLING RESISTANCE

We modify the bulging impedance in order to change the zero:

$$\frac{R_N}{R_N + \frac{1}{g_{ms} C_1}} = g_{ms} Z_2$$

↓

$$Z_2 = -\frac{1}{C_1 \left[R_N - \frac{1}{g_{ms}} \right]}$$

We may now R_N in order to have a negative zero cancel out with the second pole.

Due to R_N we have three poles

$$b_1 = C_1 R_1 + \cancel{C_2 R_2} + C_c (R_1 + R_2 + g_{ms} R_1 R_2 + R_N)$$

being $R_N \approx \frac{1}{g_{ms}}$ we repeat

$$b_1 \approx C_1 g_{ms} R_1 R_2 \quad (\text{UNCHANGED})$$

$$\Rightarrow Z_L \approx b_1 ; P_L = -\frac{1}{b_1}$$

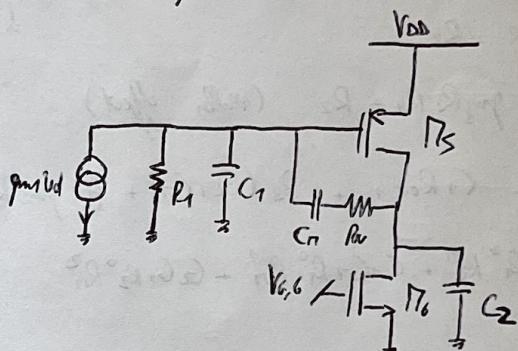
We can compute Z_M using the time constant method:

$$\left\{ \begin{array}{l} Z_1^\infty = (R_1/R_N) C_1 \approx R_N C_1 \\ Z_2^\infty = (R_2/R_N) C_2 \approx R_N C_2 \end{array} \right.$$

$$Z_C^\infty = R_N$$

$$P_H = -\left(\frac{1}{Z_1^\infty} + \frac{1}{Z_2^\infty} + \frac{1}{Z_C^\infty}\right) \approx -\frac{1}{R_N (C_1/C_2/C_C)}$$

We can compute the real pole assuming that C_C is a short (this approximation is clearly justified in "APPROXIMATIONS AND CIRCUIT INSIGHTS")



in series to the resistance across gate and drain of M5

$$b_1 = R_1^{(c)} C_1 + R_2^{(c)} C_2$$

$$R_1^{(c)} = R_1 \parallel \left[\frac{R_N + R_2}{1 + g_{mS} R_2} \right] \approx \frac{1}{g_{mS}}$$

$$R_2^{(c)} = R_2 \parallel \left[\frac{R_N + R_1}{1 + g_{mS} R_1} \right] \approx \frac{1}{g_{mS}}$$

so

$$P_2 \approx - \frac{1}{(C_1 + C_2) \frac{1}{g_{mS}}} = - \frac{g_{mS}}{(C_1 + C_2)} \quad (\text{UNCHANGED})$$

Therefore if we place the second pole at the GBWP:

$$\frac{C_T}{g_{m1}} = \frac{C_1 + C_2}{g_{mS}} \Rightarrow C_T = (C_1 + C_2) \frac{g_{mS}}{g_{m1}}$$

then

$$\left[R_N - \frac{1}{g_{mS}} \right] \frac{C_T}{g_{m1}} = \frac{g_{m1}}{g_{mS}} \Rightarrow R_N = \frac{g_{m1}}{C_T} \left(\frac{1}{g_{m1}} + \frac{1}{g_{mS}} \right) = \frac{2}{g_{mS}} \quad (\because g_{mS} = g_{m1})$$

We may implement the resistor by using a MOSFET in ohmic regime

$$I_{DS} = \frac{1}{2} \mu_m C_{ox} \frac{W}{L} [(V_{GS} - V_T) V_{DS,2} - V_{DS}^2]$$

$$g_o = \left. \frac{\partial I_{DS}}{\partial V_{DS}} \right|_{V_{DS}=0} = \frac{1}{2} \mu_m C_{ox} \frac{W}{L} (2V_{GS})$$

therefore we need

$$\frac{g_{mS}}{g_o} = 2 \Rightarrow \frac{(W/L)_S}{(W/L)_R} \frac{V_{GS,S}}{V_{GS,R}} = 2$$

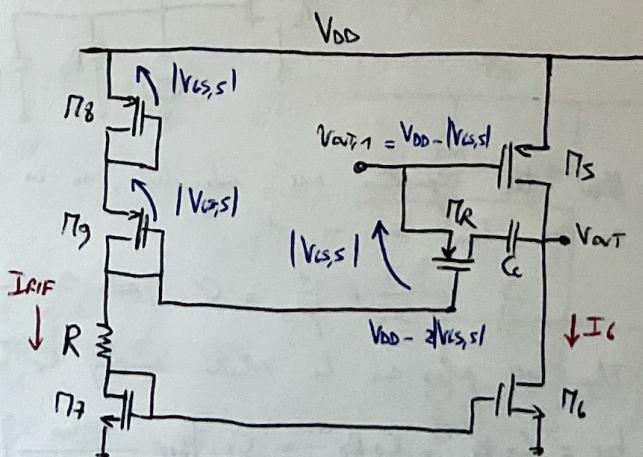
[Just by chance is equal to the transconductance of a MOSFET in saturation]

If we set carefully $V_{GS,S} = V_{GS,R}$, then the accuracy depends on a ratio, which is optimal in integrated design. In order to achieve this result we need to REPLICATE the bias condition of M_S :

M_S and M_R are sized in order to carry the same current with $V_{AR} = \frac{V_{DD}}{2}$. This current is set by the source branch of M_R , which can be set to be $\frac{1}{M}$ of I_G by sizing the

$(\frac{W}{L})_S = \frac{1}{M} (\frac{W}{L})_G$. If now we choose $(\frac{W}{L})_S = (\frac{W}{L})_R = \frac{3}{M} (\frac{W}{L})_G$, then

$$|V_{GS,S}| = |V_{GS,R}| = |V_{GS,T}|$$



Therefore the position of the zero depends only on the ratio of $\frac{(W/L)_S}{(W/L)_R}$.

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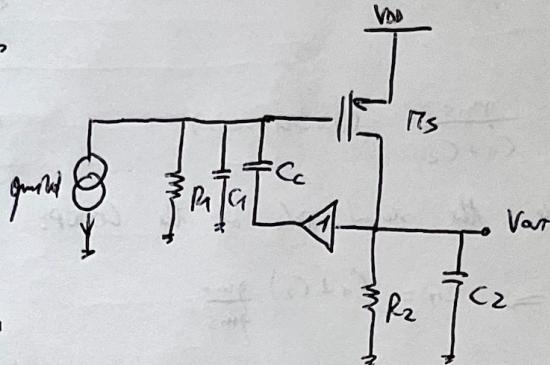
Voltage Buffer

This solution prevents the Miller effect, but kills the fast-forward path, $\approx \infty$ zero!

→ Ideal

The three capacitors are dependent, \approx we expect just two poles.

The dominant pole is still due to the Miller effect

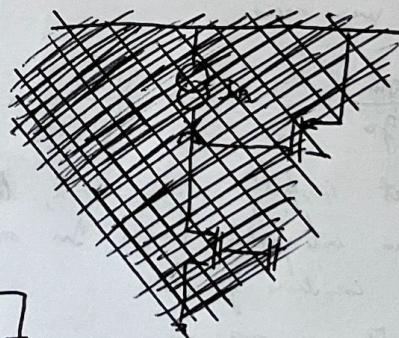
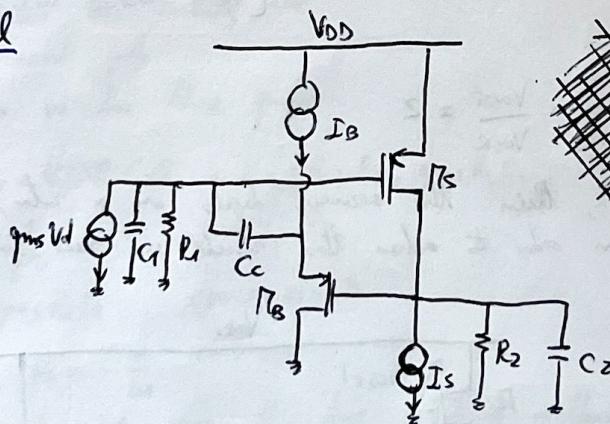


$$P_L = -\frac{1}{g_{mS} R_2 C_2 R_1}$$

The second pole can be avoided by connecting C_c or a short. C_1 is a \approx zero, while we have

$$P_H = -\frac{1}{C_2 \cdot \frac{1}{g_{mS}}} \quad (\text{higher, it doesn't depend on } C_1 !)$$

→ Real



Now the capacitors are independent, \approx we have an additional HF pole and a \approx zero. The \approx zero is at

$$\frac{1}{2C_c} + \frac{1}{g_{mB}} = 0 \Rightarrow \omega = -\frac{1}{C_c \cdot \frac{1}{g_{mB}}} \quad (\text{output unit of } \omega \text{ toward ground})$$

The HF poles can be avoided avoiding C_c or a short

$$b_1 = C_1 R_1^{(c)} + C_2 R_2^{(c)} = C_1 \cdot \left[R_1 \parallel \frac{1/g_{mB}}{1+g_{mS} R_2} \right] + C_2 \cdot \left[\cancel{R_2} \frac{1}{1+g_{mS} R_2} \right] \approx \frac{C_2}{g_{mS}}$$

$$b_2 = C_1 C_2 R_1^{(0)} R_2^{(1)_{\text{DC}}} = C_1 C_2 \frac{R_2}{g_{mB} g_{mS} R_2} = \frac{C_1 C_2}{g_{mB} g_{mS}}$$

So we get

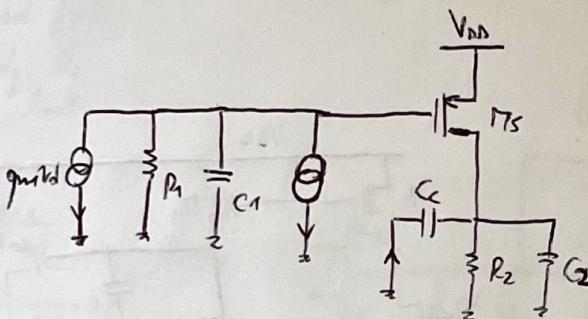
$$\left[\omega^2 \frac{C_1 C_2}{g_{mB} g_{mS}} + \omega \frac{C_2}{g_{mS}} + 1 \right] = 0$$

↓

$$W_0 = \sqrt{\frac{g_{mB} g_{mS}}{C_1 C_2}} \quad Q = \frac{g_{mS}}{C_2} \sqrt{\frac{C_1 C_2}{g_{mB} g_{mS}}} = \sqrt{\frac{g_{mS} C_1}{g_{mB} C_2}} \quad (\text{POLE PAIR})$$

• Ansatz

→ Real



The Miller effect is still present ($V_C = g_{mS} i_S R_2$, $i_C = \omega C_C g_{mS} R_2 v_S$)
Same LF pole.

C_c and C_L are in \parallel , so only two poles are expected. We can short C_c or a short and compute the MF pole:

$$b_1 = Q \cdot \infty + C_1 \cdot \left(R_1 \parallel \frac{1}{g_{mS}} \right) \approx \frac{C_1}{g_{mS}}$$

$$P_M \approx -\frac{1}{C_1 \cdot \frac{1}{g_{mS}}} \quad (\text{load capacitance plays no role!})$$

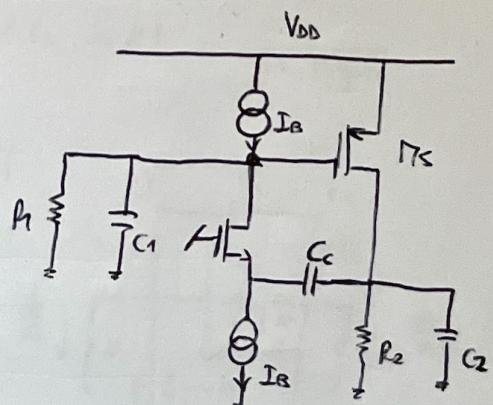
→ Real

We have a LHP zero at

$$\frac{1}{2C_C} + \frac{1}{g_{mB}} = 0$$

$$\downarrow \quad z = -\frac{1}{C_C \cdot \frac{1}{g_{mS}}} \quad (\text{output shorted to ground})$$

and three poles. The LF pole is still due to the Miller effect, which we can reduce by shorting C_C :



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$$b_1 = C_1 \cdot \left[R_1 \parallel \frac{1}{g_{ms}} \right] + C_2 \cdot R_2 \parallel \left(\frac{1/g_{ms}}{1+g_{ms}R_1} \right) \approx C_1 \frac{1}{g_{ms}}$$

$$b_2 = C_1 C_2 \cdot \frac{1}{g_{ms}} \cdot \frac{1}{g_{ms}}$$

So we get

$$\frac{C_1 C_2}{g_{ms}^2} s^2 + 2 \cdot \frac{C_1}{g_{ms}} + 1 = 0$$

$$W_0 = \sqrt{\frac{g_{ms}^2}{C_1 C_2}} \quad Q = \frac{g_{ms}}{C_1} \cdot \sqrt{\frac{C_1 C_2}{g_{ms}^2}} = \sqrt{\frac{C_2 g_{ms}}{C_1 g_{ms}}}$$

For the same C_2 , the Ahuja configuration has a higher Q factor than the voltage buffer configuration, as poles remain close and don't tend to right.

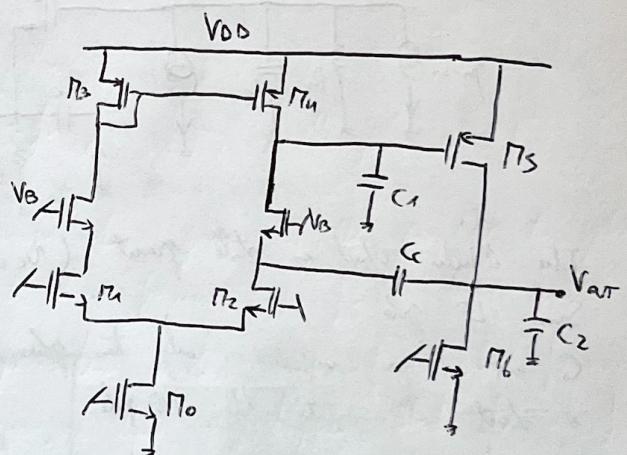
• ALTERNATIVE AHUJA (CASCODE)

Compute b_3, b_2, b_1 and a_2, a_1 and write

$$b_3 s^3 + b_2 s^2 + b_1 s + 1 = 0$$

$\Downarrow \approx$

$$(1+b_1 s) \left(1 + \frac{b_2}{b_1} s + \frac{b_3}{b_1} s^2 \right) = 0$$



COMPENSATION (THREE-STAGE)

We are three-stage OTAs as this we need a large gain and do not have enough voltage headroom to accommodate cascades:

now we have three light
infrared modes, as we need
two separate Milli conversations.

- ① Place a Mill.

Capacitor across the third stage, in order to split f_2 and f_3 .

Let's consider only the
cascade of the last
two stages:

$$LBINP_{23} = \frac{gm^2}{2\pi C_c}$$

$$j_3' = \frac{q m_3}{2\pi(C_3 + C_2)}$$

$$f_2 = \frac{q m_3}{2\pi C_c} \quad (\text{POSITIVE})$$

Usually $q_{m3} > q_{m2}$, so we can neglect f_2 .

Set ρ_m of the 2-3 stage by using $f_3 = 2 \text{ CBWP}_{23}$

- ② Place an outer Miller separator in order to further lower the frequency of the first pole.

$$GBWP = \frac{q_m 1}{2\pi G_1}$$

The next job can be started by shorty G.

$$R_3^{(o)} = \frac{R_1/R_3}{1 + g_{m2} R_2 g_{m3} (R_1/R_3)} \quad (\text{sum by } C_1 \text{ and } C_3 \text{ in } II) \approx \frac{1}{g_{m2} R_2 g_{m3}}$$

$$R_2^{(o)} = \frac{R_2}{1 + \frac{q_{m2} R_2}{q_{m3} (P_1 / R_3)}} \approx \frac{1}{q_{m2} q_{m3} (P_1 / R_3)}$$

$$R_{C_C}^{(o)} = \frac{1}{gm_2} - \frac{1}{gm_3}$$

↓

$$= \frac{gm_3 - gm_2}{gm_3 gm_2}$$

$$\begin{cases} \dot{x}_1 = -g m_2 v_1 \\ \dot{x}_2 = -g m_3 v_1 \end{cases}$$

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$$v_1 = -\frac{15}{1}$$

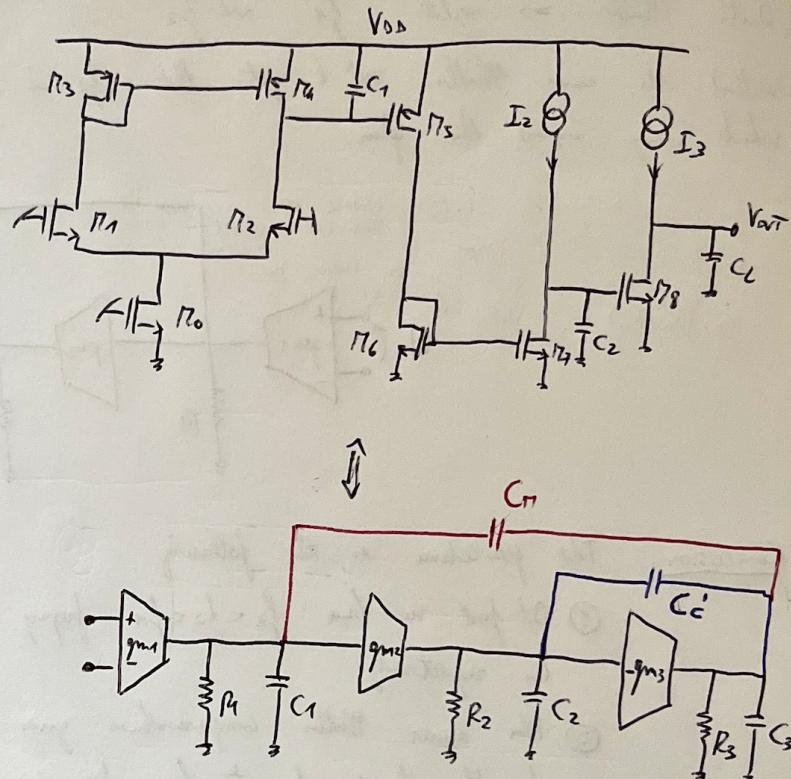
gm5

$$V_2 = -\frac{15}{5m}$$

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$$V_1 - V_2 =$$

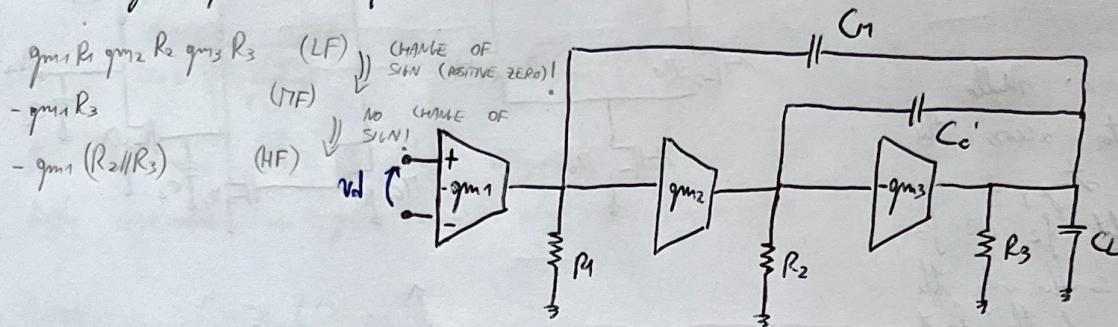
$$v_1 - v_2 = \pi^2 \left(\frac{1}{g^{m_2}} - \frac{1}{g^{m_3}} \right)$$



(29) A better state of the regulators can be obtained if neglecting C_1 and C_2 , via the capacitated loads at the nodes are damped by the Miller compensation.

$\left\{ \begin{array}{l} \text{Inner Miller} \Rightarrow \text{pre-split } f_2 \text{ and } f_3 \\ \text{Outer Miller} \Rightarrow \text{split } f_1 \text{ and } f_2 \end{array} \right. \quad (\text{ROOT LOCUS})$

Without the inner Miller we'd get that f_2 and f_3 may have a pole gain, which may degrade the φ_m .



Conclusion: The procedure is the following:

- ① At first we have $f_3 < f_2 < f_1$ frequency of the poles due to C_3, C_2 and C_1 respectively
- ② The inner Miller compensation gives us the possibility to split f_3 and f_2 , thus bring f_2 to lower frequencies and f_3 to higher ones
- ③ The outer Miller compensation moves f_1 to lower frequencies and f_2 to higher frequencies, so there may happen that f_2 and f_3 move closer to each other and become a pole gain.

Neglecting the fact that the outer Miller compensation brings f_2 and f_3 closer to each other, we have that

$$\left\{ \begin{array}{l} GBWP \approx \frac{g_{m1}}{2\pi C_M} \\ f_2 \approx \frac{g_{m2}}{2\pi C_c} \quad (\alpha \frac{1}{2\pi} \frac{1}{C_c \left[\frac{g_{m3} - g_{m2}}{g_{m2} g_{m3}} \right]}) \\ f_3 \approx \frac{g_{m3}}{2\pi C_3} \end{array} \right.$$

Therefore, in order to improve φ_m , we could either

→ increase g_{m2} (MORE POWER DISSIPATION) and g_{m3}

→ decreasing GBWP by moving C_1 (MORE SILICON AREA)