

Politecnico di Milano

Radiation Detection Systems

Hand Notes on the course programme

Leonardo Airoldi
a.a. 2023-2024



POLITECNICO
MILANO 1863

Disclaimer: these are my hand notes on the course of Radiation Detection Systems, taken during the academic year 2023-2024. I made these notes in preparation for the exam, reviewing the theory using mainly the slides. As they are not based on live class notes, some observations made by the professor may be missing.

These notes are not a substitute of the slides. Especially on the last part, some slides content may be missing. I made these notes for myself and did not plan to publish them in advance, so I also apologize for my bad writing and page layout.

Last thing, don't take everything in these notes as true! Some uncertainties are still in it, and maybe even errors I'm not aware of! So please, **question everything** and check yourself if what is written makes sense. I personally suggest in following the course as the professor for me was really clear and also available to discuss every doubt.

Good luck!

MEGASCHEMA RADIATION

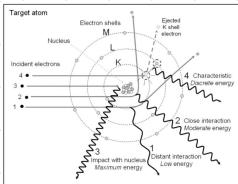
Monday, April 29, 2024 10:42 AM

RADIATION SOURCES

$$E(\text{eV}) = \frac{1240}{\lambda(\text{nm})} \quad E = h\nu = \frac{hc}{\lambda}$$

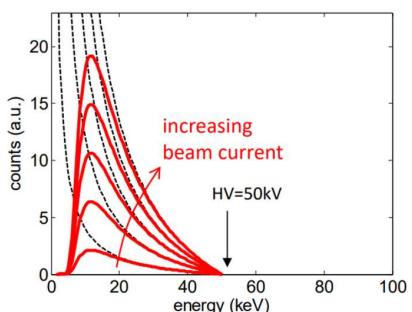
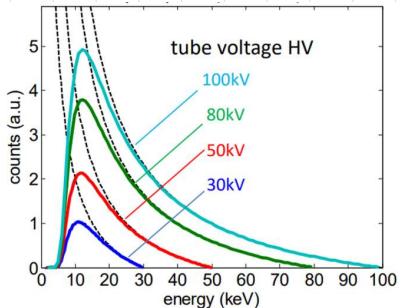
frequency

ENERGY



Hitting material with electrons

- ⇒ curving electrons ⇒ accelerating electrons
- ⇒ release of photons



spectrum of electrons including characteristic peaks (XRF)

$$I(h) = \frac{k}{h^2} \left(\frac{h}{h_{\min}} - 1 \right)$$

$$[\text{ph}/\text{h}] \text{ radiation intensity} = \frac{dh}{dh}$$

$$h_{\min} \Rightarrow E_{\max} = V/q \quad V \cdot q$$

E_{\max} proportional to high voltage giving energy to electron

$$I(E) = \left| \frac{dh}{dh} \right| \cdot \frac{dh}{dE} = \frac{k}{hc} \left(\frac{E_{\max}}{E} - 1 \right)$$

$$E = \frac{hc}{\lambda}$$

$$\left| \frac{dh}{dE} \right| = hc \frac{1}{E^2}$$

$$\begin{aligned} E_{\text{ph}}/E &= \frac{E(E)}{\text{emitted energy}} \cdot I(E) \cdot \frac{1}{E} = \frac{k}{h} \left(\frac{E_{\max}}{E} - 1 \right) \\ &\uparrow \quad \uparrow \quad \uparrow \\ &= \frac{k}{hc} \cdot \left(\frac{E_{\max}}{E} - 1 \right) \\ &= k (E_{\max} - E) \end{aligned}$$

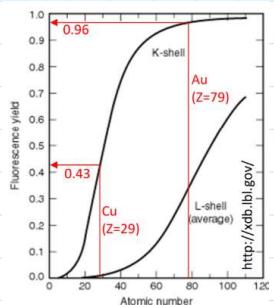
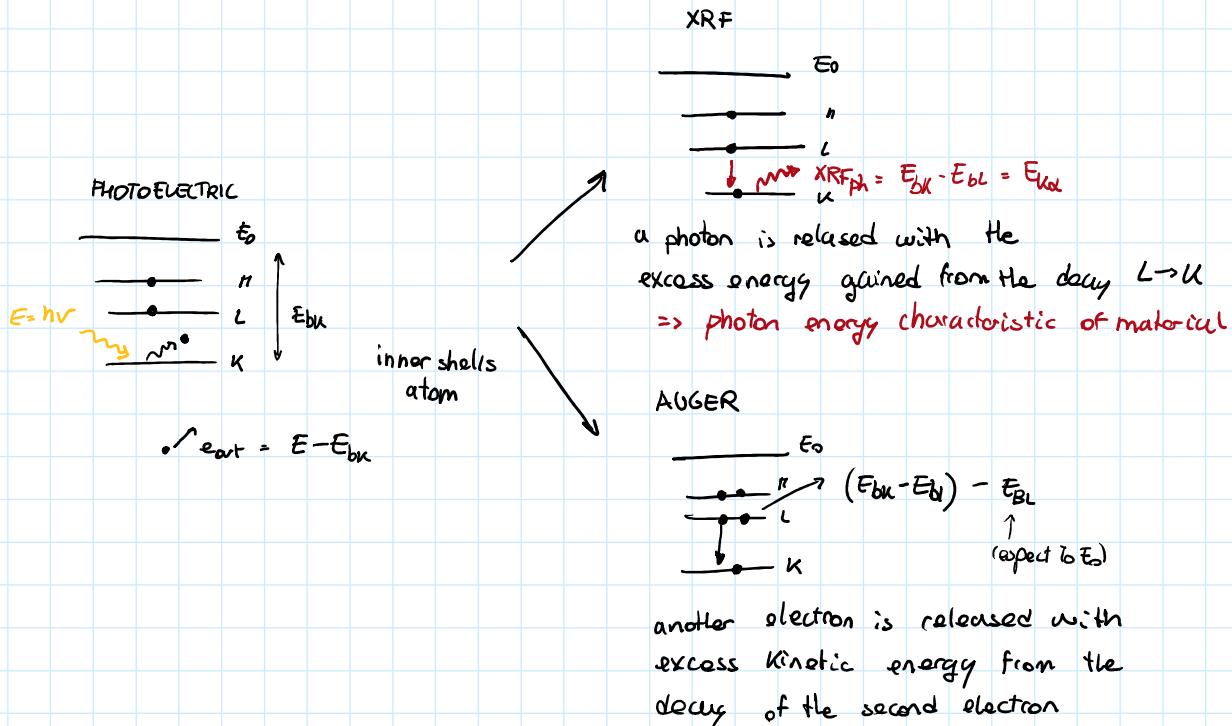
BREMSTRAHLUNG X-RAYS

Other radioactive sources..

- × Synchronous radiation
 - circular movement of electrons
- × Radioactive element decay

RADIATION SOURCES

PHOTONS INTERACTION



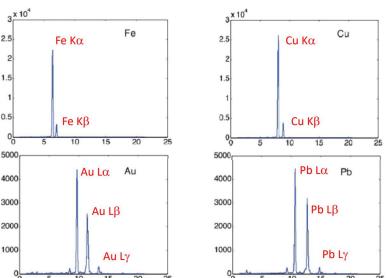
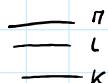
Characteristic X-rays derive from XRF

=> fluorescence yield
 probability of XRF against Auger

=> heavier elements tends to XRF more
 (Auger is harder in heavier elements, because needs another extraction from an inner shell)

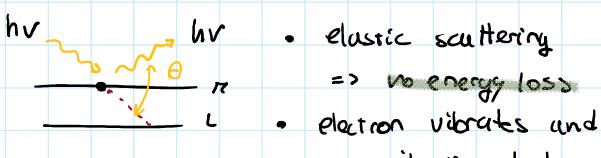
XRF nomenclature

- $K\alpha$: drop from L ($\alpha = 1$ above) to K
- $K\beta$: drop from M ($\beta = 2$ above) to K
- $L\alpha$: drop from M ($\alpha = 1$ above) to L



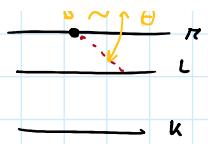
possible to do spectroscopy

PHOTOELECTRIC EFFECT



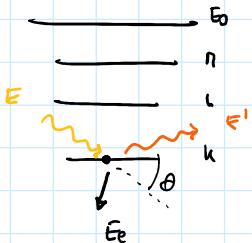
$$x = \frac{\sin(\theta)}{\lambda}$$

↑
 momentum transfer



- electron vibrates and re-emits the photon
 - change of momentum as direction changes
- \$\Rightarrow\$ no energy loss
momentum transfer

RALEIGH SCATTERING



- inelastic scattering
\$\Rightarrow\$ photon loses energy
- relevant only at high energies

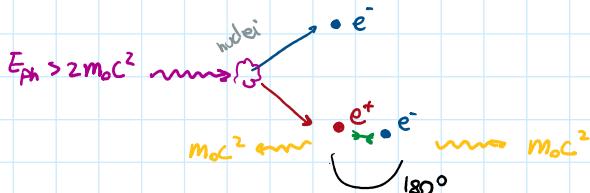
$$E' = \frac{E}{1 + \frac{E}{mc^2} (1 - \cos\theta)}$$

$$\begin{aligned} E &\gg mc^2 \\ &511 \text{ keV} \end{aligned}$$

$$E_e = E - E' - E_K$$

↑ ???

COMPTON EFFECT



- interaction with nucleus
- extremely HE photon (\$\rightarrow\$ 2 rest mass electron)
- \$e^-e^+\$ (positron) pair production
- positron soon annihilates w other electrons
\$\Rightarrow\$ producing 2 photons

PAIR PRODUCTION

INTERACTION PROBABILITY

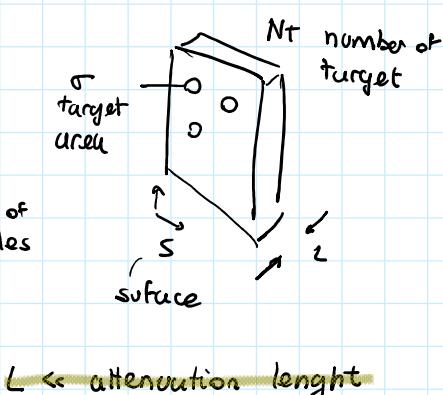
$$P_{int} = \frac{N_t \sigma}{S} = \frac{n_t \cdot \sigma \cdot S L}{S} \quad [1]$$

interaction probability

$$= n_t \sigma L \quad [1]$$

↑
target/
cm³ density

N_p
number of
particles



$L \ll$ attenuation length

$$N_t = \frac{\rho}{A u} \cdot L$$

$\rho = \text{g/cm}^3$ density
 $A = \text{atomic mass}$
 $u = \text{atomic mass unit}$

$$\text{NIT} = \frac{1}{A_U}$$

A = atomic mass
U = atomic mass unit

$$P_{\text{int}} = \frac{\rho}{A_U} \sigma L = \mu \rho L$$

$$\mu = \frac{\sigma}{A_U} \quad [\text{cm}^2/\text{g}] \quad \text{mass attenuation coefficient}$$

INTERACTION PROBABILITY

$$\bar{\Phi} \left[\frac{\text{f}^{\text{b}} / A_t}{\text{S} \cdot \Delta t} \right] = \frac{N_p}{S \cdot \Delta t}$$

↑
incident flux density
(und. rate)?

$$N_{\text{events}} = N_{\text{ph}} \cdot P_{\text{int}} = \bar{\Phi} S \Delta t \frac{N_t \sigma}{S}$$

$$= \bar{\Phi} N_t \sigma \Delta t$$

$$R_{\text{events}} = \frac{N_{\text{events}}}{\Delta t} = \bar{\Phi} N_t \sigma$$

cross section is found and defined experimentally from ↑

$$\sigma = \frac{N_{\text{events}}}{\bar{\Phi} N_t \Delta t} = \frac{N_{\text{events}}}{N_{\text{ph}}} \cdot \frac{S}{N_t} = \frac{N_p}{N_{\text{ph}} n_t L} \quad N_t = n_t \cdot S L$$

EVENT RATE

For different events, we define different target cross section

⇒ the total cross sections takes all possible interactions into account

$$\sigma = \sigma_{\text{photoelectric}} + \sigma_{\text{incoh}} + \sigma_{\text{coh}} + \dots$$

↑
incoherent (inelastic) scattering elastic (coherent) scattering

derives

$$\Rightarrow N = N_{\text{pe}} + N_{\text{incoh}} + N_{\text{coh}} \dots$$

For mixed materials / compounds

$$\mu = \sum_i w_i \mu_i \quad [\text{cm}^2/\text{g}]$$

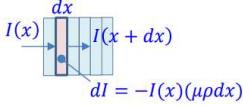
↑
relative MASS weight
in the compound

$$\begin{aligned} S &= N_t \sigma = n_t S L \sigma \\ S_1 &= N_{t_1} \sigma_1 + N_{t_2} \sigma_2 \\ &= (n_{t_1} \sigma_1 + n_{t_2} \sigma_2) S L \\ &= \left(\frac{\rho_1}{A_{t_1}} \sigma_1 + \frac{\rho_2}{A_{t_2}} \sigma_2 \right) S L \\ &= (\mu_1 \rho_1 + \mu_2 \rho_2) S L \\ &\quad \frac{\text{cm}^2}{\text{g}} \cdot \frac{\text{g}}{\text{cm}^3} = \text{cm}^2 \end{aligned}$$

COMPOSITE MASS ATTENUATION COEFFICIENT

$$P_{\text{int}} = \mu \rho L \Rightarrow P_{\text{int}} = \mu \rho \quad [\text{cm}^{-1}]$$

probability per unit length



$$I(x+dx) - I(x) = -\bar{f}(x) \mu \rho dx \quad \frac{dI}{dx} = -\mu \rho I(x)$$

$$\int_{I_0}^{I(x)} \frac{dI}{I(x)} = -\mu \rho \int_0^x dx$$

$$\left[\ln(I) \right]_{I_0}^{I(x)} = -\mu \rho x$$

$$\ln\left(\frac{I(x)}{I_0}\right) = -\mu \rho x$$

$$I(x) = I_0 e^{-\mu \rho x}$$

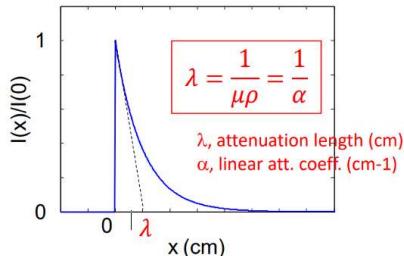
$$I(x) = I_0 e^{-x/\lambda}$$

$$\text{h (cm)} = \frac{1}{\mu \rho} = \frac{1}{\alpha}$$

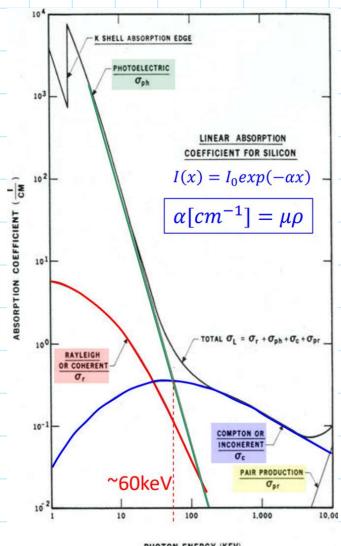
attenuation length

$$\alpha [\text{cm}^{-1}] = \text{prob} = \mu \rho$$

attenuation coefficient



ATTENUATION LENGTH



$$\alpha = \frac{1}{\lambda} \text{ absorption coefficient}$$

- dependence on photon energy
- different types of interaction

=> note the photoelectric K-shell jump when becomes

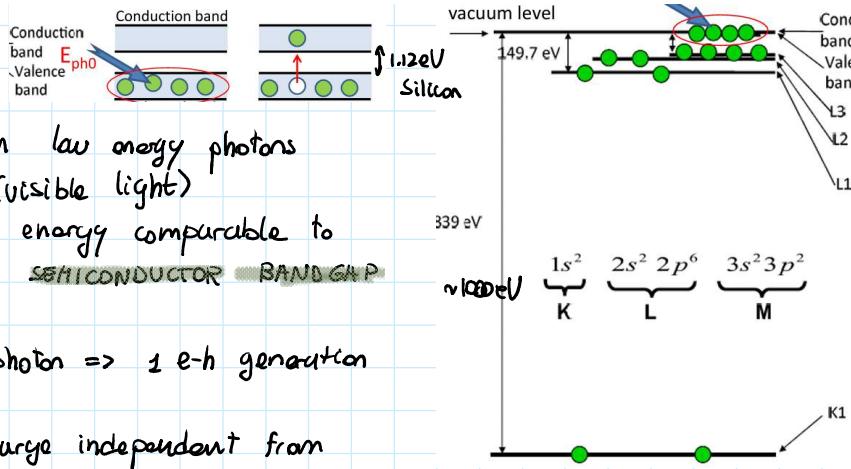
$$E_{ph} = h\nu > E_K$$

INTERACTION COMPONENTS

CHARGE GENERATION

Electron devices detect charge, so we must relate the photon interaction to a charge effect

VISIBLE - IR - UV light ~ 2eV



1 photon \Rightarrow 1 e-h generation

| Charge independent from photon energy

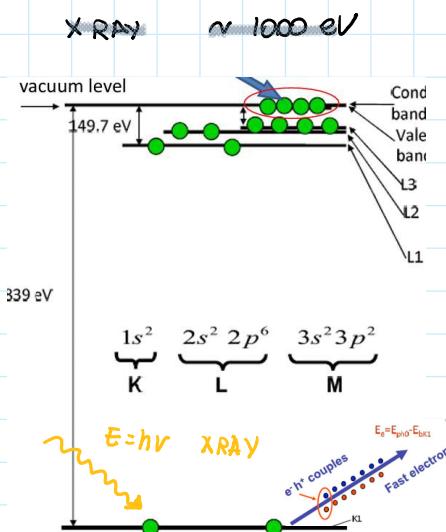
E-H PAIR VISIBLE LIGHT

A x-ray photon interacts with photoelectric effect with K shell ~ 1000 eV

\Rightarrow The free electron has a high kinetic energy

\Rightarrow The high kinetic energy

$$E_{\text{free}} = h\nu - E_{\text{K shell}}$$



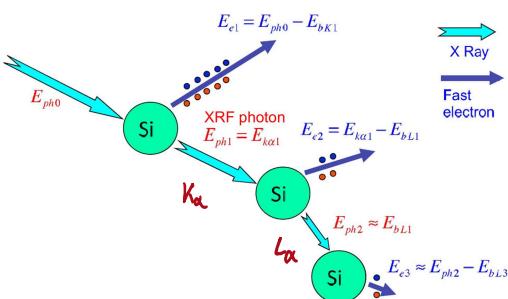
is dissipated through the creation of many e-h pairs ($> 2 \text{ eV}$) in conduction / valence

| charge dependant on photon energy

E-H PAIR GENERATION X RAYS

With high energy X RAYS a chain reaction can occur realising more XRF photons from different atoms in the materials

\hookrightarrow Problem because we will see XRF photons detected, but in reality they are coming from the detector itself



XRF CHAIN

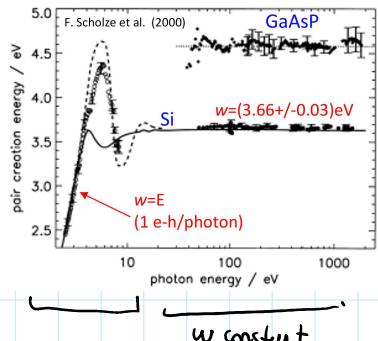
The relation between radiation energy converted into e-h

The relation between radiation energy converted into e-h
is a property of the material

$$w(E) = \frac{E}{\langle N \rangle}$$

$$w = 3.63 \text{ eV Si (300K)}$$

$$w = 2.96 \text{ eV Ge (300K)}$$



$$\begin{aligned} w(E) \propto E &\Rightarrow \langle N \rangle \propto E \\ \langle N \rangle \text{ constant} &\Rightarrow \text{XRAY} \\ \Rightarrow \text{independent} & \\ \text{on photon } E & \\ \text{LIGHT - UV - IR} & \end{aligned}$$

N number of generated e-h pairs

$$\langle N \rangle = \frac{E}{w(E)} = \frac{hv}{w(E)}$$

w constant for photoelectric
XRAY generation

$$\sigma^2(N) = F \cdot \langle N \rangle = F \cdot \frac{E}{w}$$

F FANO FACTOR

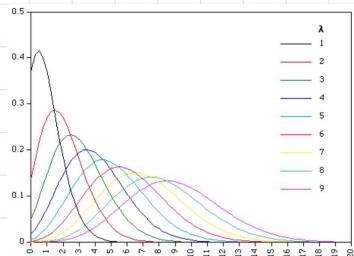
\Rightarrow variance of generated e-h pair
coefficient

$$F = 0.12 \text{ Si}$$

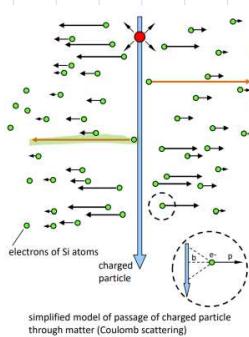
$$F = 0.13 \text{ Si}$$

Where
 N follows a poissonian
(modified by fano factor, so $\sigma^2(N) \neq \langle N \rangle$)
distribution

\hookrightarrow tends to gaussian when $\langle N \rangle$ big



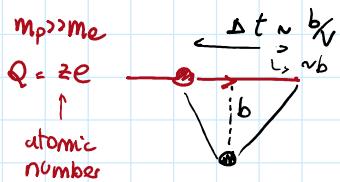
CHARGED PARTICLES INTERACTIONS



charged particles entering the device
lose energy

- by giving kinetic energy to nearby electrons, ionizing the atoms ($\mu t + \text{atom} = \mu t + \text{atom} + e^-$)
- by close collisions that generate gamma rays
- by emissions of a photon due to the deceleration induced by

- by emissions of a photon due to the deceleration induced by the substrate

CHARGE INTERACTIONS

Assumptions

- Energy transfer \gg binding energy
- Electron at rest during interaction (impulse transfer)
- Whole momentum transferred to electron

Model:

$$F_c = \frac{1}{4\pi\epsilon_0} \frac{ze^2}{b^2} \quad \text{coulomb force} \sim \frac{ze^2}{b^2}$$

$$\Delta t = \frac{b}{v} \quad \text{interaction time}$$

$$I_{\perp} = m_e v_e = \int F dt = \frac{ze^2}{b} \cdot \frac{1}{v} \quad \text{impulse} = \text{momentum transfer}$$

$$= p_e =$$

$$\hookrightarrow v_e = \frac{ze^2}{bv m_e}$$

$$W = \frac{1}{2} m_e v^2 = \frac{p^2}{2m_e} = \frac{z^2 e^4}{b^2 v^2 2m_e}$$

I struggled on this part, so check!

↳ Energy transfer in a material

$$dE = \frac{z^2 e^4}{b v^2 2m_e} \cdot v dt \Rightarrow \boxed{\frac{dE}{dx} = \frac{z^2 e^4}{V^2 2m_e b}}$$

 \Rightarrow integrate over $[b_{\min}, b_{\max}]$ We then obtain Bethe - Bloch formula

↳ Energy loss is normalized by target density

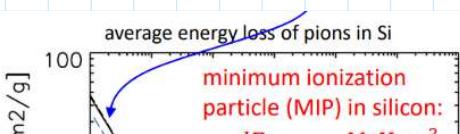
 $\alpha \frac{1}{\sqrt{z}} \alpha \frac{1}{E} ??$

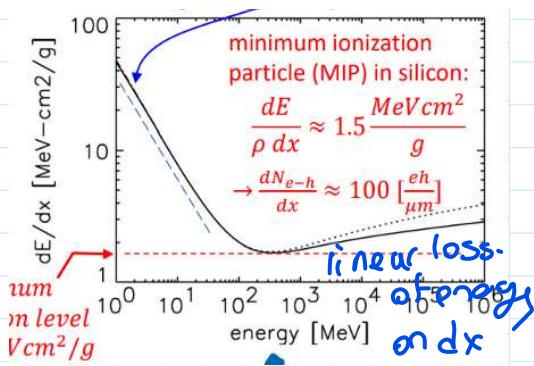
$$-\frac{1}{\rho} \frac{dE}{dx} \propto \frac{z^2}{A \beta^2} \propto \frac{1}{E} \propto \left[\frac{\text{MeV}}{\text{cm}^{-2}} \frac{\text{cm}^3}{\text{g}} \right] \propto \frac{1}{E^2}$$

↑
to normalize term with density

\downarrow $v/\sqrt{z} \approx \text{Energy}$ of incoming particle (due to $v/\sqrt{z} \Rightarrow$ velocity of ionizing incoming particle)

if plotted against energy?

Using mean creation energy (W_0 in silicon)



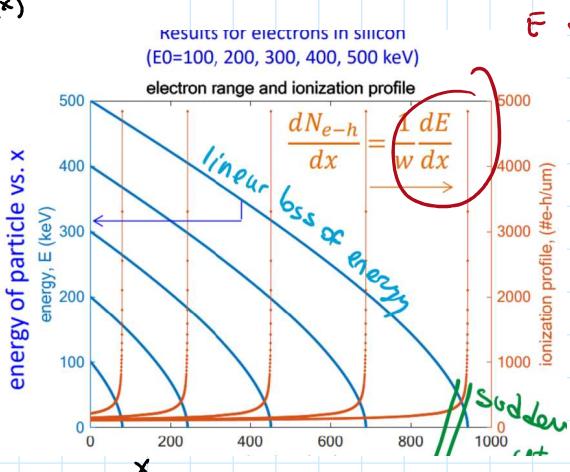
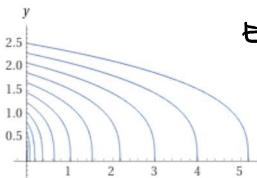
Using mean creation energy (W_e in silicon)

\Rightarrow we can derive number of electron hole pairs generated with the energy released by He ionizing radiation in silicon

$$\Rightarrow \frac{dN_{e-h}}{dx} = \frac{dN_{e-h}}{W_e dx} \approx 100 \frac{e-h}{\mu m}$$

Ionization profile is obtained through integration

$$-\frac{dE}{dx} = f(E) \propto \frac{1}{E^2} \quad \Leftarrow \text{differential equation}$$

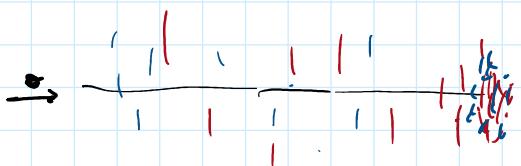


E tends to 0 near the end

$$\frac{dE}{dx} \propto \frac{1}{x^2} \downarrow$$

$\frac{dE}{dx} \uparrow$

Big energy released near the end!



trajectory with big drop of energy near the end

MEASUREMENT SEMICONDUCTORS

Monday, June 24, 2024 2:39 PM

1) PAIR CREATION ENERGY 2-5 eV (3.63 eV Si)

- low: large number of charges in detector per photon
- higher energy resolution \Rightarrow high S/N ratio

2) ENERGY GAP 1-3 eV

- small leakage current (good medium high bandgap)
- \hookrightarrow less shot noise

3) DENSITY $\rho = 2-10 \text{ g/cm}^3$

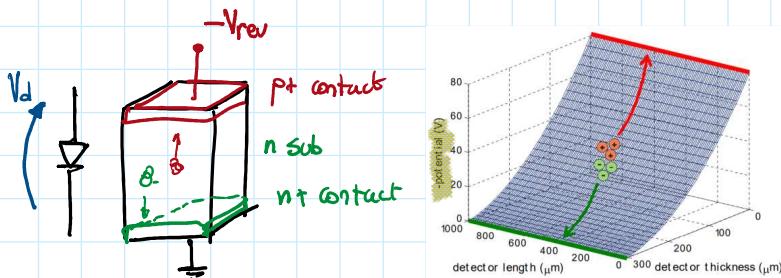
- high energy loss per unit length for ionized part. detection \sim

$$\frac{dE/e}{dx} = 100 \text{ eV}/\mu\text{m} \text{ Si MIP}$$

(minimum ionization particle)

- \hookrightarrow thin detectors
- \hookrightarrow precise position measurement

WHY SEMICONDUCTOR

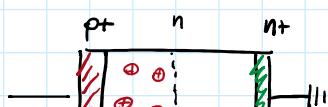


p-n junction
is reversed

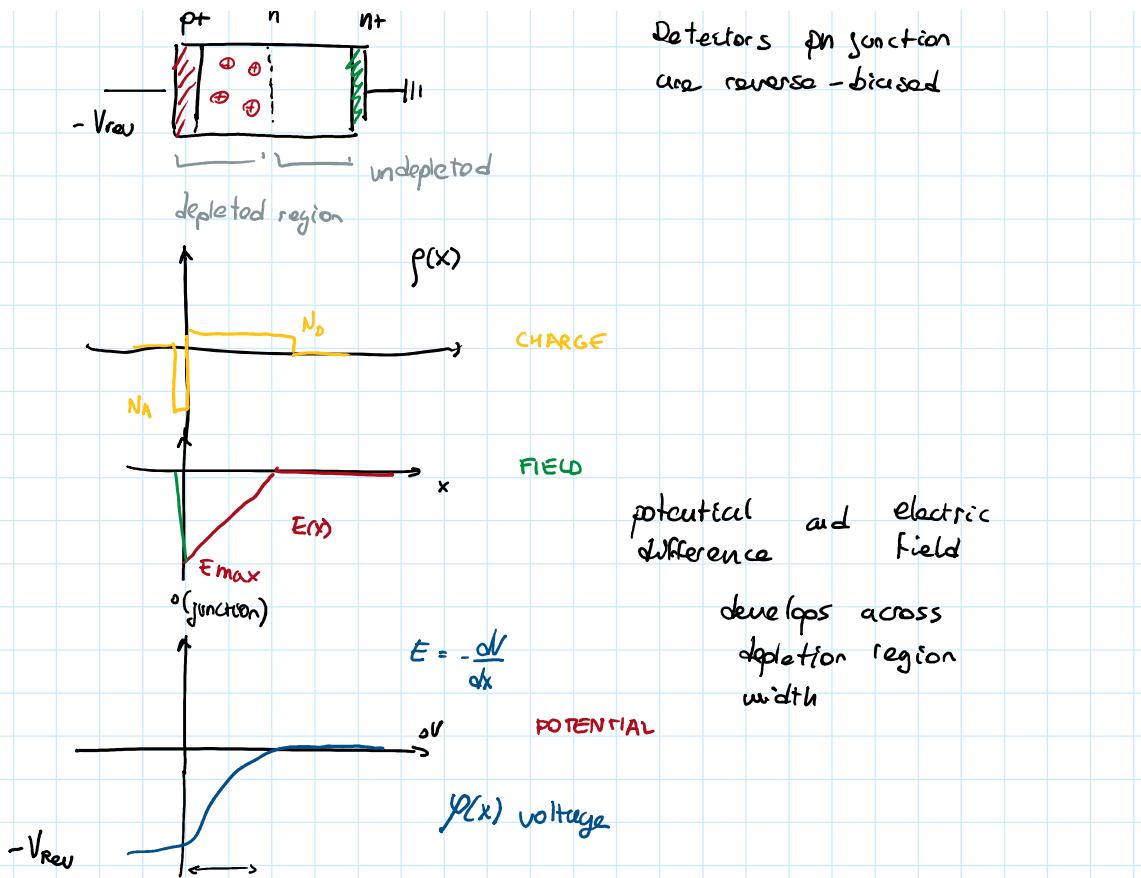
$$C_{dep} = \epsilon_0 \epsilon_r \frac{A}{t_{dep}} \quad \rightarrow \text{detector active area}$$

$$t_{dep} \rightarrow \text{depletion region width}$$

P-N JUNCTION



Detectors p-n junction
are reverse-biased



Detectors p-n junction
are reverse-biased

develops across
depletion region
width

potential and electric
field

$$E = -\frac{dV}{dx}$$

POTENTIAL

$$\frac{dE}{dx} = \frac{\rho}{\epsilon}$$

$$-\frac{d\phi}{dx} = E$$

potential - field - charge
equations 2D

$$\frac{d^2\phi}{dx^2} = -\frac{\rho}{\epsilon}$$

$$\frac{d^2\phi}{dx^2} = -\frac{qN_d}{\epsilon}$$

Poisson equations

Poisson equation

Assuming a unibipolar Junction - highly doped p+

$$\rightarrow E(x) = \int_0^x \frac{\rho}{\epsilon} dx = \frac{\rho}{\epsilon} (x - x_d)$$

$$|E_{max}| = \frac{qN_d}{\epsilon} x_d \quad x_d \approx x_n$$

$$V_{bias} = \int_0^{t_{dep}} -E dx = \underbrace{\sim \frac{1}{2} |E_{max}| x_d}_{\text{triangle area}} = \frac{1}{2} \frac{qN_d}{\epsilon} x_d^2$$

\times detection is useful only
if it happens in the undepleted region

- x** detection is useful only if it happens in the depleted region
⇒ field moves charges and creates signal

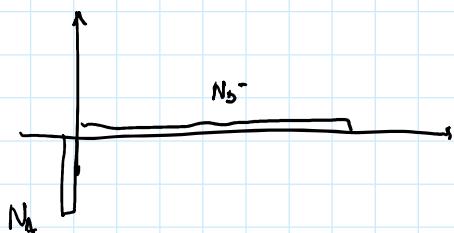
- x** if undpleted, the generated electron-hole pair shortly recombines

PN JUNCTION DETECTOR

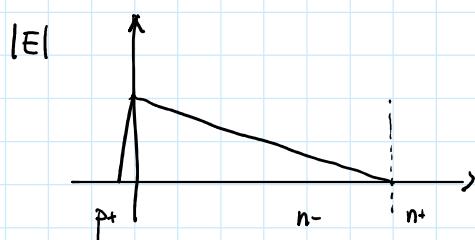


how to avoid neutral region problems:
fully deplete region

how to fully deplete:
dope n region to have $t_{dep} = \text{width device}$

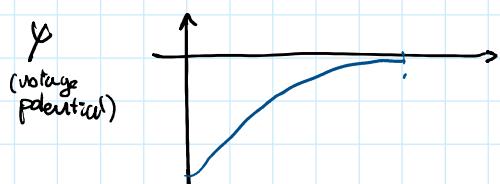


$$P_n = qN_D$$



$$\frac{dE}{dx} = \frac{qN_D}{\epsilon}$$

$$|E_{max}| = \frac{qN_D}{\epsilon} \cdot x_{dep} = \frac{qN_D}{\epsilon} W$$



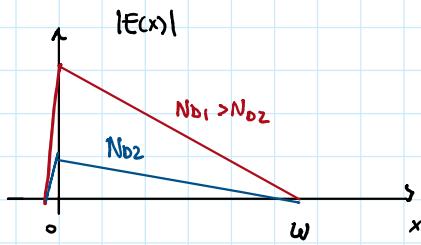
$$-\frac{d\phi}{dx} = E$$

$$V_{dep} = \int -Edx = \frac{1}{2} \frac{qN_D}{\epsilon} W^2$$

$$E_{max}$$

- x** neutral region removed, so possible to detect charge in whole device
- x** back side unwanted illumination is possible

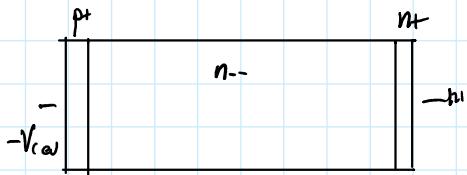
Balance between



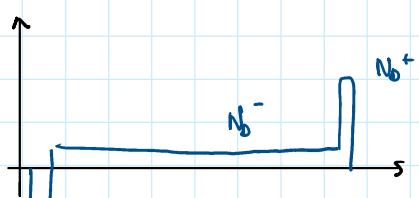
$$E_{\max} = \frac{qN_D}{\epsilon} w \quad \text{critical field} \propto \text{doping}$$

- need low substitute doping avoiding large E field

FULLY DEPLETED PN DETECTOR

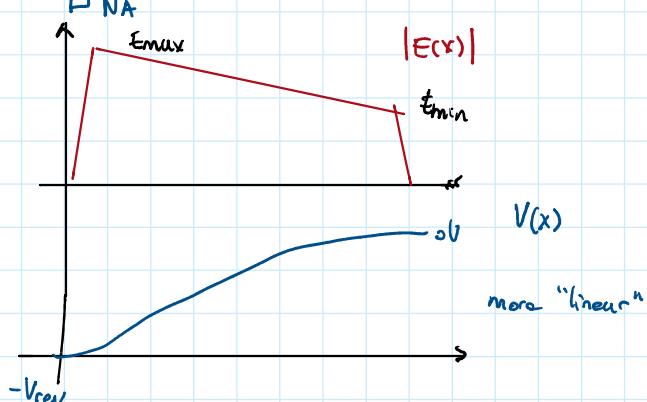


The fully depletion concept can be also improved:
overdoping - punch through of electric field



Doping in the n- region is so low that the electric field punches through to the n+ region

Depletion region reaches n+



$$V_{\text{bias}} = V_{\text{dep}} + \Delta V_{\text{dd}}$$

$$E_{\min} = \frac{\Delta V_{\text{dd}}}{d} \quad (\text{offset of constant } E_{\min})$$

$$E_{\max} = E_{\min} + E_{\text{dep}}$$

$$= \frac{\Delta V_{\text{dd}}}{d} + \frac{2V_{\text{dep}}}{d}$$

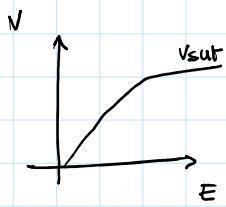
$$\hookrightarrow V_{\text{dep}} = \frac{1}{2} E_{\text{dep}} d$$

$$E(x) = E_{\min} + \frac{qN_b}{\epsilon} (x_0 - x)$$

- E_{\min} helps avoiding near zero fields, resulting in a low velocity of generated e-h pairs and in a more probability of recombination

E_{min} could be sized to ensure that the carriers are always in saturation velocity

As Drude model



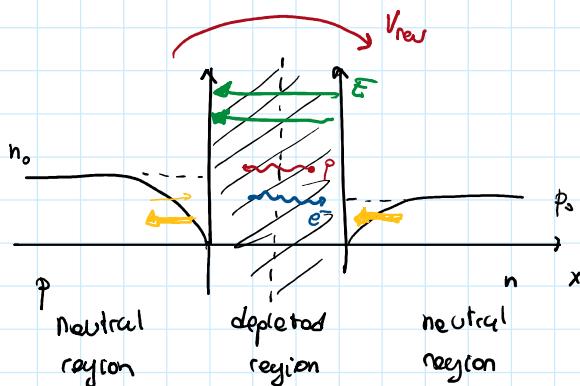
$$v = \mu E$$

$$v_{sat} = N E_{sat}$$

↑ mobility

$$E_{min} = E_{sat} = \frac{v_{sat}}{\mu} \rightarrow 10^7 \text{ cm/s Si}$$

PUNCH THROUGH - OVERDEPLETED PN JUNCTION DIODE



minorities
concentration
in a reverse biased
junction -
long base diode

$$\text{mass action law}$$

$$np = n_i^2$$

$$\text{min concentr } n_{min} = \frac{n_i^2}{N_A}$$

proton

current conserves through all device. current can be derived from neutral region

$$J_{tot} = J_{diff_n}(0) + J_{diff_p}(0)$$

by Ficks law

$$J_{tot} = + q D_n \frac{dn}{dx} \Big|_0 - q D_p \frac{dp}{dx} \Big|_0$$

$$J_{tot} = q D_n \frac{n_0}{L_n} + q D_p \frac{p_0}{L_p}$$

in long base diodes, lifetime of carriers is

$$L_n = \sqrt{D_{n\text{th}}} \text{ (from charge continuity eq.)}$$

$$J_{tot} = q \left(n_{p0} \frac{L_n}{Z_n} + p_{n0} \frac{L_p}{Z_p} \right)$$



$$n(0) = n_0 e^{qV_B/2kT} \text{ bolzmann}$$

$$n(x) = n_0 \left(e^{qV_B/2kT} - 1 \right) e^{-x/L_n} + n_0$$

when $V_B = -V_{BE}$
(reverse biased diode)
 $n(0) \approx 0$

$$\Rightarrow n(x) = n_0 \left(1 - e^{-x/L_n} \right)$$

LEAKAGE REVERSE CURRENT | NO GENERATION IN DEPLETION REGION

$$I_{\text{tot}} = \underbrace{q \left(n_{\text{ns}} \frac{L_n}{Z_n} + p_{\text{no}} \frac{L_p}{Z_p} \right)}_{\text{neutral region contribution}} + \underbrace{\frac{q h_i}{2Z} W_{\text{dep}}}_{\text{obtain } W_{\text{dep}} \text{ from poisson eq.}}$$

Neutral region contribution

Generation/Recombination processes depleted region

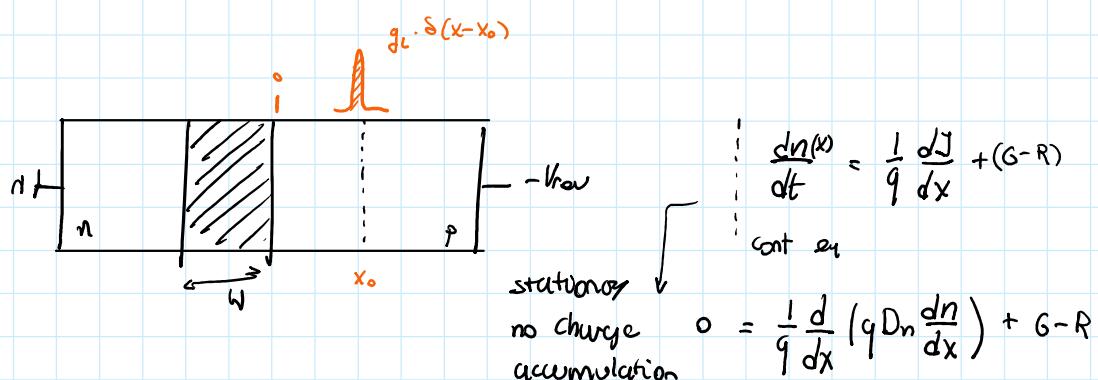
LEAKAGE REVERSE CURRENT

with a fully depleted diode, only contribution to the current are generation / recombination processes in depleted region

$$I_{\text{leak}} \approx q \frac{h_i}{2Z} W_{\text{dep}} (\omega) \quad 1.45 \cdot 10^{10} \text{ cm}^{-3}$$

FULLY DEPLETED JUNCTION - REVERSE LEAKAGE CURRENT

LEAKAGE CURRENT



continuity equation in p neutral region

$$D_n \frac{d^2 n(x)}{dx^2} - \frac{n(x) - n_0}{Z_n} = -g \delta(x-x_0)$$

$\frac{1}{q} \frac{dI}{dx}$

R

G

recombination rate

We can obtain the current generated by this δ like collection solving the continuity equation in point $x=0$ as before

solving the continuity equation in point $x=0$ as before

$$J_n = q D_n \frac{dn(0)}{dx} = q D_n \frac{n_0}{L_n} - q g_L e^{-\frac{x_0}{L_n}}$$

(assuming
 $e^{\frac{qV_{rev}}{kT}} \approx 0$)

from this term

we obtain that generated current is \sim detectable when collection happens in depleted region or at $x < L_n$ in the neutral region

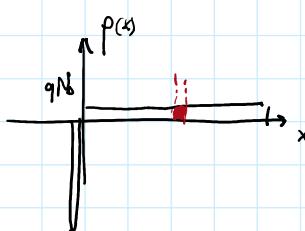
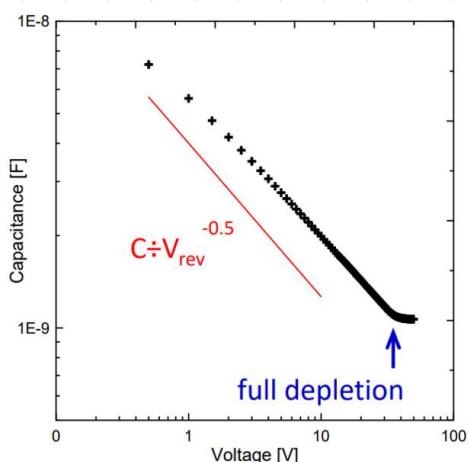
Why?

From differential equation solution

NEUTRAL REGION DETECTION

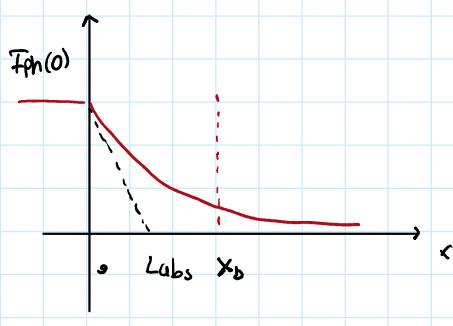
$$C_D' = \left. \frac{dQ}{dV} \right|_{V=V_R} \cdot \frac{1}{A} = \frac{\epsilon_0 \epsilon_r}{w(V_k)} \approx \frac{\epsilon_0 \epsilon_r}{x_n(V_R)} \propto \sqrt{\frac{N_d}{V_R}}$$

unilateral junction



Capacitance goes down with increase of w_{dep}

DEPLETED REGION CAPACITANCE



$$I_{ph}(x) = I_{ph}(0) e^{-\frac{x}{L_{abs}}}$$

attribution / absorption length

DETECTION / QUANTUM EFFICIENCY

$$\gamma = \frac{I_{ph}(0) - I_{ph}(x_0)}{I_{ph}(0)}$$

$$\gamma = 1 - e^{-\frac{x_0}{L_{abs}}} = 1 - e^{-N_p x}$$

We should size

$$x_0 \approx \frac{N_p}{\text{few}} L_{abs}$$

$\Rightarrow \approx$ all particles absorbed are generating current signal

depletion region
dependent on
electronics or
detector
Vbias N_d

size
 \Leftarrow

absorption length
light - material
dependent on
 N_p ρ
material

DEPLETED REGION SIZING

HIGH RESISTIVITY LOW DOPING DETECTORS

low doping

\Rightarrow allows for fully depleted junctions without exceeding high E_{max}

(?)
true?

RESISTIVITY DEPENDS ON DOPING

$$\rho = \frac{l}{q N_{res}}$$

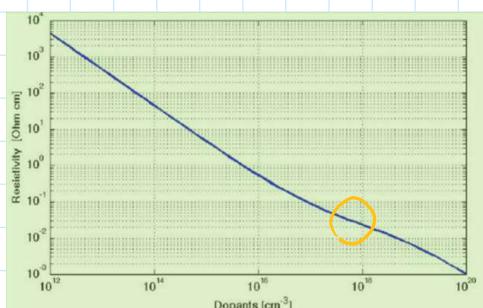
[N] direct dependence on doping

$N \uparrow \quad \rho \downarrow$
more carriers

[P] $N \uparrow \quad \mu \downarrow \quad \rho \uparrow$

mobility lowers as there are more scattering effects

2nd order effect



RESISTIVITY

low doping limit

\hookrightarrow increases non-linearly as under 10^{12} cm^{-2}

low doping limit

→ we cannot realistically go under 10^{12} cm^{-3} of doping
($n_i = 1.45 \cdot 10^{10} \text{ cm}^{-3}$)

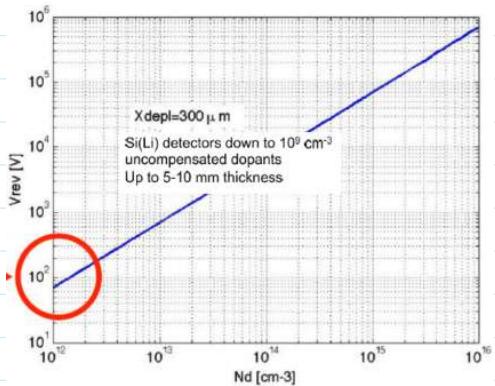
→ in perspective 10^{12} cm^{-3} is an extremely pure silicon crystal - too pure to obtain!

$10^{12} \text{ cm}^{-3} \rightarrow 1 \cdot \mu\text{m}^{-3}$ dopant concentration
= 1 dopant per μm^3

In $1 \mu\text{m}^3$ there are

$$\frac{1 \mu\text{m}^3}{(0.5)^3 \cdot \mu\text{m}^3} = 8 \cdot 10^9 \text{ silicon lattice units}$$

LOW DOPING LIMIT

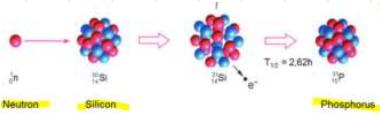


Required to keep reasonable voltage / field values!

$$E_{max} = \sqrt{\frac{2q}{\epsilon} N_0 k_{B} T}$$

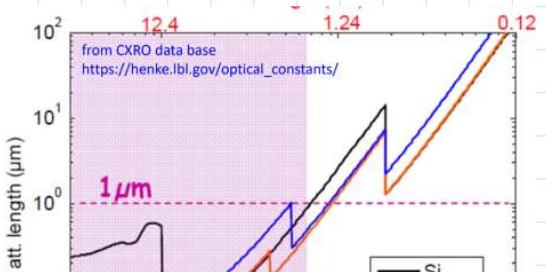
→ certisity: high purity silicon **detector grade** is produced using

NTD (neutron transmutation doping)



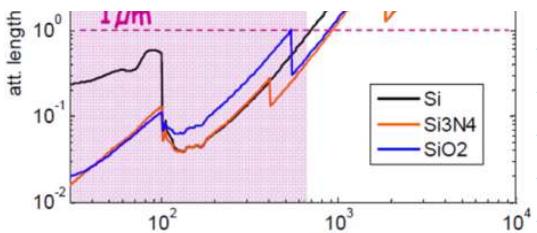
(not using implantation or deposition of dopants)

LOW DOPING NECESSITY



Attenuation Length - Energy
(contrary to visible light)

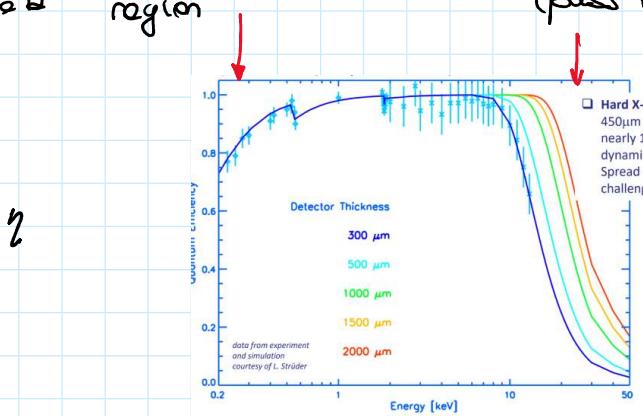
Attenuation length **increases** with energy in X RAYS



energy in X-RAYS
 \Rightarrow more energetic photons travel further in the device

- too small LABs
 photon is stopped too soon
 in dead layer or a low field region

SOFT X



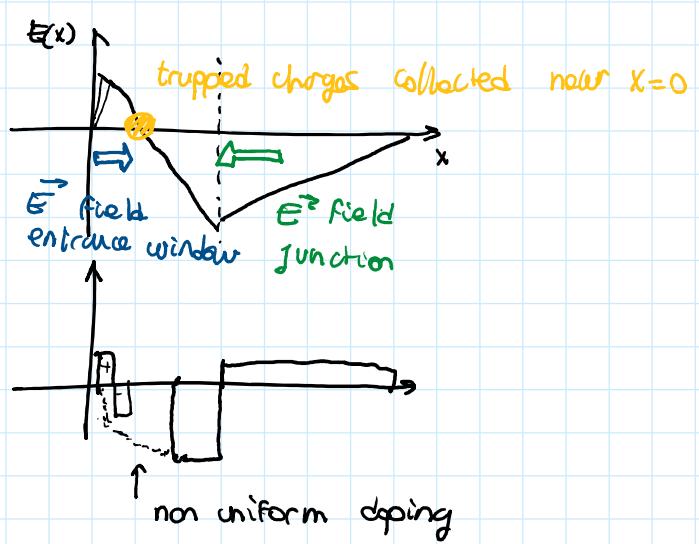
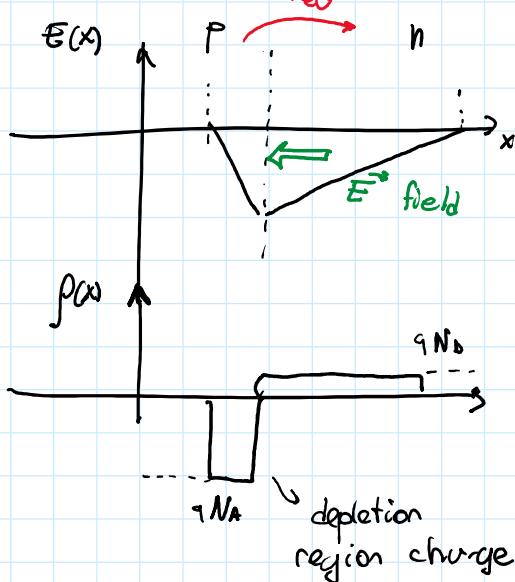
- too large LABs
 photon may not interact with the detector (pass through)

HARD X

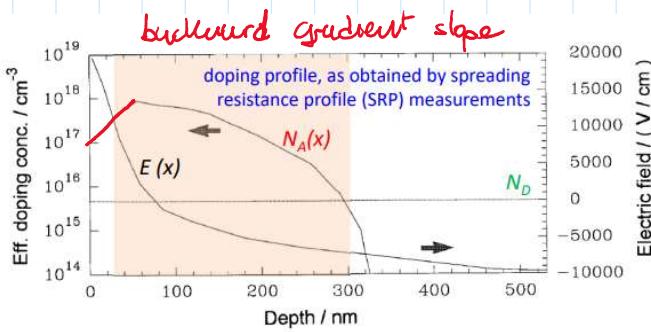
ABSORPTION LENGTH w ENERGY

- High energy inefficiency can be solved only through a deeper detector
 or using high τ detector (higher stopping power)
- Low energy inefficiency : ENTRANCE WINDOW

\hookrightarrow technology process limit (wafer thickness) (2mm)



the small backward E field is created due to
doping process - non idealities

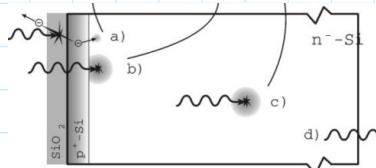


rev. Ficks law

$$J_{\text{diff}} = g D_n \frac{dp}{dx} \Big|_{n=0}$$

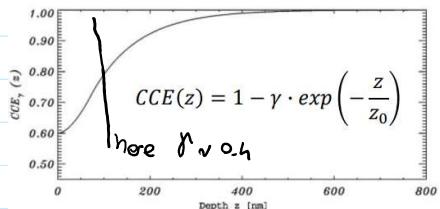
=> a sort of junction is created
=> contrast electric field

other imperfections are:



- oxide gradient on wafer

the incomplete charge collection can be modeled as



empirically

$$\gamma = 0.09 \quad : \text{maximum lost charge fraction}$$

$$z_0 = 100 \text{ nm}$$

$$CCE(0) = 1 - \gamma$$

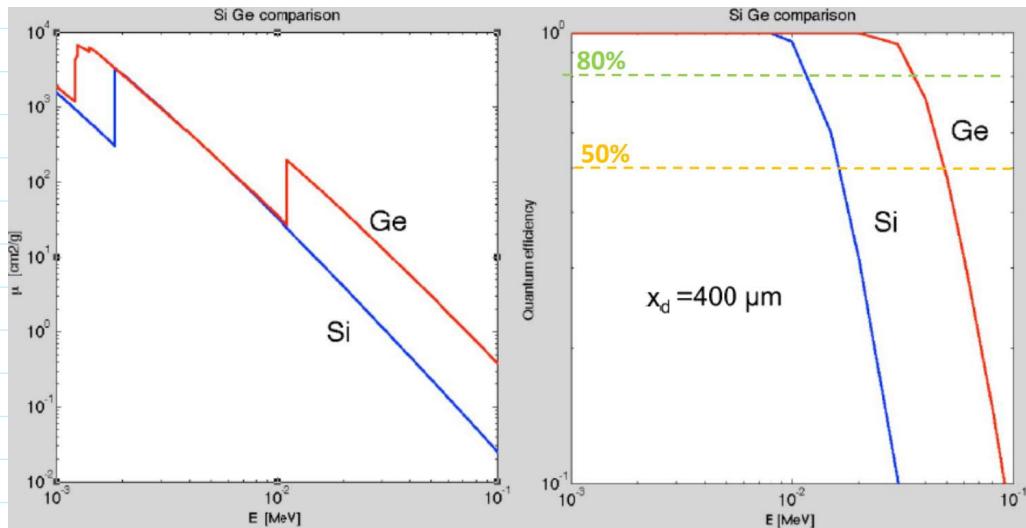
ENTRANCE WINDOW

High Z detector are useful for their higher absorption coefficient (μ)

=> they are able to capture more efficiently photons

=> same thickness (process limited) → higher detectable energy

Z: atomic number



- for example, Germanium suitable for γ -rays

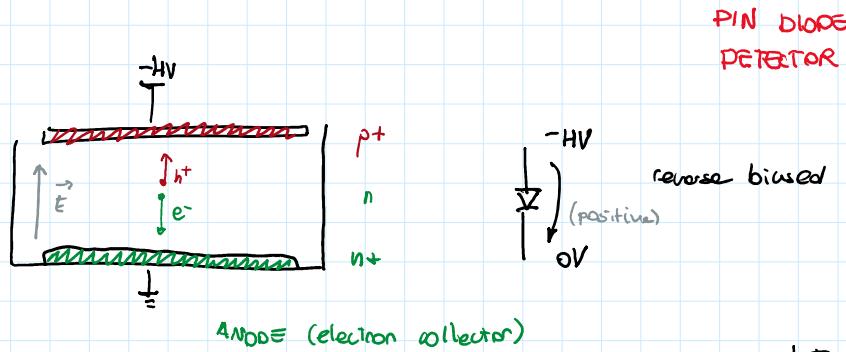
Reverse however for bandgap:

- lower bandgap means higher leakage current!
- => this detectors (lower bandgap) have a higher $1/\text{kg}$ current
- => higher noise

To mitigate, they must be cooled extensively (77K)

2D DETECTORS

Tuesday, May 14, 2024 11:32 AM



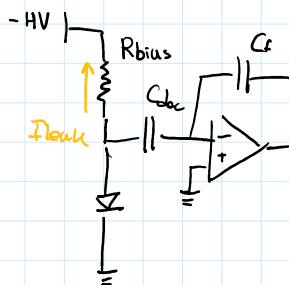
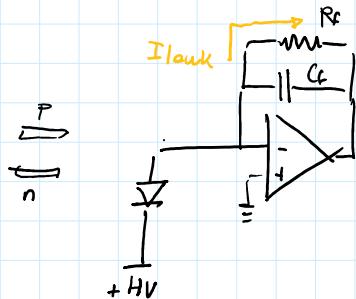
Capacitance

$$C_{dep} = \frac{2\pi\epsilon_0(N_A + N_D)}{qN_A N_D} (V_{bi} - V_{rev}) = \frac{\epsilon_0 \epsilon_r A}{d}$$

\uparrow depletion region width

PIN DETECTOR

DETECTOR BIASING



DC: leakage current

$$\Rightarrow V_{out} = I_{lk} \cdot R_f$$

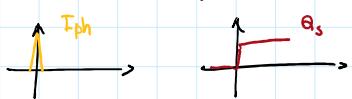
DC: leakage current

R_{bias} big \Rightarrow tuned to I_{lk}

$$I_{lk} = -J_0 A$$

\Rightarrow decoupled from V_{out}

AC PULSE: charge detection



$$\Rightarrow V_{out(t)} = I_{ph} \left(\frac{R_f}{1 + s R_f C_f} \right) \sim I_{ph} \frac{1}{s C_f} = \frac{Q_{ph}}{C_f}$$

AC PULSE

$$V_{out} = I_{ph} \cdot \frac{1}{s C_f} = \frac{Q_{ph}}{C_f}$$

$$V_{out} = HV - J_0 A R_{bias}$$

AC-DC BIASING

The detector (that can be divided in strips) is biased through contact

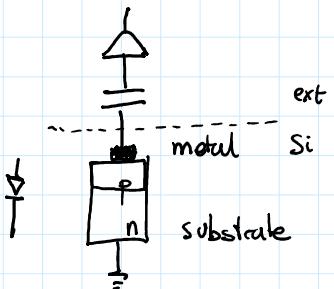
regarding AC biasing (with capacitor on diode)
the biasing can be made in 2 ways

DC * COUPLING

AC COUPLING

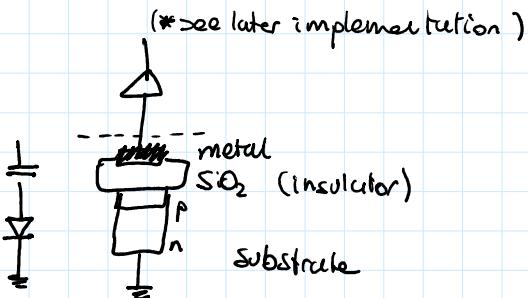
(*no later insulation - tristate)

DC COUPLING



- external capacitor
- external bias (as seen before)

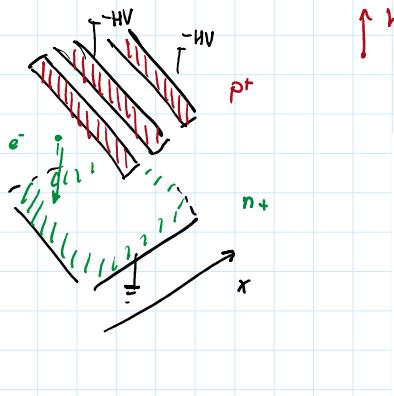
AC COUPLING



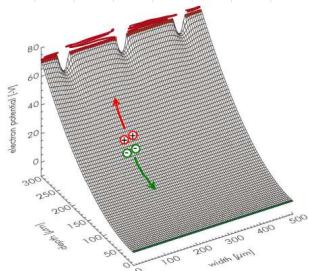
- capacitor integrated and interleaved with pn junction
- must be biased internally (diode cathode cannot be reached from ext to bias HV)

EXTERNAL-INTERNAL COUPLING

POSITION SENSING WITH STRIPS



$\uparrow h^+$ enters in one of the strips along x axis
=> 1D position detection



1D DETECTOR STRIP

~ fully depleted diode

PIN DIODE FOR 1D POSITION DEDET.

2D data can be extracted to get the position of the photon hit

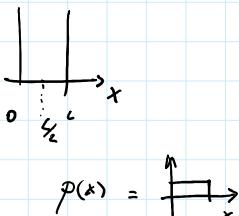
2) Digital readout

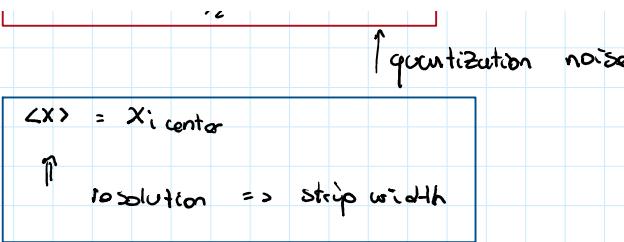
when $L \gg \sigma_x$ charge
(L strip \gg charge cloud)



$$\sigma_x^2 = \frac{1}{L} \int_{-L/2}^{+L/2} x^2 dx = \frac{L^2}{12}$$

↑ quantization noise





$$P(x) = \begin{cases} 1 & 0 \leq x \leq L \\ 0 & \text{else} \end{cases}$$

$$\sigma^2 = E(x^2) - (E(x))^2$$

$$\stackrel{?}{=} \frac{1}{L} \int_0^L x^2 dx$$

2) Interpolation of charge cloud
 $L \ll \sigma_x$ charge

$$\langle x \rangle = \frac{x_1 Q_1 + x_2 Q_2}{Q_{\text{tot}}} = \frac{x_1 Q_1 + (x_1 + L) Q_2}{Q_{\text{tot}}}$$

$$= x_1 + \frac{Q_2}{Q_1 + Q_2} L$$



"infinite" resolution

\Rightarrow depending on SNR we use to read $Q_1, Q_2 \dots$

RESOLUTION CAN BE BETTER THAN STRIP WIDTH

$$\sigma_x^2 = \frac{ENC^2}{Q^2} \cdot L^2 = \frac{L^2}{(SNR)^2}$$

- use bigger strips
- low SNR
- \Rightarrow better result!

for cloud size, use brownian (thermal) motion

$$\text{max drift time} = t_h = \frac{\sqrt{\epsilon}}{N_h q N_D} \quad \vec{v} = \mu \vec{E}$$

max drift time, in detector



$$T_{\text{drift}} = \frac{d}{\vec{v}} = \frac{d}{\mu \vec{E}} = \frac{d}{N_h q N_D d / \epsilon} = \frac{\epsilon}{N_h q N_D}$$

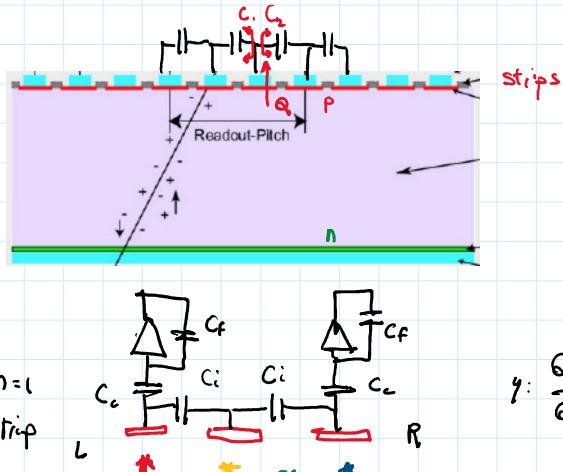
$$\begin{aligned} \epsilon &= \int \frac{p}{\epsilon} dx \quad \text{(assuming const. doping)} \\ &\approx g/\epsilon d = \frac{q N_D d}{\epsilon} \end{aligned}$$

$$\Delta x_{\text{diff}} = \sqrt{2 D_h T_h} \quad \rightarrow \text{brownian motion Gaussian FWHM}$$

https://en.wikipedia.org/wiki/Fick%27s_laws_of_diffusion#Example_solution_2_Brownian_particle_and_mean_squared_displacement

CHARGE READOUT METHODS

interpolation readout can be done using capacitances
 - capacitive coupling



charge shared equally between R L

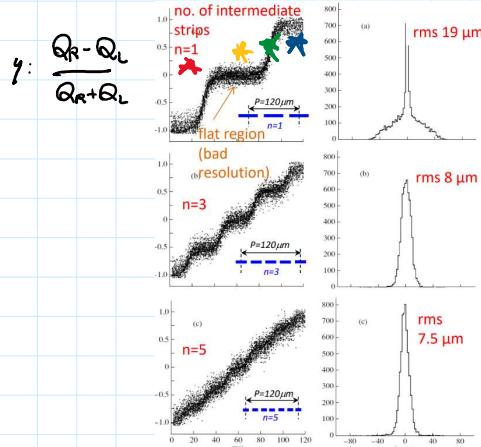
* ~ charge all on R
 * ~ charge all on L

(consider as a virtual ground

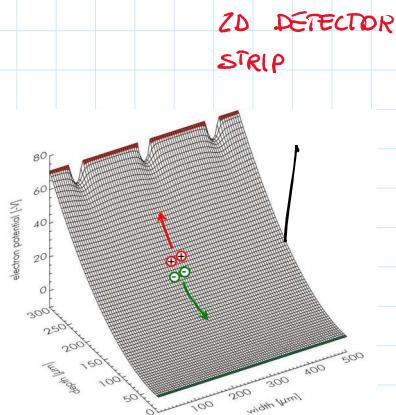
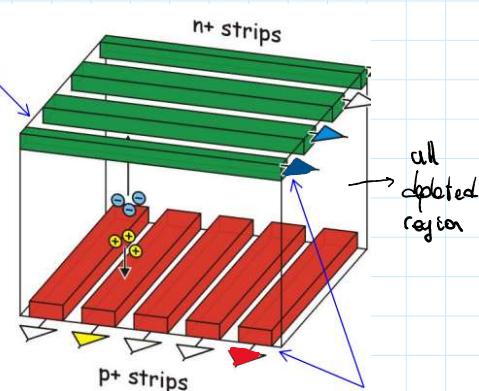
R, L connection post C_c

approximation, as C_c big ??)

* charge sharing happens between the strips
 (Interpolation regime



INTERPOLATION READOUT



We can exploit having 2 different carriers to have 2D position sensing, using strips on both sides

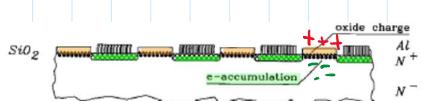
2D STRIP DET.

Introducing $n+$ strips causes a problem. As oxide is always deposited onto the surface (Required by process)

Introducing n^+ strips causes a problem. As oxide is always deposited onto the surface (Required by process)

SiO_2 contains \oplus trapped charges

\Rightarrow INSULATION ISSUE

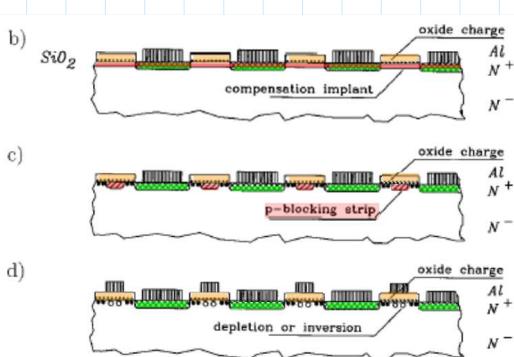


(+) trapped charges attract
(-) electron carriers

this creates two main problems

- carriers (electrons) remain trapped under strip-to-strip spaces
- the strips are no more insulated as a channel forms between strips (few $\sim \text{k}\Omega$)
↳ this may favor spread of carriers and lower position precision

Possible solutions are:



| pt spray: replace electron
(-) accumulation with
| N_A (\ominus fixed charge)

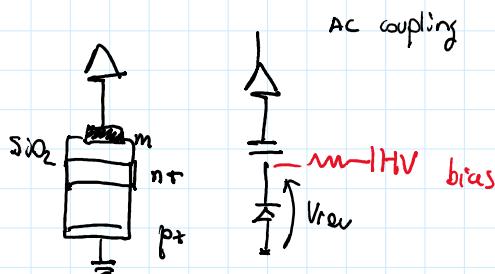
| same principle, but pt
is implanted and not on
| all surface

| A field effect (like in nmos)
can be used to invert
the layer, removing (-)
accumulation

(on pt strips we don't have this problem, as they are interfaced with n- substrate, so the inter-strip space is depleted - no free carrier to accumulate underneath)

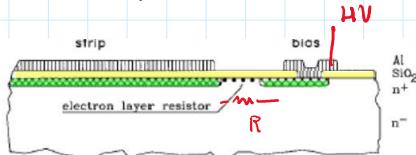
SiO_2 INSULATION ISSUE

* AC BIASED strips

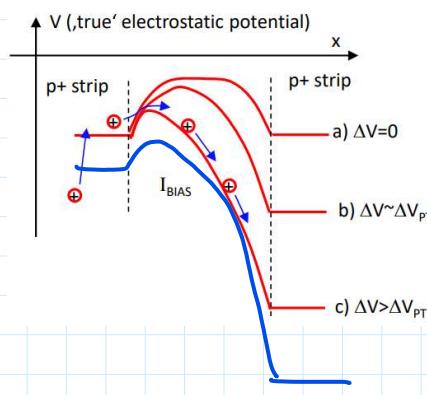
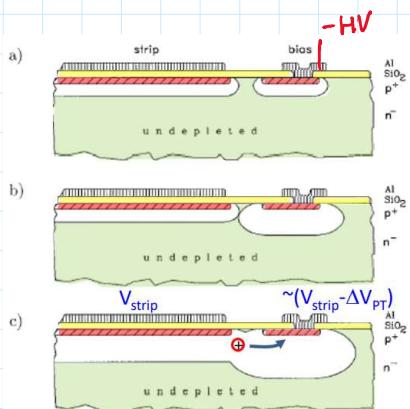


1) POLYSILICON RESISTOR: connected with n+ can brought out to a metal contact

2) ELECTRON ACCUMULATION LAYER RESISTANCE
by exploiting SiO_2 positive trapped charges.
creating an accumulation (resistive layer)



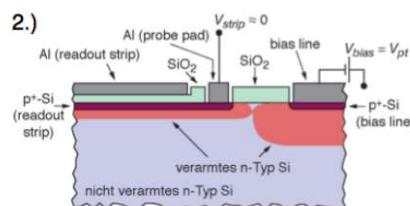
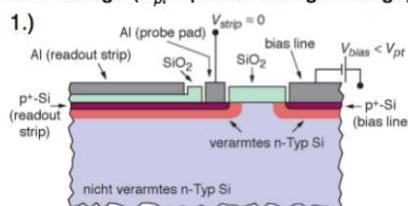
3) P+ BIASING BY PUNCH THROUGH (on p+ strips - other side)



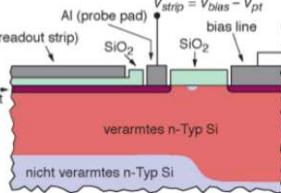
when punch through reached, the strips are like "shorted out" (zero diode like)

→ so pulling further the bias voltage drags down the strip voltage

Punch through effect: Figures show the increase of the depletion zone with increasing bias voltage (V_{pt} = punch through voltage).



The effect is due to a net flux of holes from strips to the bias line (called "guard-ring") through a potential barrier. The height of barrier decreases with V_{bias} .
The current is $I_{pt} = I_s e^{\alpha V_{pt}}$

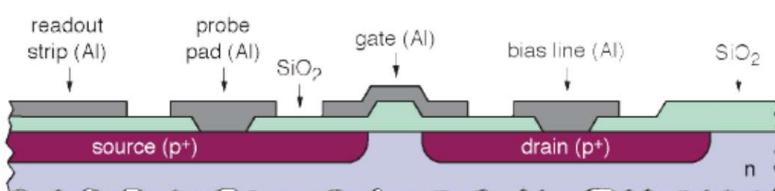


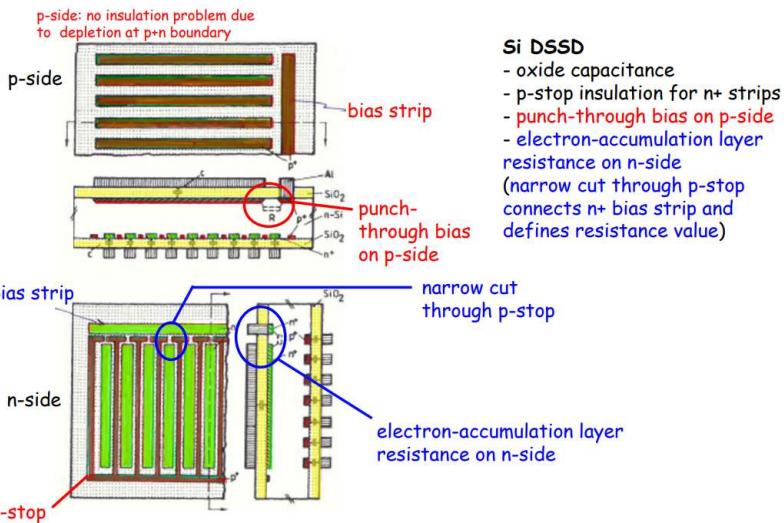
The result is that the strips have a positive potential wrt the bias line.
The punchthrough resistance depends on the barrier height and is fixed by $1/R_{pt} = dI_{pt}/dV_{pt}$
Typical values of R_{pt} are of $O(1-10 \text{ GOhm})$

Advantage: No additional production steps required.

4) FOXFET

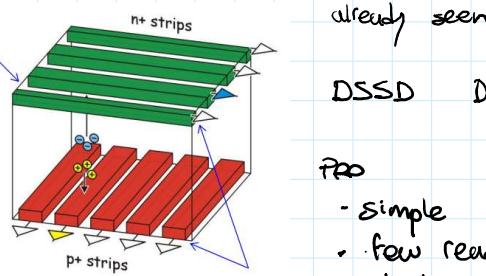
Create a resistive channel using FET gate. The resistance is variable and can be modulated





BIASING METHODS

STRIP DETECTOR TOPOLOGIES



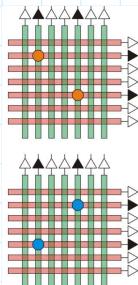
DSSD Double side Silicon Drift detector

PRO

- simple
- few readout electronics

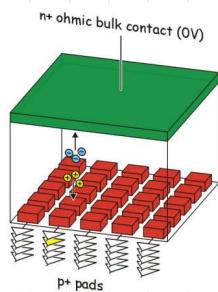
CON

- n+ strip insulation problem
- simultaneous event corruption
 - ↳ not suitable for high occupancy measurements



DSSD

P pixel on N substrate (p or n)

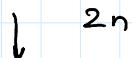


PRO

- solves n+ strip problems
- solves sim events

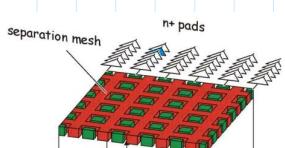
CON

- n^2 electronics
- readout instead of $2n$

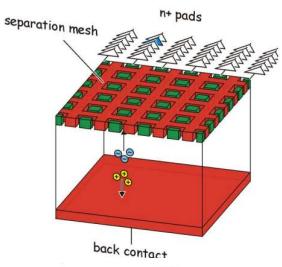


+ POWER CONSUMPTION,
+ interconnections

P-on-N

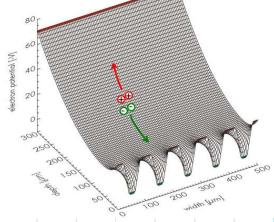


n pixels but with p+ mesh
to avoid accumulation problems
on n strips

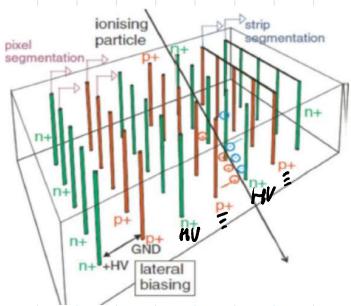


n pixels but with $p+$ mesh
to avoid accumulation problems
on n strips

same as P on N



N on N



3D detector

- PRO
- lower voltage need (closer strips)
- CON
- larger capacitance
- signal amplitude and charge collection can be chosen separately



BEFORE, ON PLANAR DETECTOR

more deep \Rightarrow more charges generated on
more deep \Rightarrow less repassing

\Rightarrow more charge collection

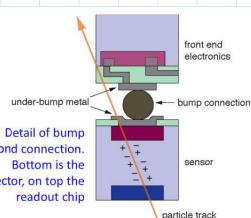
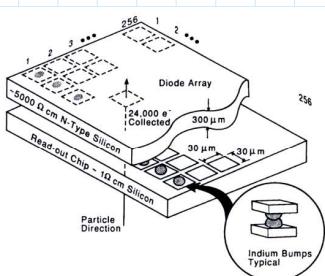
distance (more cloud Δx ,
collection time) (bad!)

TRADE OFF

3D detector

deeper \Rightarrow charges more generated &
repassing
charge collection
distance fixed by
distance between
 n and p trenches

3D DETECTORS



connection
method for
a strip detector
to a readout
electronics chip

pro:

- possibility to choose different purity grade silicon \$
- different process / technology too!

HYBRID PIXEL

ELECTRONICS

Sunday, June 30, 2024 11:40 AM

ENERGY RESOLUTION

Here we are interested in the ability to distinguish different photon energies, and count the intensity of photons at such energy.

that gives the ability to do **spectroscopy**, so to visualize the spectrum of the incoming light.



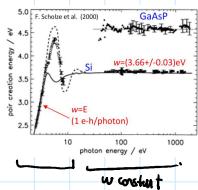
Spectrum example:

- 1) good energy resolution
- 2) bad energy resolution

ENERGY SPECTRUM

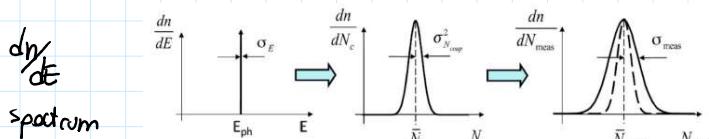
Energy of an **impinging photon** can be derived from the numbers of e-h pairs generated inside the detector.

=> see PART I : mean creation energy



$$\langle N_{e-h} \rangle = \frac{E}{\langle W(E) \rangle} = \frac{h\nu}{\langle W(E) \rangle}$$

$$\sigma_{N_{e-h}}^2 = F \cdot \langle N_{e-h} \rangle$$



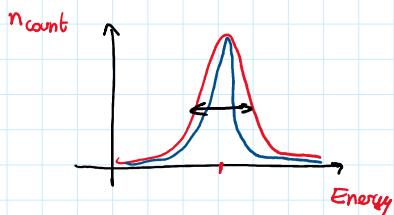
$\frac{dn}{dE}$
Spectrum
 n photons n photons on
energy energy
 N_{e-h} generated
(at energy E) N_{e-h} read by electronics
(at energy E)

$$\sigma_E^2 = 0$$

$$+ \sigma_{\text{noise Fano}}^2$$

$$+ \sigma_{\text{noise electronics}}^2$$

ENERGY RESOLUTION

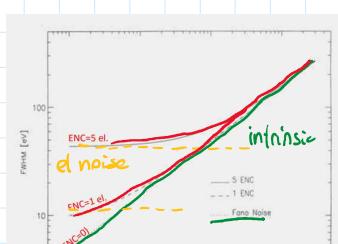


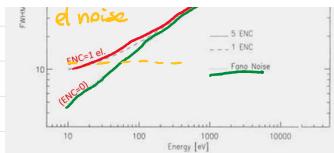
$$FWHM = 2.335 \cdot \frac{W}{E} \sigma_{N_{e-h}}$$

↑ ↑
mean created
Energy carriers
generated

$$= 2.335 \cdot W \sqrt{\frac{F \cdot E}{W} + \left(\frac{ENC}{q}\right)^2}$$

↑ ↑
Fano electronics
(intrinsic)
contribution contributions





(Intrinsic) contributions
contribution

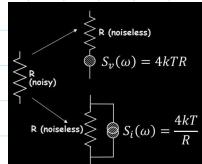
$$\sigma_n^2 = \frac{F(E)}{\omega} + \left(\frac{ENC}{q}\right)^2$$

$\#_{\text{ANO}}$ - ELECTRONICS NOISE contrib.

Noise in electronics components.

Note that the photon power spectral densities are UNILATERAL

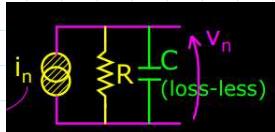
RESISTOR



$$S_v(f) = 4kT R$$

$$S_{z_0}(f) = \frac{4kT}{R}$$

CAPACITOR (LOSSY)



shunt resistance across
capacitance - due to dielectric
relaxation

admittance

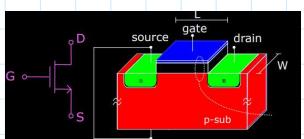
$$Y = j\omega(\epsilon_r' j\epsilon_r'') \frac{\epsilon_r A}{d} = j\omega C + \frac{\omega C \tan(\delta)}{\text{real}}$$

$$S_i(\omega) = \frac{4kT}{R} = 4kT \omega C \tan(\delta)$$

$$S_v(\omega) = 4kT \frac{\tan(\delta)}{\omega C \tan(\delta)} ?$$

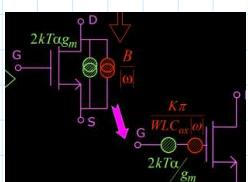
\Rightarrow lossy capacitors introduce $1/f$ noise

MOSFET



They introduce white and
 $1/f$ noise.

$1/f$ contribution is often
significant and must be addressed

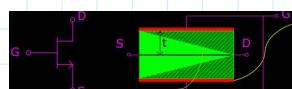


$$S_{w_0}(f) = 4kT \alpha g_m (\text{A}^2/\text{Hz})$$

sat-region $\alpha \approx \frac{2}{3} \approx 2$

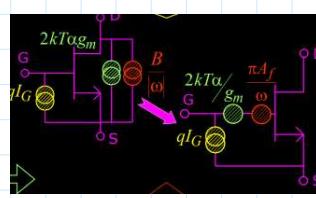
$$S_{w_0}(f) = 4kT R_{\text{chan}} (\text{A}^2/\text{Hz})$$

JFET



Junction FET exploit a
pn junction depletion region
to close the channel
between source and drain.
this avoid using a gate capacitor,
introducing oxide and trapped charges
giving rise to $1/f$ noise

However pn junctions add shot
noise, due to leakage current
between gate and s/d



$$S_{w_0} = 4kT \alpha g_m (\text{A}^2/\text{Hz})$$

$$S_{W_0}(f) = 4kT R_{cha} \left(\frac{A^2}{Hz}\right)$$

ohmic region

$$S_t = \frac{S_w \cdot w_c}{\omega} \left(\frac{A^2}{Hz}\right)$$

↓ (along $\frac{S_t}{g_m^2}$ - physics on Gate)

$$S_v = \frac{k}{C_o \omega L} \cdot \frac{1}{\omega} \left(V^2/Hz\right)$$

$$S_{W_0} = 4kT g_{av} \left(\frac{A^2}{Hz}\right)$$

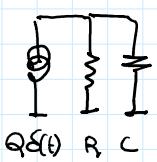
$$S_{\frac{1}{f}} = \frac{S_w \cdot w_c}{\omega} \left(\frac{A^2}{Hz}\right)$$

shot

$$S_s = 2gI \left(\frac{A^2}{Hz}\right)$$

between between gate / drain

NOISE IN DEVICES



$$S_v = S_i \cdot \left(\frac{R}{1+sRC} \right)^2 = \frac{4kT \cdot R^2}{R} \cdot \frac{1}{(1+\omega^2 RC^2)}$$

$$= \frac{4kTR}{(1+\omega^2 RC^2)}$$

(unitless)

$$\sigma_v^2 = \int_{-\infty}^{+\infty} S_{out}(f) dt = \text{noise BW} = 4kTR \cdot \frac{\pi}{2} \cdot \frac{L}{2\pi RC} = \frac{KT}{C}$$

$\frac{1}{4RC}$

noise charge on capacitor

$$C = \frac{Q}{\delta V}$$

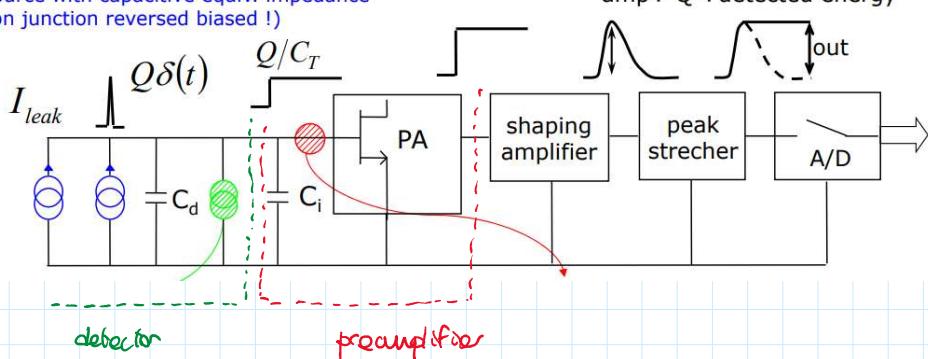
$$\sigma_c^2 = (C^2) \cdot \sigma_v^2 = KTC \quad \text{independent on } R$$

because proportionally increase noise and inverse proportionally decrease BW, does not impact on noise MSV

KTC RC FILTER NOISE

NOISE PERFORMANCE MEASUREMENT SYSTEM TOPOLOGY

The sensor is modeled as a current source with capacitive equiv. impedance (pn junction reversed biased !)



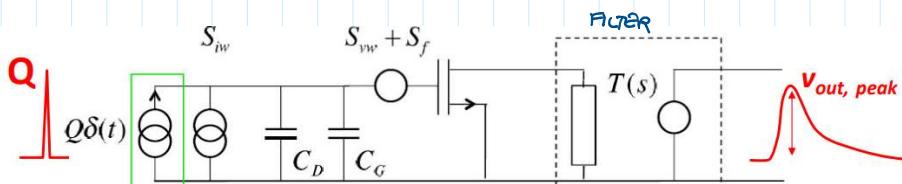
Signal:
can be modeled
as a pulse like
current delivering

typically a
common source
FET

- as a pulse like current delivering a fixed amount charge
- I_{leak} : junction leakage
 - C_D junction depletion region capacitance
 - noise: shot noise from leakage current
- common source FET
- C_G gate capac.
 - \rightarrow series Voltage f noise
- $$\frac{2kT\alpha}{g_m} + \frac{1}{2} \frac{A_f}{f} [V_f^2]$$
- (bilateral)

ELECTRONICS OVERVIEW

pay attention, in following calculations we use bilateral spectral densities

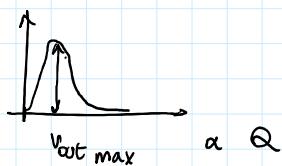


$$F(s) = Q \quad (\text{delta } \Rightarrow \text{constant in } F)$$

(step \Rightarrow integral of δ
 \Rightarrow $1/s$ -constant)

$$I_{\text{drain}} = Q \cdot \frac{1}{s(C_D + C_G)} \cdot g_m$$

$$V_{\text{out}}(s) = Q \cdot \frac{1}{s(C_D + C_G)} \cdot g_m \cdot T(s) = Q \cdot H(s)$$

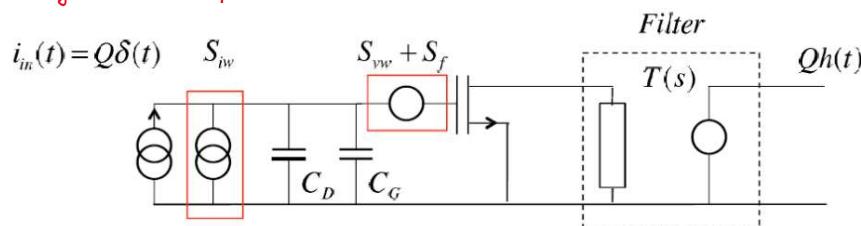


$$V_{\text{out}}(t) = \frac{Q}{(C_D + C_G)} g_m \cdot \mathcal{L}^{-1}\left(\frac{1}{s} T(s)\right)$$

↑ filter

SIGNAL OUTPUT

using bilateral spectral densities: unilateral / 2



$$S_{in} : \text{detector shot noise} = q I_{\text{leak}}$$

b

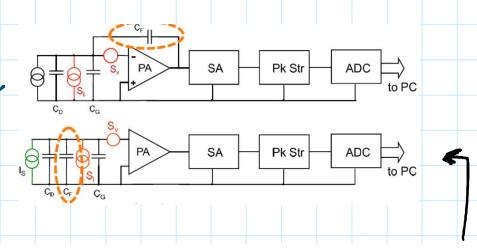
$$S_{vw} : \text{input referred FET white noise} = \frac{2kT\alpha}{g_m}$$

a

$$S_f : \text{input referred FET } 1/f \text{ noise} = \frac{1}{2} \frac{A_f}{f} = \frac{\pi A_f}{\omega}$$

c

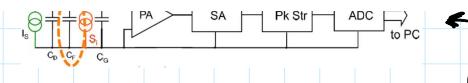
noise power spectral density



i noise power spectral density

$$C_D = C_{\text{stray}} + C_{\text{detector}} + C_{\text{feedback}}$$

charge amplifier
feedback at ANY
why



For the S/N calculation
(NOT SIGNAL OR NOISE ONLY S/N)
 C_F can be considered parallel to
detector

We can then compute the power spectral density at the output

$$S_{\text{out}}(\omega) = \left(\frac{2kT_\alpha}{g_m} + \frac{\pi A_F}{\omega} + qI_K \cdot \frac{1}{\omega^2(C_0+C_G)^2} \right) g_m^2 |H(j\omega)|^2$$

To get the noise power (variance of the noise)

$$\sigma_n^2 = \int_{-\infty}^{+\infty} S_{\text{out}} dF = \frac{1}{2\pi} \int_{-\infty}^{+\infty} S_{\text{out}} d\omega \quad d\omega = d(2\pi f)$$

To quickly evaluate signal impact, it is common to
input refer the noise sources / power spectral densities

\Rightarrow input referred current (equal to signal)

$$S_{\text{out}} = \left| \left(\frac{2kT_\alpha}{g_m} + \frac{\pi A_F}{\omega} \right) \cdot \omega^2(C_0+C_G)^2 + qI_K \right|^2 |H(s)|^2$$

input referred power spectral densities

NOISE OUTPUT

Signal to noise ratio is computed

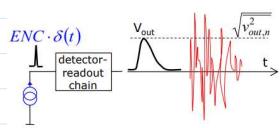
$$\left(\frac{S}{N} \right)^2 = \frac{Q^2 \frac{g_m^2}{(C_0+C_G)^2} \cdot \text{tr} \chi \left(\mathcal{L}^\dagger T(s) / s \right)}{\frac{1}{2\pi} \int_{-\infty}^{+\infty} S_{\text{out}}(\omega) d\omega} \sim 1 \quad V_{\text{out}}^2 (\text{peak})$$

$$\left(\frac{S}{N} \right)^2 = \frac{Q^2 \frac{g_m^2}{(C_0+C_G)^2}}{\frac{1}{2\pi} \int_{-\infty}^{+\infty} S_{\text{out}}(\omega) d\omega}$$

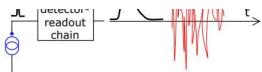
We can define the EQUIVALENT NOISE CHARGE
figure of merit

If we impose a $S/N = 1$ we can find the minimum
detectable signal at the input finding the needed signal
charge to equal the noise amplitude

$$1 = \frac{(ENC)^2 \cdot \frac{g_m^2}{(C_0+C_G)^2}}{\frac{1}{2\pi} \int S_{\text{out}}(\omega) d\omega}$$



$$\frac{1}{2\pi} \int S_{\text{noise}}(\omega) d\omega$$



$$ENCL^2 = (C_0 + C_G)^2 \frac{2KT\alpha}{g\omega} \cdot \frac{1}{2\pi} \int_0^{+\infty} |T(j\omega)|^2 d\omega + (C_0 + C_G)^2 \cdot \pi A_F \frac{1}{2\pi} \int \frac{1}{\omega} |T(j\omega)|^2 d\omega$$

series white (voltage) noise series $\frac{1}{f}$ voltage noise

$$+ qI \cdot \frac{1}{2\pi} \int_{-\infty}^{+\infty} \frac{1}{\omega^2} |T(j\omega)|^2 d\omega$$

parallel white (current) noise

SNR / ENC CALCULATION

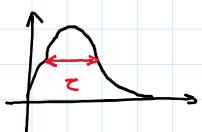
ENC contains different terms, each differently dependent on $T(j\omega)$ (shaping circuit)

The output pulse will be a combination of all

HPF action (to cut LF noise, such $\frac{1}{f}$)

LPF action (to cut WIDEBAND noise, like white noise)

The output pulse will be something like



if we want to enhance the time (z) dependence, we can rewrite

$$\omega = \frac{x}{z} \quad x = \omega z \quad \frac{dx}{z} = dw$$

$$ENCL^2 = (C_0 + C_G)^2 \frac{2KT\alpha}{g\omega} \cdot \frac{1}{2\pi} \int_0^{+\infty} |T(jx)|^2 dx + (C_0 + C_G)^2 \cdot \pi A_F \frac{1}{2\pi} \int \frac{x}{z} |T(jx)|^2 \frac{dx}{z}$$

series white (voltage) noise series $\frac{1}{f}$ voltage noise

$$+ qI \cdot \frac{1}{2\pi} \int_{-\infty}^{+\infty} \frac{z^2}{x^2} |T(jx)|^2 \frac{dx}{z}$$

parallel white (current) noise

$$ENCL^2 = (C_0 + C_G)^2 a \frac{1}{z} A_1 + (C_0 + C_G)^2 c A_2 + b z A_3$$

series white noise series $\frac{1}{f}$ noise parallel white noise

Note the dependence on z = shaping time

$\uparrow z$ \downarrow series (voltage) white

$x \geq$ $\frac{1}{f}$ noise

| , | , .. , .. , .. |



while the A_1, A_2, A_3 term are dependent only on the shaper output "shape", not on the absolute time of the shape

SHAPER IMPACT

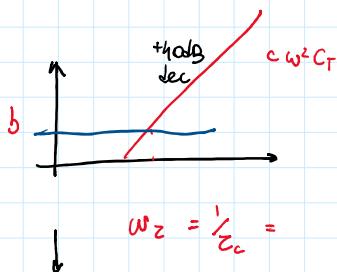
The filter has so an impact on SNR, as its filtering action must enhance SNR

Let's consider only white noise at first

$$\text{SNR} = \frac{\frac{Q^2}{a}}{\frac{2kT\alpha}{\text{Gain}} C_T^2 \cdot \int_{-\infty}^{+\infty} \omega^2 |h(t)|^2 dt + qF \cdot \int_{-\infty}^{+\infty} |h(t)|^2 dt}$$

a $\frac{Q^2}{a}$
 constant terms
 out integral
 --- series white ----- parallel white

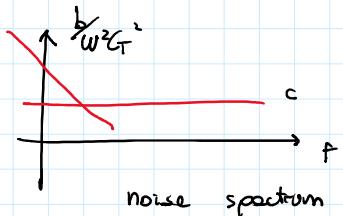
noise power spectral density



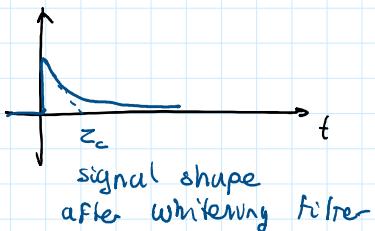
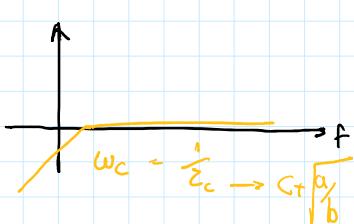
$$W_Z = \frac{1}{Z_C} = Z_C = C_T \sqrt{\frac{a}{b}}$$

noise corner time constant

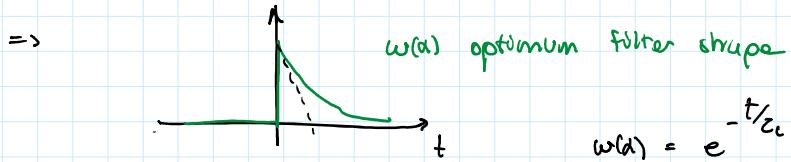
after capacitance $\cdot \frac{1}{\omega^2 C_T^2}$



signal time shape
impulse to step (integration
on capacitance)



and build filter on signal shape



$$w(t) = e^{-t/z_c}$$

z_c cutoff point

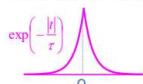
$$= C_T \sqrt{\frac{a}{b}}$$

OPTIMUM SHAPING ON WHITE NOISE

If $1/f$ noise is present and not negligible, unfortunately we can't whiten it and can't build an optimum filter

We can size and shape the filter optimizing the output

- **Indefinite cusp** (optimum shape for white noises)



$$\exp\left(-\frac{|t|}{\tau}\right)$$

A1=1
A2=2/ π
A3=1
F=1 worsening factor

- **RC-CR**



A1=1.85
A2=1.18
A3=1.85
F=1.359

- **Triangular** (optimum shape for white voltage noise and finite measurement time)

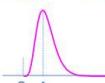


A1=2
A2=(4ln2)/ π
A3=2/3
F=1.075



A1=2
A2=1.38
A3=5/3

- **Pseudo-Gaussian (4th order)**



A1=0.51
A2=1.04
A3=3.58
F=1.165

- The "flat-top" regions contributes only to A2 and A3.
- A1 is equal to the triangular case having the same leading and trailing edges

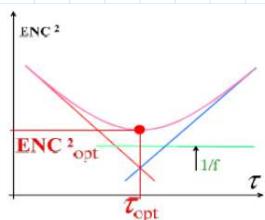
OTHER FILTERS SHAPES

The shaping time can be optimized following the noise contributions. (when we can't follow the optimum filter)

$$ENC^2 = (C_D + C_G)^2 a \frac{1}{2} A_1 + (C_D + C_G)^2 c A_2 + b c A_3$$

series white

series $1/f$ parallel white



$$\tau_{opt} \Rightarrow ENC^2_{series\ white} = ENC^2_{parallel\ white}$$

$$\tau_{opt} = \sqrt{\frac{A_1}{A_3}} \sqrt{\frac{a}{b}} (C_D + C_G)$$

$$ENC^2_{opt} = c(C_D + C_G)^2 A_2 + 2 \cdot \sqrt{A_1 A_3} \sqrt{ab} (C_D + C_G)^2$$

OPTIMAL SHAPING TIME w $1/f$ NOISE

Input transistor parameters (preamplifier) influence the SNR via

$$ENC^2 = (C_D + C_G)^2 a \frac{1}{2} A_1 + (C_D + C_G)^2 c A_2 + b c A_3$$

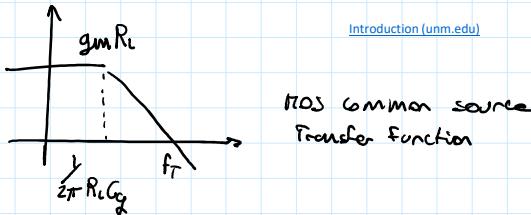
$$ENC^2 = (C_0 + C_D)^2 \left[\frac{1}{Z} A_1 + (C_0 + C_D)^2 C A_2 \right] b \geq A_3$$

$$\alpha = S_{Vb\text{whole}} = \frac{2KT\alpha}{g_m}$$

$$C = \pi A_f \Leftrightarrow S_{V_{1/f\text{bb}}} = \frac{\pi A_f}{\omega}$$

noise contributions

$$C_G = C_{ox} \frac{WL}{t_{ox}}$$



$$f_T = g_m R_L \cdot \frac{1}{2\pi R_L C_g}$$

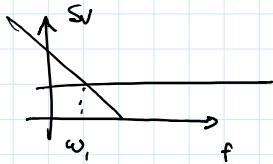
$$\omega_T = g_m / C_g$$

gate capacitance

$$g_m = \omega_T C_g$$

↑ cutoff frequency

The transistor noise is described as



to find noise corner frequency

$$\frac{\pi A_f}{\omega_c} = \frac{2KT\alpha}{g_m} \quad \pi A_f = \frac{2KT\alpha}{\omega_c C_g} \omega_c$$

↑ $\frac{1}{f}$ noise ↑ white

$$ENC^2 = (C_G + C_D)^2 \cdot \frac{2KT\alpha}{\omega_c C_g} \cdot \frac{1}{Z} A_1 + (C_G + C_D)^2 \cdot \pi A_f A_2$$

series white

series $\frac{1}{f}$

$$+ q I_{lau} Z A_3$$

parallel white

$$ENC^2 = (C_G + C_D)^2 \cdot \frac{2KT\alpha}{\omega_c C_g} \cdot \frac{1}{Z} A_1 + (C_G + C_D)^2 \frac{2KT\alpha}{\omega_c C_g} w_c + q I_{lau} Z A_3$$

$$= \frac{(C_G + C_D)^2}{C_g} \frac{2KT\alpha}{\omega_c} \left(\frac{1}{Z} A_1 + w_c A_2 \right) + q I_{lau} Z A_3$$

assuming ω_T constant
~ bandwidth

requires fixed
current density

to minimize the ENC of the transistor, we must minimize

$$\frac{(C_G + C_D)^2}{C_g} = C_D \left(\sqrt{\frac{C_D}{C_g}} + \sqrt{\frac{C_g}{C_D}} \right) \rightarrow \begin{matrix} \text{minimize when} \\ \text{matching} \end{matrix}$$

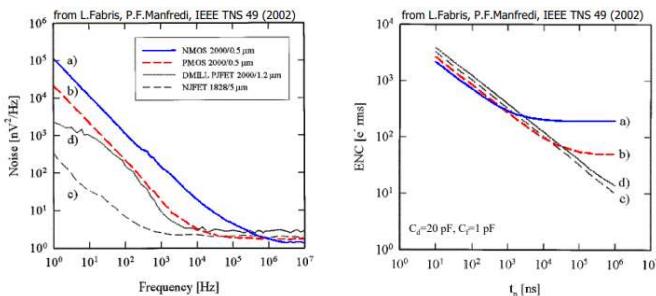
$$\frac{C_{\text{in}} + C_{\text{d}}}{C_0} = C_0 \left(\sqrt{\frac{C_0}{C_{\text{in}}}} + \sqrt{\frac{C_{\text{d}}}{C_0}} \right) \rightarrow \text{minimize } \text{crosstalk}$$

$$C_0 \approx C_{\text{d}}$$

matching gate capacitance with detector capacitance

Keep in mind that also gate leakage current must be considered if the transistor type is JFET or maybe BJT

$$\text{shot contribution} = q \left(I_{\text{drain}} + I_{\text{gate}} + \dots \right) z$$



PFET:

- higher g_m
- smaller white noise

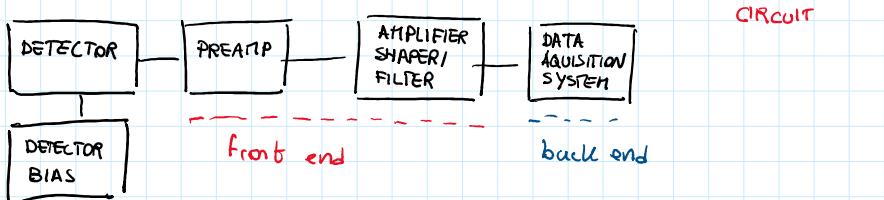
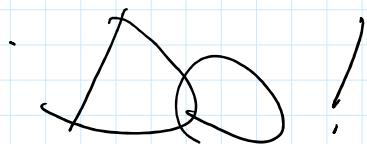
PFET

- lower f_T noise than NMOS
- if shaping times $\approx 2 \mu\text{s}$

PFET > NMOS, JFET

PREAMPLIFIER TRANSISTOR NOISE CONTRIBUTION

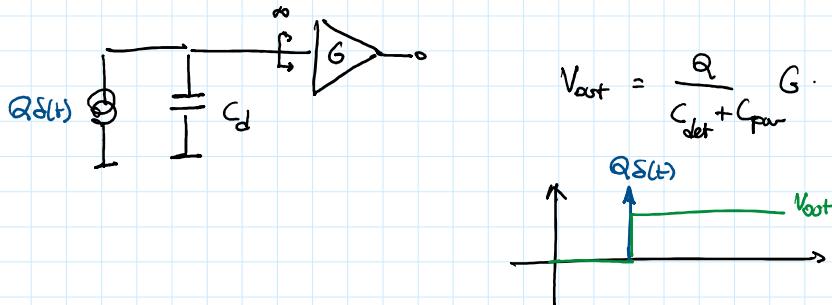
capacitor matching for save power dissipation?



- why the preamplifier
 - minimize the noise of the following stages via amplification
 - minimize noise contribution of transmission by placing it as close as possible to the detector

- minimize noise contribution of transmission by placing it as close as possible to the detector
 → transmission signal driving

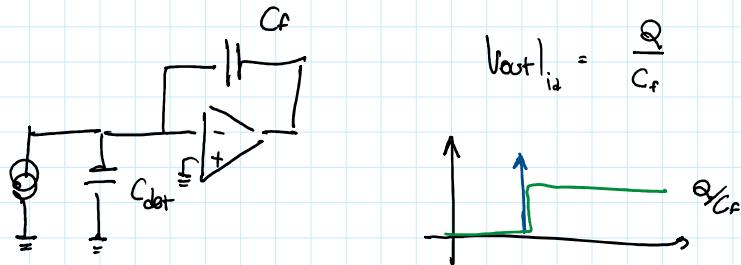
PREAMPLIFIER



The major drawback of the voltage preamplifier is the direct dependence on

- C detector: not stable and varying with bias
- C parasitic: not stable and hardly predictable

VOLTAGE PREAMPLIFIER



The charge amplifier (invented by Enrico Gatti!) solves the issue of dependence on unstable capacitance

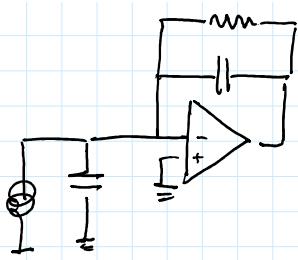
The feedback capacitance can be freely designed and produced

CHARGE AMPLIFIER (IDEAL)

$$\begin{aligned} V_{out}|_{real} &= V_{out|_{id}} \cdot \frac{1}{1 - \frac{1}{G_{loop}}} = \frac{Q}{C_f} \cdot \frac{1}{1 + \frac{1}{A \cdot \left(\frac{1/C_{in}}{1/C_{in} + 1/C_f} \right)}} \\ &= \frac{Q}{C_f} \cdot \frac{1}{1 + \frac{1}{A} \cdot \frac{C_f + C_{in}}{C_f}} = \frac{Q}{C_f} \cdot \frac{1}{1 + \frac{1}{A} \left(1 + \frac{C_{det} + C_{par}}{C_f} \right)} \end{aligned}$$

$$\text{if } A \gg 1 \Rightarrow V_{out}|_{real} \approx V_{out}|_{id} = \frac{Q}{C_f}$$

FINITE GAIN FEEDBACK

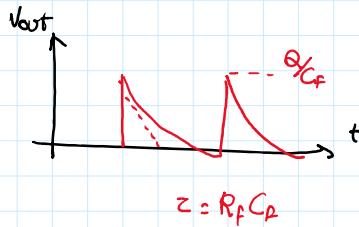


capacitor (integrator) must be reset between pulses,

otherwise multiple pulses overlap

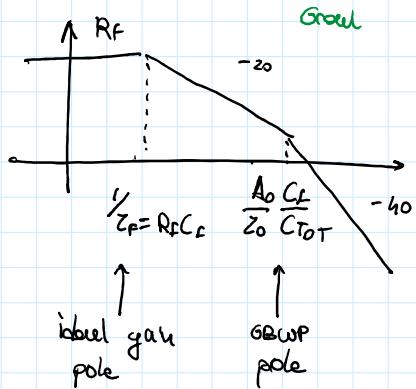
we do it with resistance
or (open mosfet in short channel technologies)

→ **continuous reset**



typically $z \approx 40 \mu s$

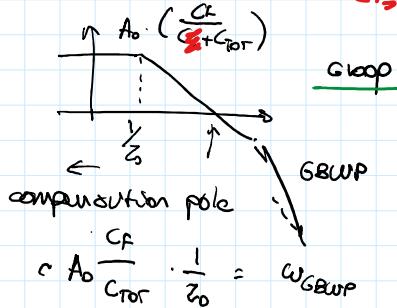
We also must consider that the amplifier often has at least one pole



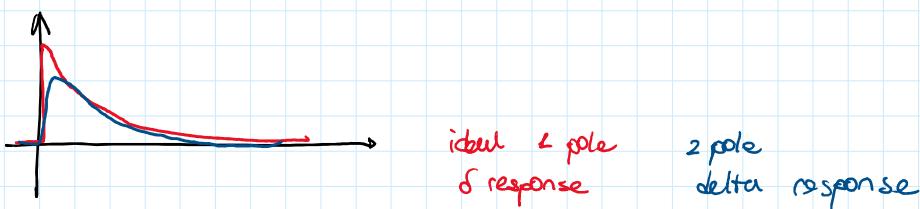
$$A(s) = A_0 \cdot \frac{1}{2 + sZ_0}$$

$$Z_0 = r_0 C_0$$

$$C_{TOT} = C_0 + C_f$$



REAL TRANSFER FUNCTION



(finite rise time $\approx 100 \text{ ns} \Leftrightarrow \text{GQWP}$)

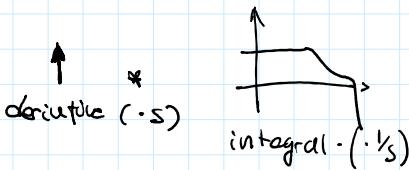
(Also think of this time domain response as

STEP response of a LPF + HPF

δ is derivative of step so

STEP is integral of δ

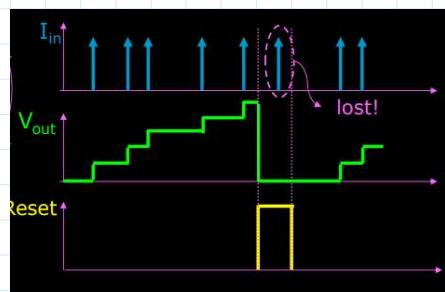
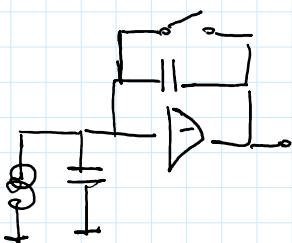
⇒ same time response



REAL PREAMP TIME RESPONSE

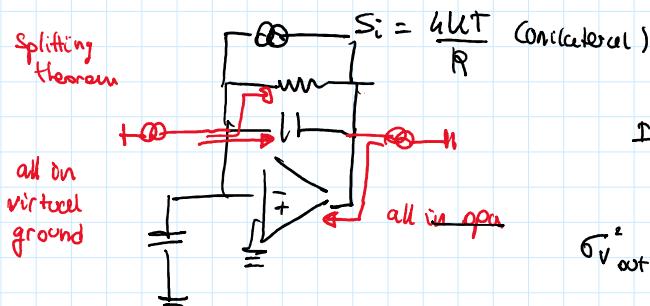
The reset can also be pulsed / non continuous

→ The capacitance can be discharged with a mos



GATED RESET

The introduction of the reset resistor also adds a reset noise contribution



If virtual ground ideal

$$\sigma_{V_{out}}^2 = \int_0^{+\infty} S_i(f) \cdot \left| \frac{R_f}{1 + s R_f C_f} \right|^2 df$$

$$= \frac{4kT}{R_f} \cdot R_f^2 \cdot \frac{\pi}{2} \cdot \frac{1}{2\pi R_f C_f}$$

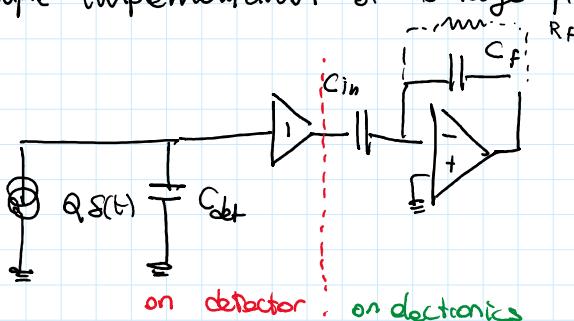
Noise Eq
BW

$$= \frac{kT}{C_f}$$

$$\sigma_{ch}^2: ENC^2 = \frac{kT}{C_f} \cdot C_f^2 = kT C_f$$

KTC ON RESET RESISTOR

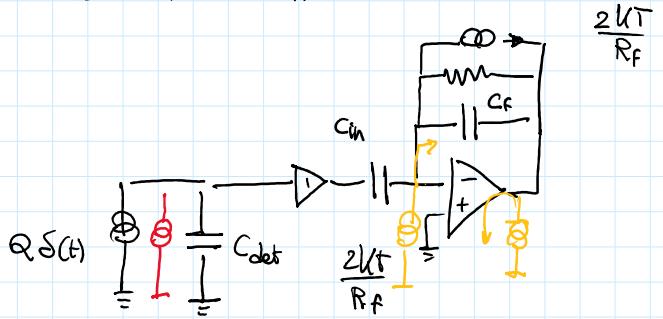
Example implementation of 10 stage preamplifier



$$V_{out} = \frac{Q}{C_{det}} \cdot \frac{C_{in}}{C_f} \quad \text{assuming ideal}$$

$\hookrightarrow \left(\frac{Z_2}{Z_1} \text{ inverting opa} \right)$

noise contribution



input referring the noise

$$I_o = I_{in} \cdot \frac{1}{SC_{det}} \cdot \frac{1}{SC_{in}} = I_{in} \cdot \frac{C_{in}}{C_{det}}$$

noise

$$\star S_{in} = \frac{2kT}{R_f} \cdot \left(\frac{C_{det}}{C_{in}} \right)^2$$

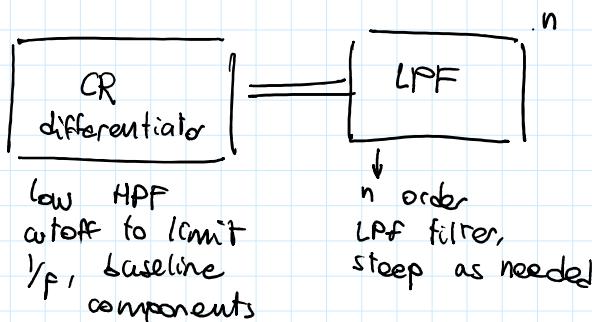
VOLTAGE PREAMP EXAMPLE

SHAPING AMPLIFIER

from the preamp, the signal comes out as a step: as the delta-pulse signal is integrated
(on the detector capacitance or by a charge amplifier)

The shaper transform this step-like function in a reusable pulse / signal (reducing filters to improve SNR)

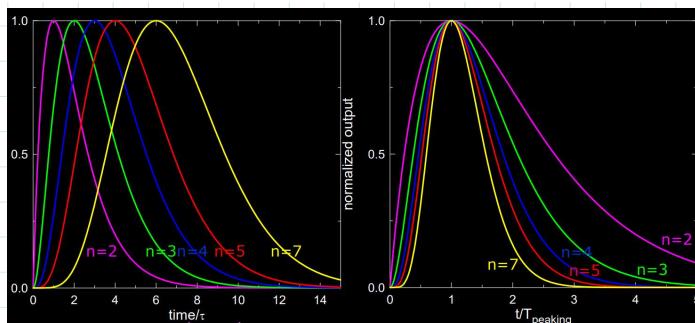
Typically



$$T(s) = \frac{s^2}{(1+s^2)^n} \rightarrow \text{real coincident poles}$$

other LPF transfer functions can be obtained using non real poles

(chebyshev - resonant filter - bessel etc)



A high pass alternative that is a
NON CONSTANT PARAMETER SIGNAL

is the baseline restorer

\Rightarrow cuts low frequency noise:

- $1/f$
- baseline offset

needs
a sync
signal
when
signal
increasing

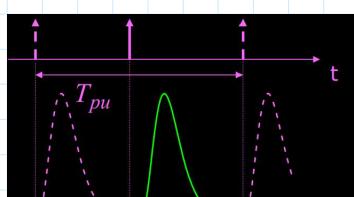


SW closed when NO SIGNAL
 \hookrightarrow LPF on baseline

SW open when SIGNAL
 \hookrightarrow no action on signal

SHAPING AMP TYPES/FILTERS

The shaping amplifier also has a great impact on multiple pulse detection

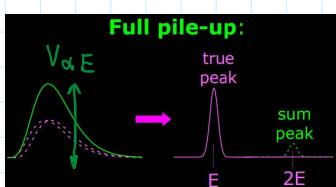


$$\text{Pnnp} = e^{-h_{in} T_{pu}}$$

$$p_{pileup} = 1 - p_{nnp}$$

The pileups can be of two types:

FULL PILEUP



Two peaks almost perfectly align giving rise to

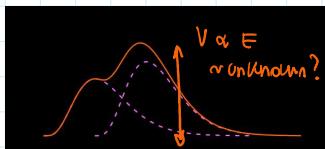
$$\text{Detection of energy } E_{pu} = E_1 + E_2 = (2E) \quad (\text{in this case})$$

They can be recognized for 2 reason:

- A known frequency (a combination of sum of true frequencies)
- The intensity increases with a higher counting rate

- A known frequency (a combination of sum of true frequencies)
- The intensity increases with a higher counting rate (as pileups become more probable)

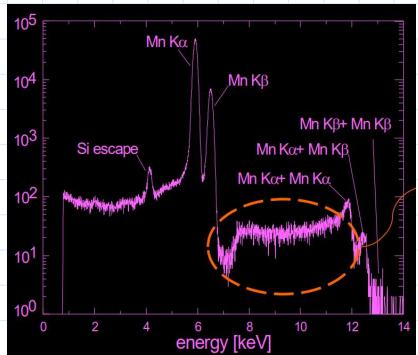
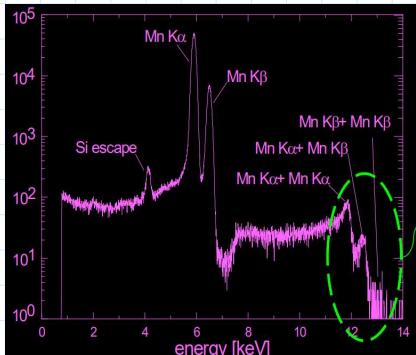
INTERMEDIATE PILEUP



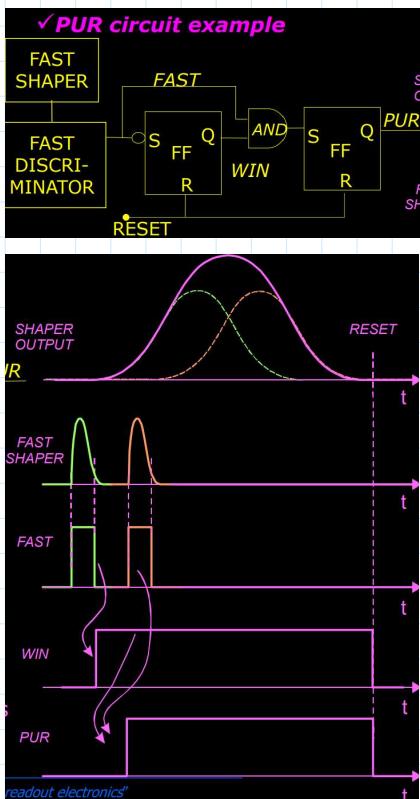
Most undesirable because :

- unknown result energy (sum can be off phase by an unknown current) ↓

⇒ any amplitude between peak energy and the sum can be obtained



PILEUP



A Pile Up Rejector circuit can be used to detect pileups and take action

⇒ fast shaper allows the detection of pileups in the "normal" shaper time

↳ the fast shaper output must be compared with a threshold. The threshold must be chosen carefully as a fast shaper output is more noisy

- too low : false positive rejections
- too high : pileups may not trigger the PUR

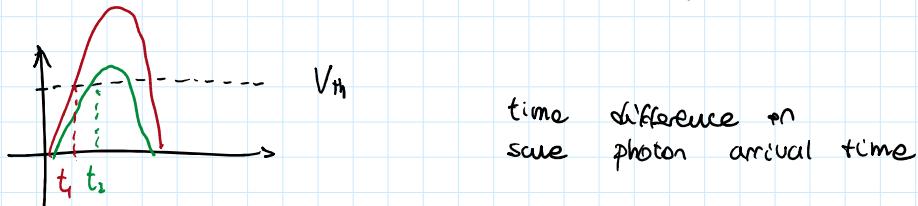
PILEUP REJECTOR

TIME MEASUREMENTS

TIME MEASUREMENTS

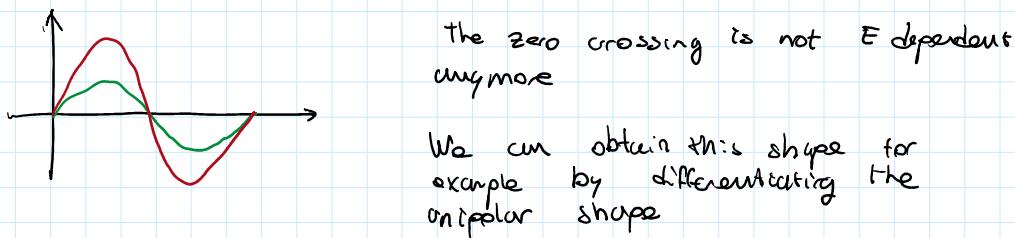
If we are interested in measuring the time of arrival of a photon, we must add a time detector circuit.

We could simply put a comparator on the shaper output. However, this is not very precise, as a comparator triggered by a fixed threshold would trigger at slightly different shapes ("times") depending on the amplitude (and so on the energy) of the incoming photon



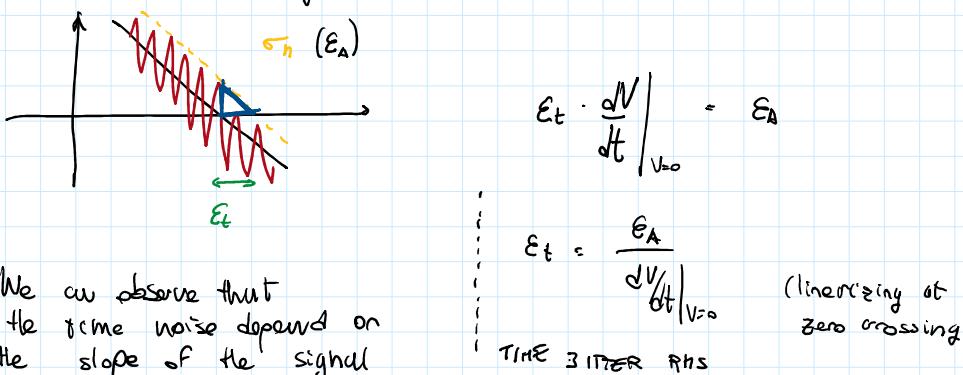
ONIPOLAR TIME COMPARATOR

To solve the amplitude dependence issue, we can use a bipolar comparator, that triggers at the zero crossing

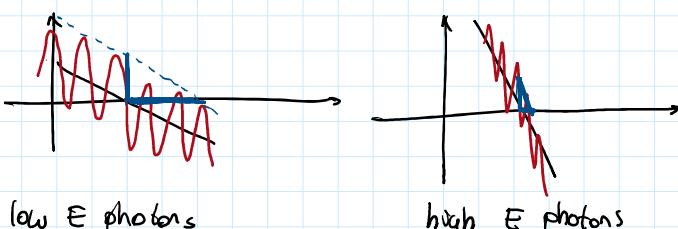


BIPOLAR COMPARATOR

However also the timing precision is affected by noise. On the zero crossing the signal affected by noise



However the slope of the signal is dependent on signal energy itself.



I V V VI

1

low E photons

high E photons

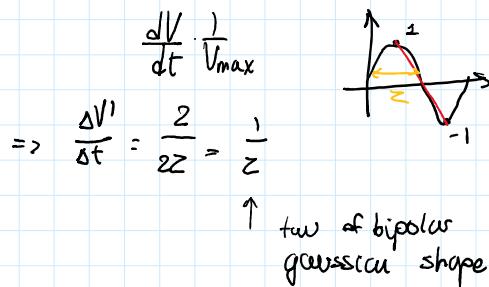
- * with the same voltage noise, the timing precision is better on a high energy photon

We can derive an alternative expression for time jitter considering

$$G_Q = \frac{V_{\max}}{Q}$$

$$E_T = \frac{\frac{G_a}{(dV/dt)}}{G_a} = ENC_{in} \cdot \frac{\frac{I}{G_a} \left(\frac{dV}{dt} \right)}{\frac{Q}{V_{onset}}} = \frac{ENC_{in}}{\frac{Q}{V_{onset}} \cdot \frac{dV}{dt}}$$

$$E_T = \frac{ENCL_q}{Q} \cdot z$$



RAMO THEOREM

Saturday, July 6, 2024 10:26 AM

SIGNAL FORMATION

The Shockley Ramo theorem states a relation between

INDUCED CURRENT
ON AN ELECTRODE

$\Leftrightarrow \frac{d}{dt}$ (RATE OF CHANGE)
OF ELECTROSTATIC FWD.
(\vec{E} field on electrode
surface)

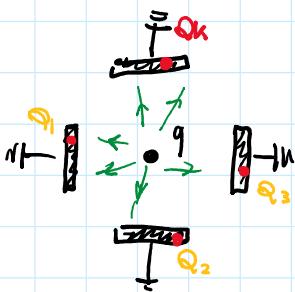


this can lead us to a relation between induced current on electrode and the instantaneous position (and movement) of moving charges

RAMO

We start by considering the green reciprocity theorem. Considering two cases in the same environment

* derived from V poisson equation



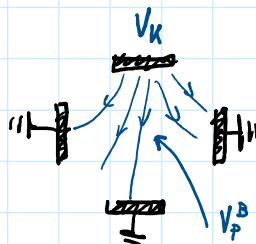
4

- Ground all electrodes
- Charge is placed in point P

||
the P charge generates an electric field, attracting charges on electrodes

on A

$$\begin{array}{ll} q_p & V_p \\ Q_K & V_K \\ Q_1 & V_1 \\ \dots & \dots \end{array}$$



B

- ground all electrodes, except K
 - No charge
 - apply voltage on indirect electrode
- ||
Voltage will be present on the point P

$$\begin{array}{ll} 0 & V_p \\ Q_K & V_K \\ \dots & \dots \end{array}$$

$$\begin{matrix} Q_K & V_K \\ Q_1 & V_1 \\ Q_2 & V_2 \\ Q_3 & V_3 \end{matrix}$$

$$\begin{matrix} 0 & V_P \\ Q_K & V_K \\ Q_1 & V_1 \\ Q_2 & V_2 \\ Q_3 & V_3 \end{matrix}$$

\Rightarrow Green reciprocity theorem states that

$$\sum Q_i^A V_i^B = \sum Q_i^B V_i^A$$

$$\begin{vmatrix} q \\ Q_K^A \\ Q_1^A \\ Q_2^A \\ Q_3^A \end{vmatrix} \begin{vmatrix} V_P^B \\ V_K^B \\ 0 \\ 0 \\ 0 \end{vmatrix} = \begin{vmatrix} 0 \\ Q_K^B \\ Q_1^B \\ Q_2^B \\ Q_3^B \end{vmatrix} \begin{vmatrix} V_P^A \\ 0 \\ 0 \\ 0 \\ 0 \end{vmatrix}$$

Solving the equation for our situation

$$q_p V_p^B + Q_K^A V_K^B = 0$$

particle charge

$$Q_K^A = -q_p \frac{V_p^B}{V_K^B} = -q_p \tilde{V}_W -$$

normalized potential at point p induced by $1V$ on V_K^B electrode

induced charge

on plate K due to q_p

potential at point p induced by electrode at V_K^B (no charge)

potential inducing V_p^B on electrode

GREEN RECIPROCITY

To derive the induced current on electrode K we can use the definition of current

$$i_K(t) = \frac{dQ_K}{dt}$$

Following green reciprocity

$$i_K(t) = \frac{dQ_K}{dt} = -\frac{d(q_p \tilde{V}_W)}{dt} = -q_p \frac{d\tilde{V}_W}{dt} \cdot \frac{d\vec{l}}{d\vec{l}} = -q_p \frac{d\tilde{V}_W}{d\vec{l}} \frac{d\vec{l}}{dt}$$

$\vec{l} \rightarrow -d\tilde{V}_W$ $\vec{x} = \vec{v}$

$$\vec{E}_w = - \frac{dV_w}{dx}$$

$$\frac{d\vec{x}}{dt} = \vec{v}$$

along line l

$$i_w(t) = q_p \vec{E}_w \cdot \vec{v}$$

weighting potential

Field: not the true

\vec{E} field in the device,
just the field induced
by testing potential ΔV
on electrode

carrier velocity

if we assume drift in
the device, we see that
the q_p charge velocity
is dependent on the true
electric field present
in the device

$$\vec{v} = N \vec{E}$$

(mobility model)

RATIO INDUCED CURRENT

The induced charge can be calculated on the
weighting potential map

$$Q_i(t) = \int_0^t i(t) dt = \text{by RUMO} = \int_0^t q \vec{E}_w \cdot \vec{v} dt$$

$$= \int_{z_0}^z q \vec{E}_w \cdot d\vec{x} = -q [V_w(z) - V_w(z_0)]$$

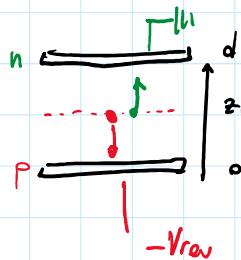
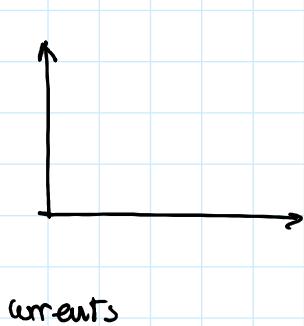
↑
line integral

INDUCED CHARGE

SIGNAL FORMATION EXAMPLES

Assuming a device with ~ uniform field (like pin junction) we can derive using RUMO the signal generated from the detector when a charge moves in it

generated from the detector when a charge moves in it



Whegthening field

$$E_W = -\frac{dV_W}{dx}$$

constant on device

$$= \frac{V_W}{d} = \frac{1}{d}$$

$$i_n(t) = -q(-E_W \cdot v_n) = q \frac{1}{d} \cdot V_n = \frac{q}{T_n} \cdot V_n$$

$$i_p(t) = q(-E_W \cdot v_p) = q \frac{1}{d} V_p = \frac{q}{T_p} \cdot V_p$$

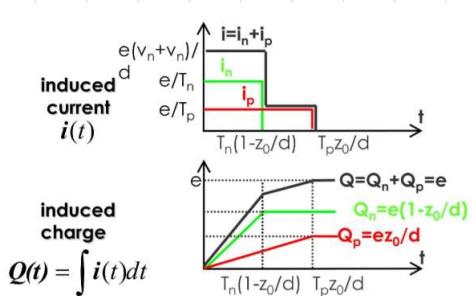
Velocity

$$v = \mu E = \mu \frac{V_{rev}}{d}$$

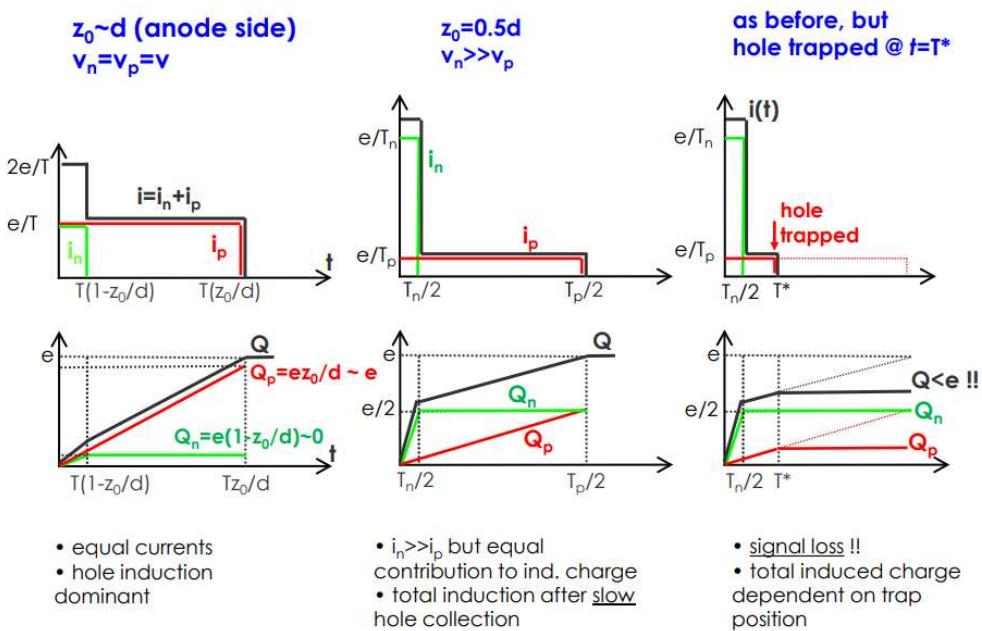
want depend on transit time in the whole electrode however carriers are not transitioning for the whole time, as they are generated in position z_0

$$\begin{cases} i_n(t) & 0 < t < T_n(1 - \frac{z_0}{d}) \\ i_p(t) & 0 < t < T_p(\frac{z_0}{d}) \end{cases}$$

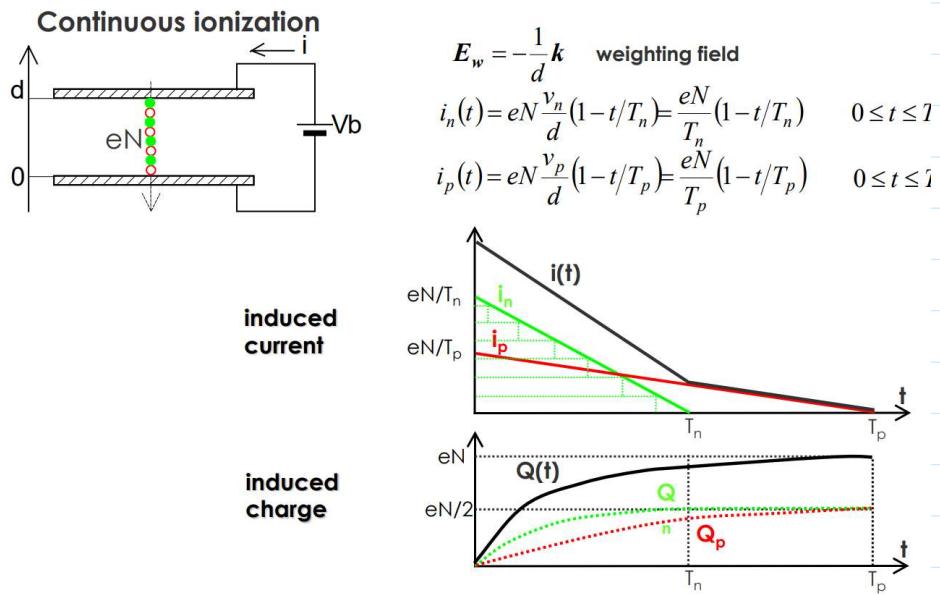
consider that typically also, $v_p < v_n$ as holes have a lower mobility (so lower velocity)



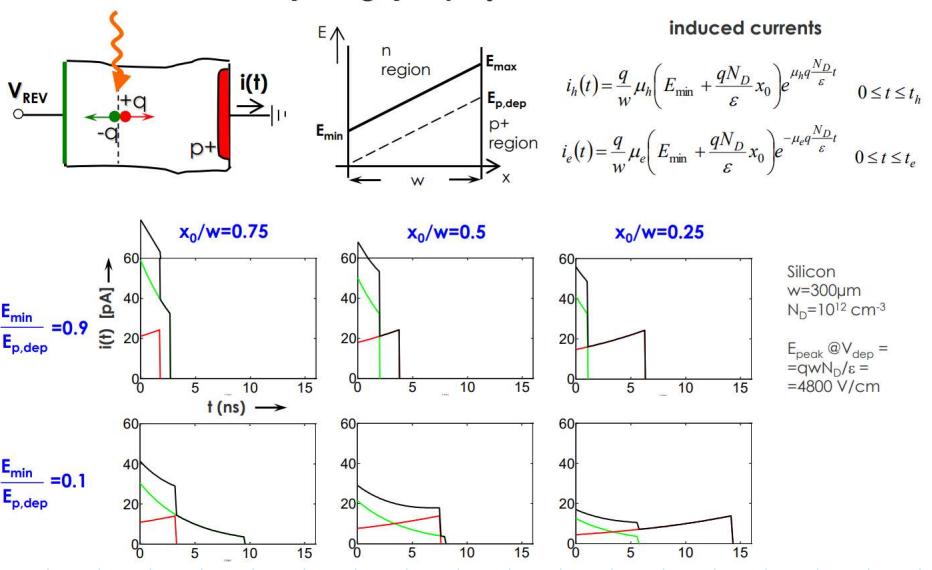
- sensing the charge moving
- not important collected charge
- at the end $Q = q$ (collected charge)



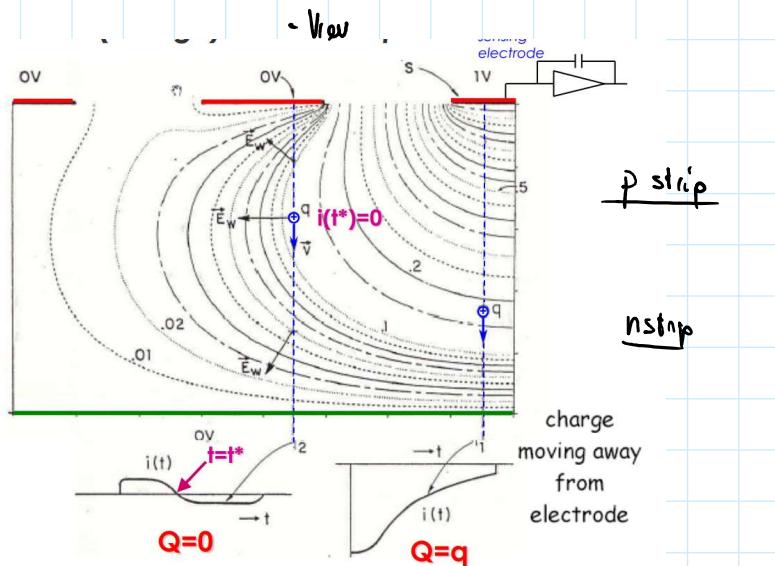
✓ Induced current (charge) in planar electrode geometry



✓ Induced current (charge) in pn junction



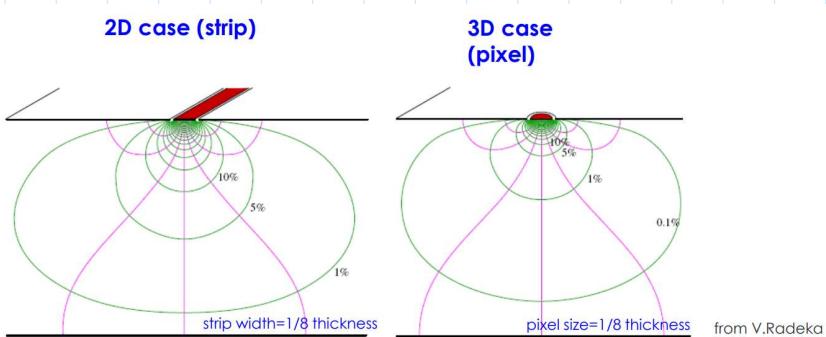
INDUCED CURRENT EXAMPLES



$$i = q \vec{E}_w \cdot \vec{v}_w \quad \text{Stripe detection mechanism}$$

here if we can observe 2 things

- a charge detected by one strings can influence the current on another electrode
- the strip width - comparable with detector depth - mutes the E_w weighting field evenly distributed
 \Rightarrow charge collected on detector depends on starting position of ionization



We can reduce both effects by having a strip width \ll defector depth

↳ Weighting Potential will be concentrated only near the strip

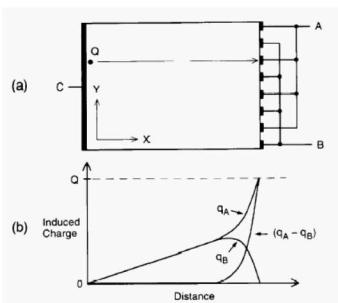
→ Weakening Potential will be concentrated only near the strip

=> Charge collection will be almost independent on initial ionization position :

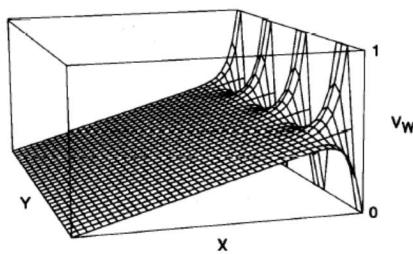
$Q \propto q$ (as if it started from one electrode to another)

=> also almost independent for hole trapping

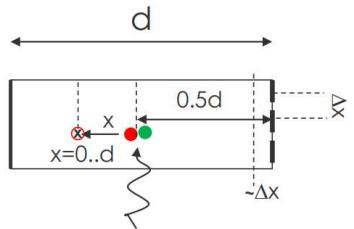
DETECTOR STRIPS



P.Luke, IEEE Trans Nucl. Sci., vol.42, no.4, Aug. 1995

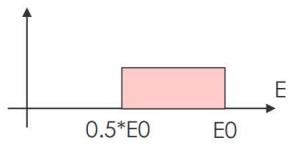


- Two inter-digitated coplanar grid electrodes sense the motion of charge carriers in the detector (solid-state equivalent of the "Fritsch grid" of gas detectors)
- A small potential difference applied btw the C-grid and NC-grid to avoid charge sharing and double polarity signals
- When generated in the bulk, a charge carrier induces equal amount of charge on the 2 grids. A net difference signal is induced only when the carriers to be collected (e.g. electrons) are close to the coplanar electrodes.
- The net result is a measured charge nearly independent of the interaction depth

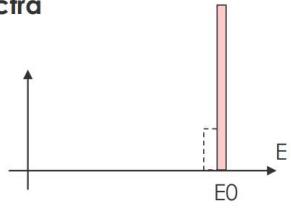


- radiation incident at $x=0.5d$.
- hole trapping can take place at x , uniformly distributed within $(0, 0.5d)$
- electrons travel with no trapping

energy spectra



single-ended readout

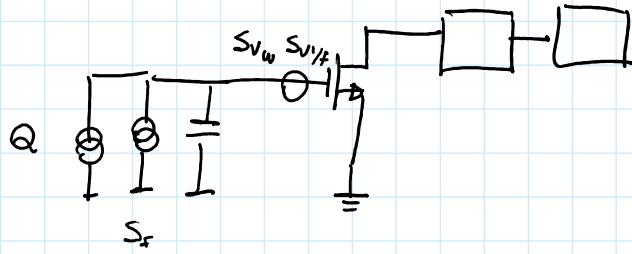


differential readout

ADVANCED SILICON DRIFT DETECTORS

Sunday, July 7, 2024 4:53 PM

Detector capacitance plays a key role in ENC



series white

$$ENC^2 = A_1 \frac{2kT\alpha}{w_t} C_G \cdot \left(\sqrt{\frac{C_0}{C_G}} + \sqrt{\frac{C_0}{C_G}} \right)^2 \frac{1}{z}$$

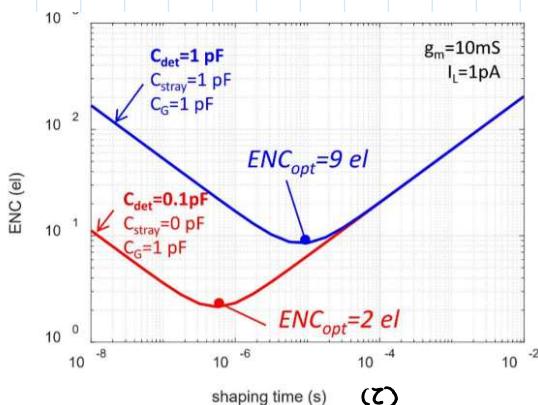
↑ f_{cutoff} nMOSFET

parallel white

$$+ A_2 (\pi A_f C_G) C_D \left(\sqrt{\frac{C_0}{C_D}} + \sqrt{\frac{C_0}{C_D}} \right)^2 + A_3 (q I_L) z$$

series Y_f

when matching cap



Supposing we can optimize setting

$$C_{G_{opt}} = C_D$$

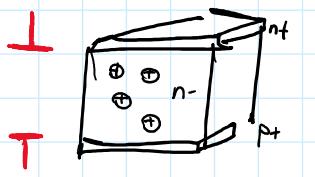
$\Rightarrow C_D$ determines contribution of series white and Y_f

$\Rightarrow ENC_{opt}^2$ (found by setting white par = white zero) (z)

$$\propto \sqrt{\frac{C_D I_L}{w_t}}$$

lowering as much as possible C_D increases S/N

DETECTION CAPACITANCE

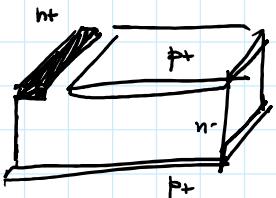


classic pin junction

- A of capacitance is big
- thickness cannot be increased due to technology limitations

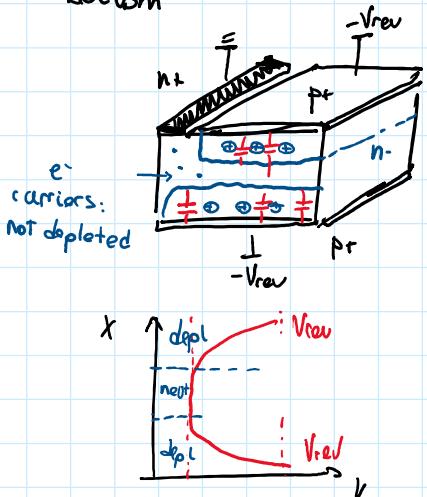
- thickness cannot be increased due to technology limitations (wafer thickness)
 - would increase active area
 - would decrease capacitance

CLASSICAL PIN LIMITATIONS



To increase active area we could develop the pin horizontally

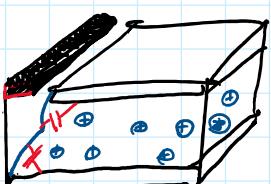
the depletion region would develop between p+ and n- (like in classic p-n junctions) both on top and bottom



This however still results in a big depletion capacitance (big active area on both sides)

no E field in the neutral region

- ⇒ However, to increase performance, we can deplete the whole n- thickness
- ⇒ this gives a huge advantage in capacitance as in fact reduces active area of the depletion region



The active area now becomes basically only the fringing contribution

We must deplete all the device

$$V_{rev} = \frac{qN_d}{2\epsilon_0\epsilon_s} x_d^2 = \frac{qN_d}{2\epsilon_0\epsilon_s} \left(\frac{d}{2}\right)^2 = \frac{1}{4} \frac{qN_d}{2\epsilon_0\epsilon_s} d^2$$

↑ both sides → half thickness

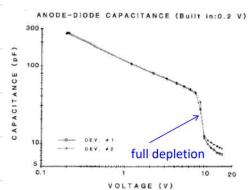
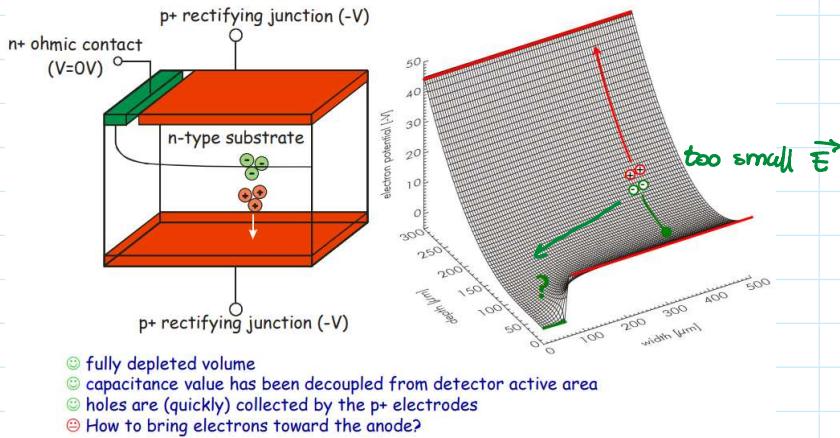


Fig. 12. Capacitance versus voltage plots of two of the test devices provided on the wafer for monitoring its doping uniformity.

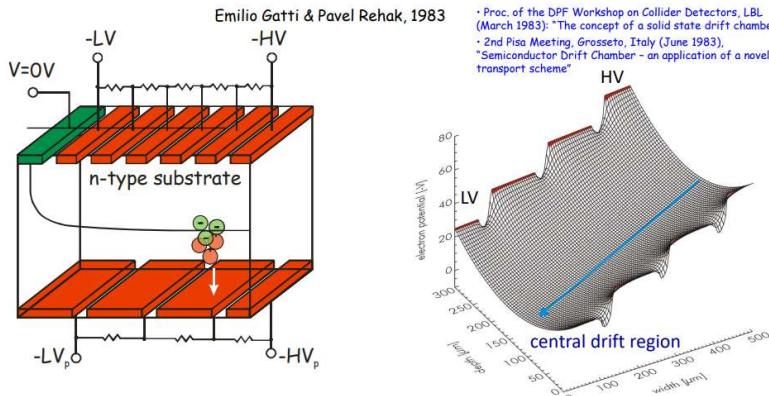
(Emilio Gatti)

SILICON DRIFT DETECTOR - FULL DEPLETION

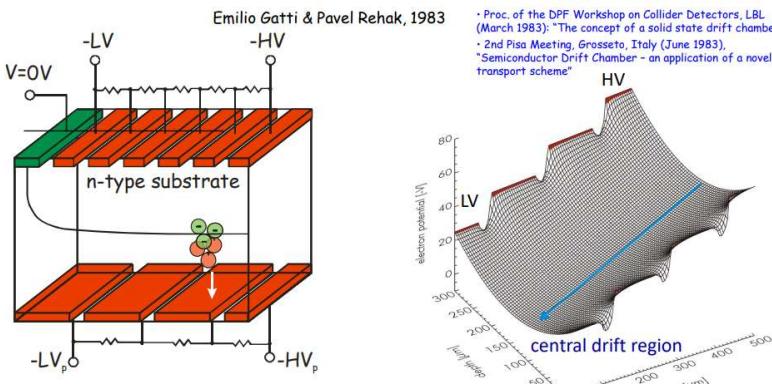
However this design present an issue.
The drift between the two contacts is too slow
as the field bends only toward the center



pn collectional drift

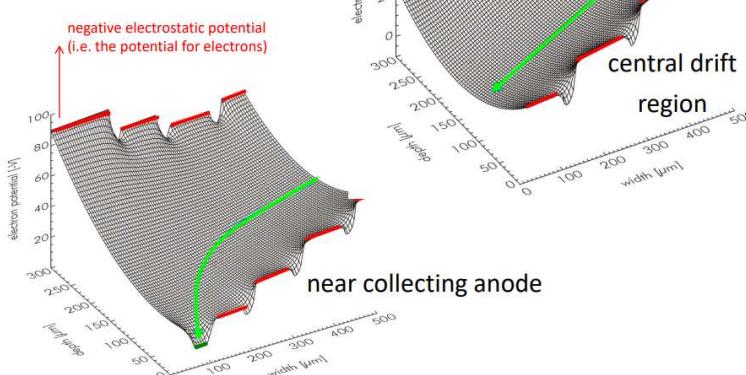


- p+ junctions segmented in strips with linearly increasing potential on both sides
→ uniform drift field parallel to the surface
- signal electrons are focused in the center of the wafer and drift at constant velocity towards the readout anode



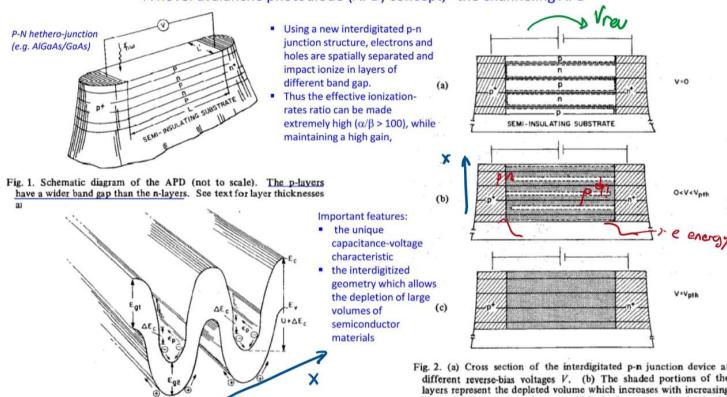
- p+ junctions segmented in strips with linearly increasing potential on both sides
 - uniform drift field parallel to the surface
 - signal electrons are focused in the center of the wafer and drift at constant velocity towards the readout anode

Shape the potential for the charge collection region



The simultaneity of ideas

A novel avalanche photodiode (APD) concept, "the channeling APD"



F. Capasso, IEEE Trans. Electron Device, Vol. ED-29, No. 9, Sept. 1982

SILICON DRIFT DETECTOR

SPECIALIZED SDD

• SPECTROSCOPY ORIENTED

these SDD do not implement position sensing.

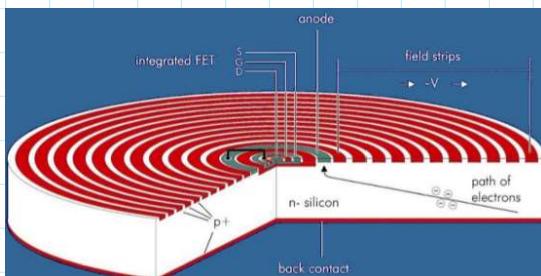
- **SPECTROSCOPY DEFINED**

these SDDs do not implement position sensing.
they use all their active area to collect photons and distinguish between different impacting photon energies

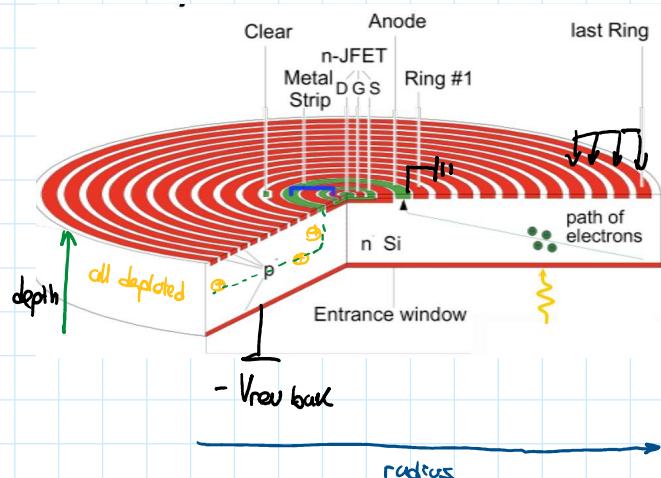
- **POSITION SENSING**

These devices deflect the impact point like a camera constructing an image (20 - 30 - 16 bits)
they can or cannot distinguish different energies of impact photons (pixel spectroscopy)

SPECTROSCOPY

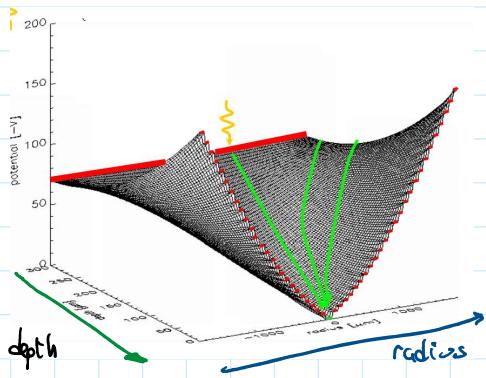


- Cylindrical shape : single anode in the center
↳ all active area to detection
- Back contact is the exposed side to photons
↳ p+ back contact is an entrance window
- Transistor is integrated in the detector, directly connected to the anode



p+ front segmentation
: makes the electrons drift toward the center

p+ back
p+ front
: serves to fully deplete the SDD (-cap) and divide the carriers toward the center depth



$A_{\text{typical}} = 5-100 \text{ mm}^2$
 $\text{Dot cap} = 100-200 \text{ fF}$

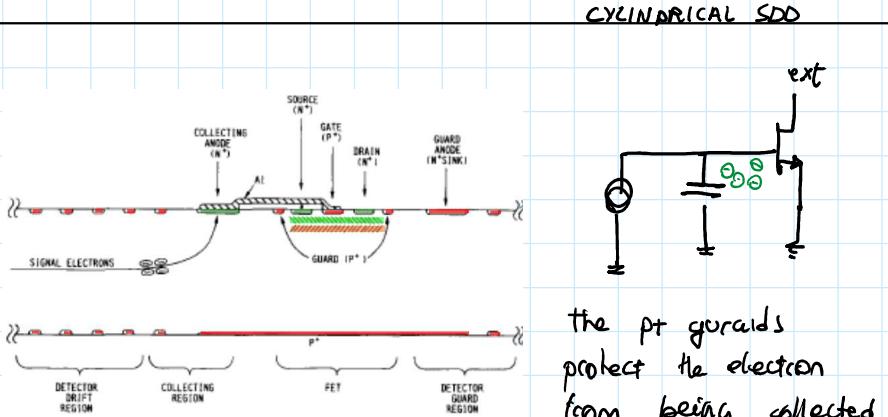


Fig. 1. Qualitative cross section of a completely depleted detector (semiconductor drift detector) with the input FET integrated on it.

\Rightarrow electrons are collected in the anode, connected to the gate of the JFET via a short metal contact

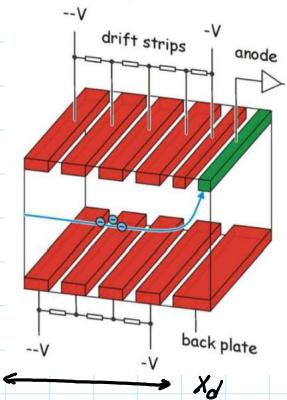
- ↳ extremely low capacitance on preamp
- ↳ smaller contributions on series noises (dependent on capacitance)

The cylindrical shape enables a 360° collection

POSITION SENSING SDD

Position sensing can be achieved

- via subdivision of strips in anode
- via timing correlation



Assuming

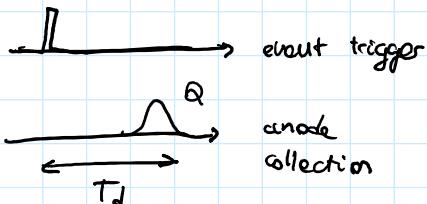
- constant Electric field
(in low doped n- $\frac{dE}{dx} \approx 0$)

\Rightarrow constant drift velocity

$$v = \mu E$$

(or due to sizing of
Vsaturation in whole
device)

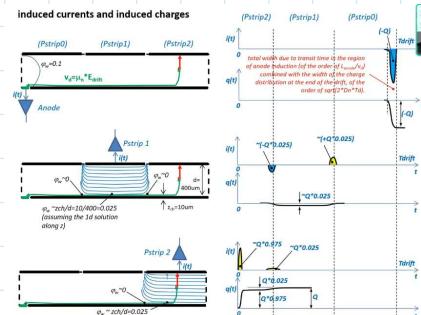
$$x_d = \sqrt{\mu E} \cdot T_d$$



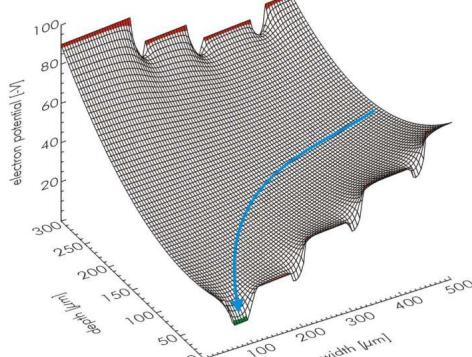
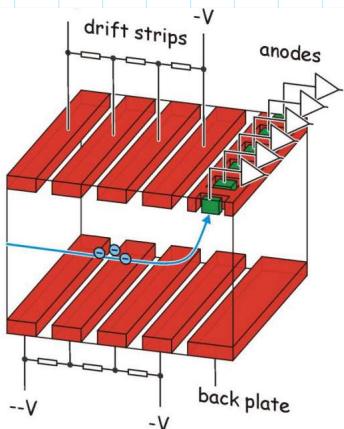
Even if the relation
is not so linear, it
(can be derived from testing)

Need of a start event trigger

- x available if hit time is known - a priori
(particle accelerators)
- x can be derived by holes generation;
immediately collected by pt strips



2D POSITION SENSING W TIMING

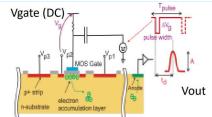


2D position sensing can be achieved combining
multiple strips on one direction and
timing detection on the other

2D DETECTION

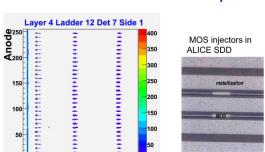
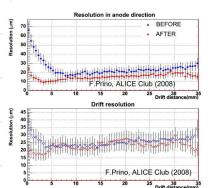
* MOS injectors

Drift speed depends on T:
 $v_{\text{drift}} = \mu_0 E_x + \mu_0(T) \times T^{2.4}$
 Charge injectors allow real-time calibration of time vs. distance curve



calibration mechanisms
for timing detection.

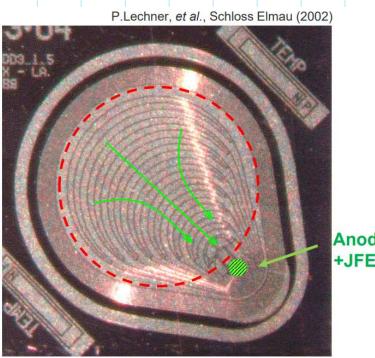
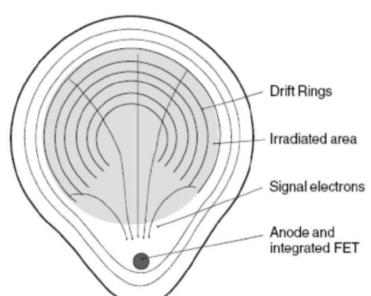
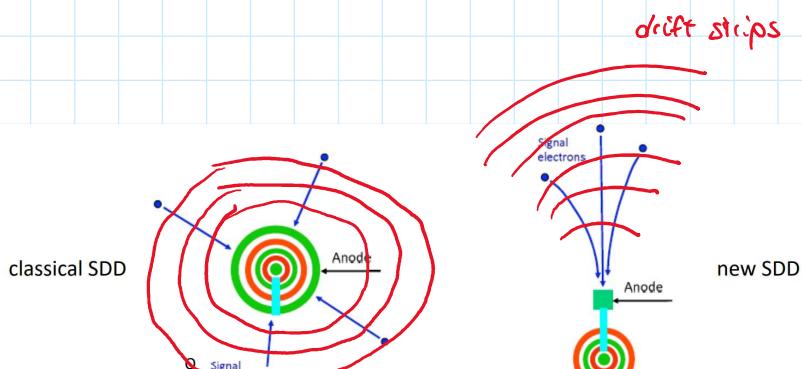
* Position resolution after calibration ~20-25 μm



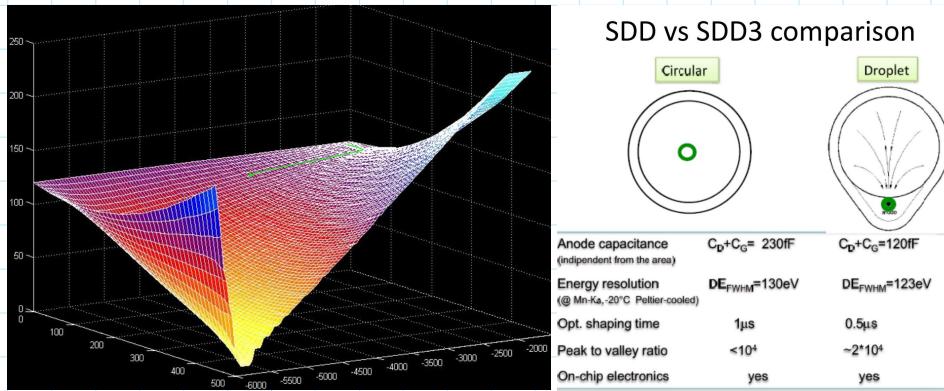
ADVANCED SDD CONCEPTS

The cylindrical SDD needs a circular anode
 \Rightarrow big capacitance

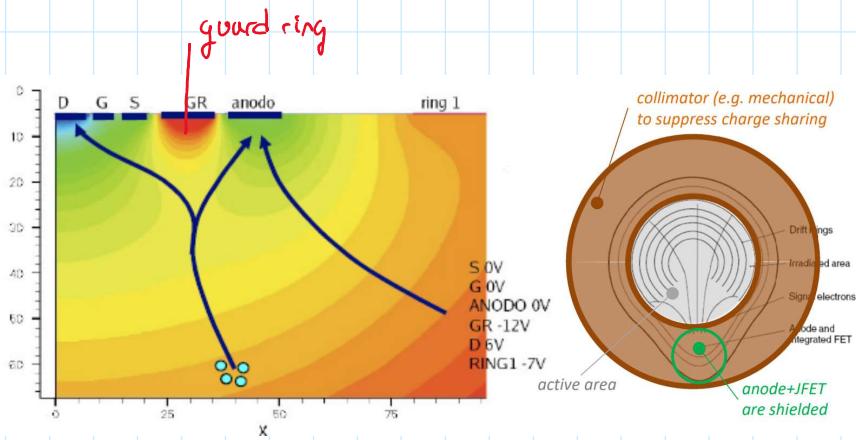
If we instead of making the device circular, but
 we instead make it strip-like



This can optimize the area of the anode and the JFET making it less and optimizing for ENC.



DROPLET SDD SDD3



If an interaction (photon hit) happens near the anode region, it may happen that the interaction charge is splitted above the guard ring and some can pass directly to the drain.

\Rightarrow this can be avoided putting a screen on the outer ring, avoiding collisions near the anode

\hookrightarrow beware that the anode region will not be active area.

GUARD RING - NEAR ANODE COLLECTION

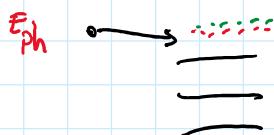
* Silicon Escape spectroscopy
- important to signal formation and radiation dos.

When doing spectroscopy, we are collecting the charge sensed via the electronics created by

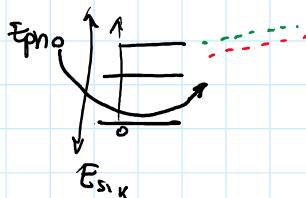
When doing spectroscopy, we are correlating the charge sensed via the electronics created by the incoming photon via the mean creation energy

$$\langle N \rangle = \langle w \rangle E_{ph}$$

However, it can happen that incoming high energy photons interact with the lower shells of silicon



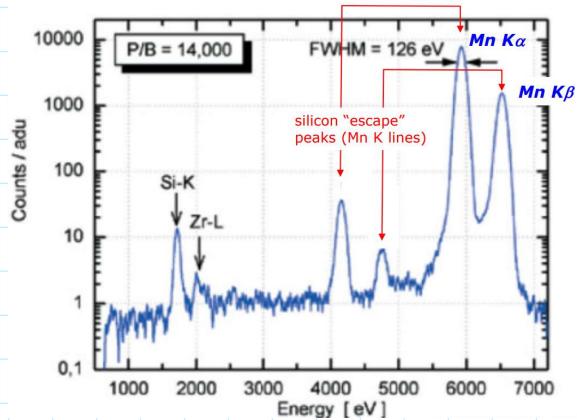
$$\langle N_d \rangle = \langle w \rangle E_{ph}$$



$$\langle N_d \rangle = \langle w \rangle \cdot (E_{ph} - E_{Si_K})$$

This result in "false" incoming detected photons at energies at exactly

$$E_{ph} - E_{K_{Si}}$$

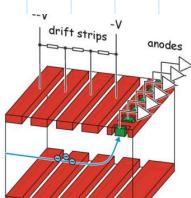


SILICON ESCAPE PEAKS

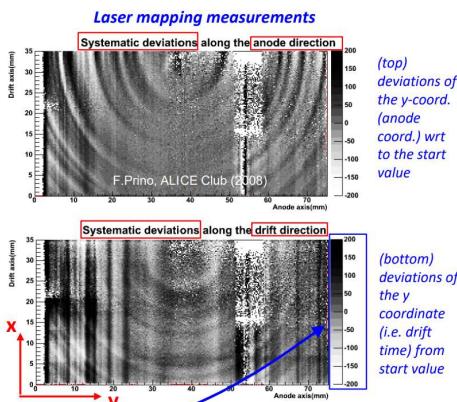
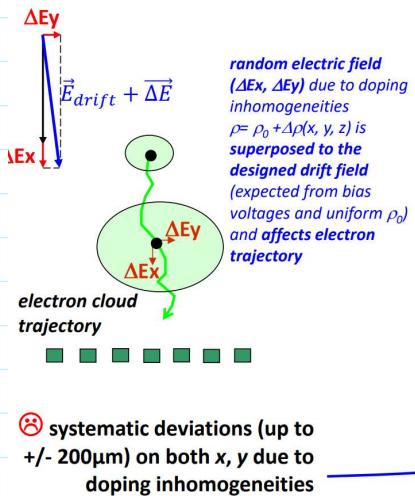
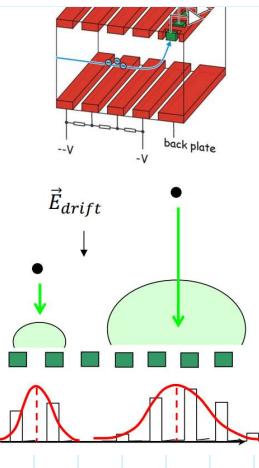
2D position sensing in MULTI ANODE SDD presents some limitation

× timing direction
⇒ a trigger is needed

× strips direction

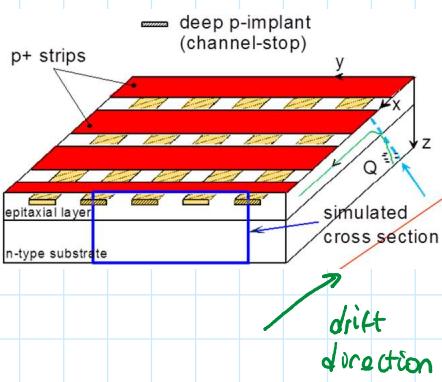


- > strips direction
 - ⇒ drift adds a non equal distribution of the charge cloud depending on the timing direction position
 - ⇒ drift direction is also not perfectly vertical due to systematic process errors



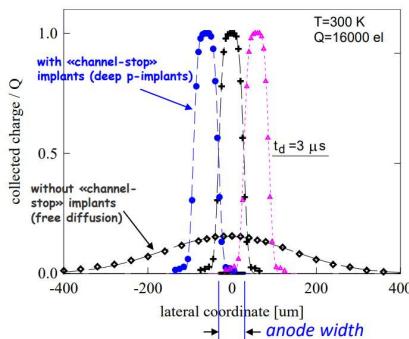
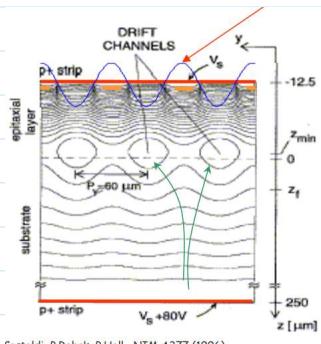
LIMITATIONS OF MULTI ANODE STRIP DET.

Suppression of lateral broadening with channel-stops



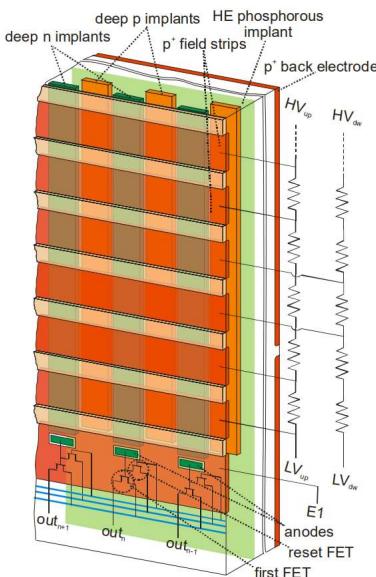
The p channel implant are put at a higher (negative, in module) potential to reject electron

In this way electrons drift in these channels avoiding dispersion in a charge cloud



experimental results

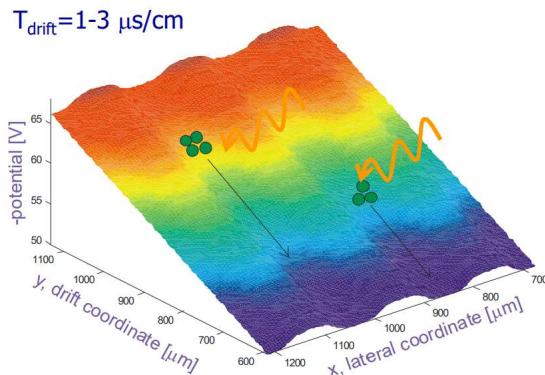
These devices are called Multi Linear SSD



- fully depleted n-type bulk
- p+ entrance window implanted on the back side
- array of p+ strips implanted on the front side
- **channel-stops (deep p-implants)** for lateral confinement
- **channel-guides (deep n-implants)** for lateral confinement and drift enhancement
- HE n implant locates the drift channel close to the finely structured surface
- on-chip electronics (JFET in source follower configuration)

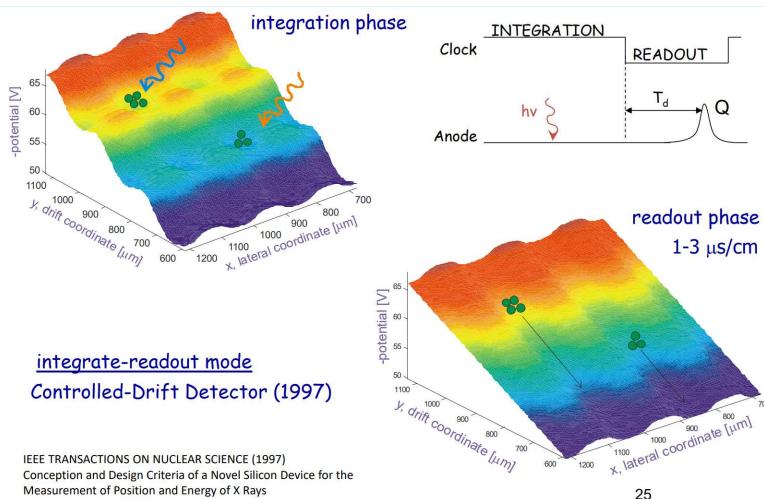
more doped so
asymmetric potential at
depth
=> n+ implants
=> drift line (minimum
energy line)
closer to surface

As before,
trigger must
✓ external
✓ from holes
to p+ immediate
collection
on strips/



MLSDD MULTI LAYER SSD

The MLSDD can also be used in another mode, as CDD (Controlled Drift Detector)



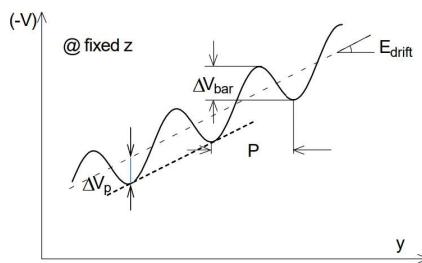
integrate-readout mode
Controlled-Drift Detector (1997)

IEEE TRANSACTIONS ON NUCLEAR SCIENCE (1997)
Conception and Design Criteria of a Novel Silicon Device for the
Measurement of Position and Energy of X Rays

25

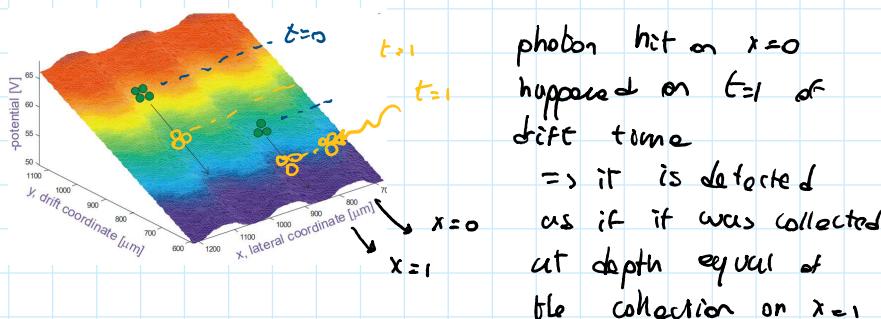
The voltage on the drift stripes can be alternated during an "integration time" in which potential wells are created and electrons accumulate in it

When the voltage is then set to drift mode, all the integrated charge is collected via the classic readout



In the CDD however there is one problem
"out of time events"

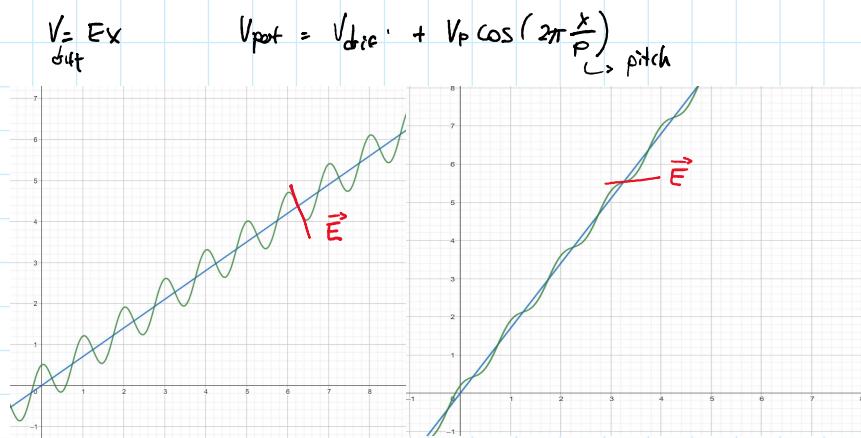
- * If during readout a photon collection happens it may seem from a different position from when it happened



photon hit on $x=0$
happened on $t=1$ or
drift time
=> it is deflected
as if it was collected
at depth equal to
the collection on $x=1$

To solve this problem we should stop the detection during readout time (maybe with a mechanical mask, not so easy)

CDD CONTROLED DRIFT DETECTOR



$$E = -\frac{dV}{dx} = 0 \quad \frac{dV_{\text{drift}}}{dx} + V_p \frac{2\pi}{P} \sin\left(\frac{2\pi x}{P}\right) = 0$$

$\downarrow \text{max point}$

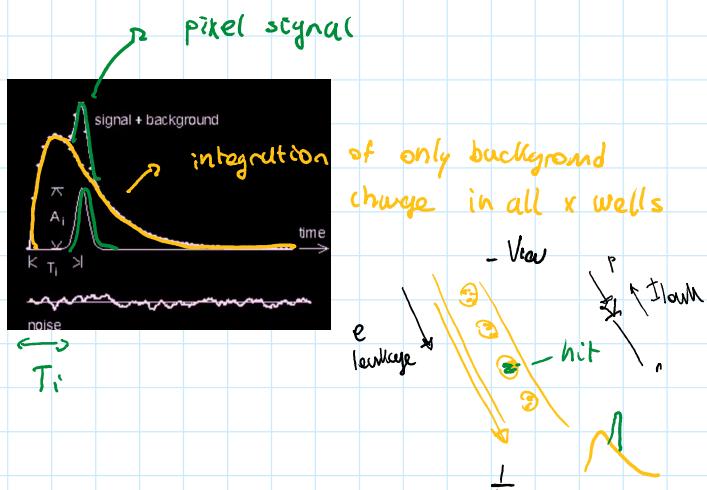
$$E_{\text{drift}} + V_p \frac{2\pi}{P} (1) = 0$$

$$V_p = \frac{E \cdot P}{2\pi}$$

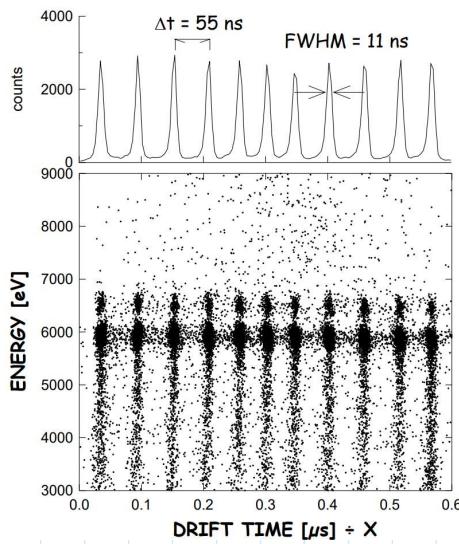
if $V_p > \frac{EP}{2\pi} \Rightarrow \text{field inversion}$

SIZING OF A VRARRIER

The integration-drift device has however a readout slightly different



BACKGROUND PEAK



correlating every reading with

- time
=> X (drift) position
- amplitude
=> collected charge (energy)

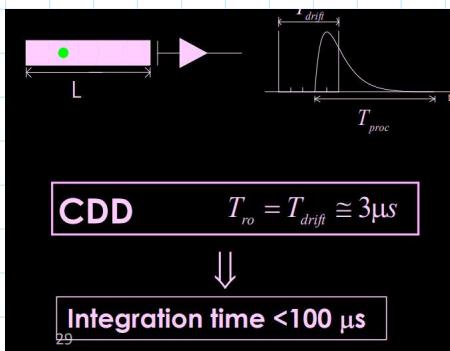
we can obtain a 2D spectroscopy image!

(attention: in He integration time more photon hits could pile up!
=> may lead to "fake" high energies detected)

IMAGING SPECTROSCOPY

CDD are really fast as they do the whole readout of the image in one shot

$$\text{CDD time} \sim T_{\text{drift worst}} + T_{\text{shaper}}$$



Extremely fast
respect to CCD detector!
(however works best with
low level of occupancy)

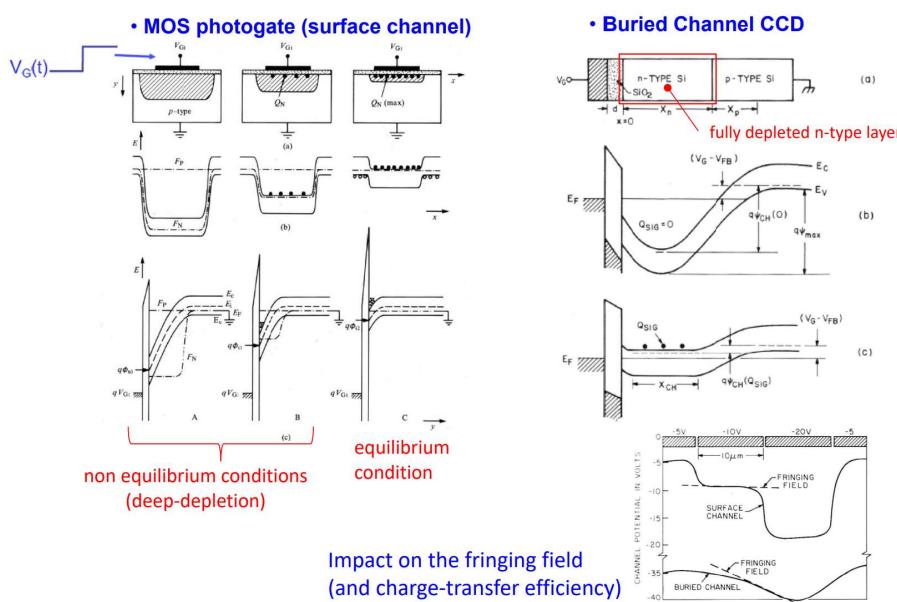
CCD

CHARGE COUPLED DEVICES

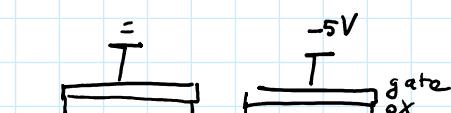
A limitation of CCD is that the readout of the image happens pixel by pixel

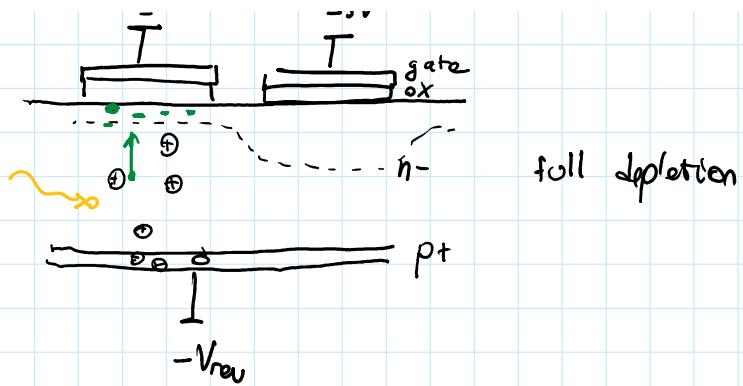
$$CCD_{time} = N_{pix} \cdot \left(\frac{1}{f_{shift}} + T_{processing} \right)$$

- $\frac{1}{f_{shift}}$ is basically the drift time from 1 pixel to the next
 - (readout)
 - $T_{processing}$ is the τ of the output shape (shaper) optimized to reduce ENC
 - \Rightarrow If integration time too large we increase leakage current
 - If we want higher frequency, (readout time less) we have to cut down processing time, increasing noise
 - || higher frequency
 - || higher resolution
- CCD TIMING IS SLOW RESPECT TO CDD



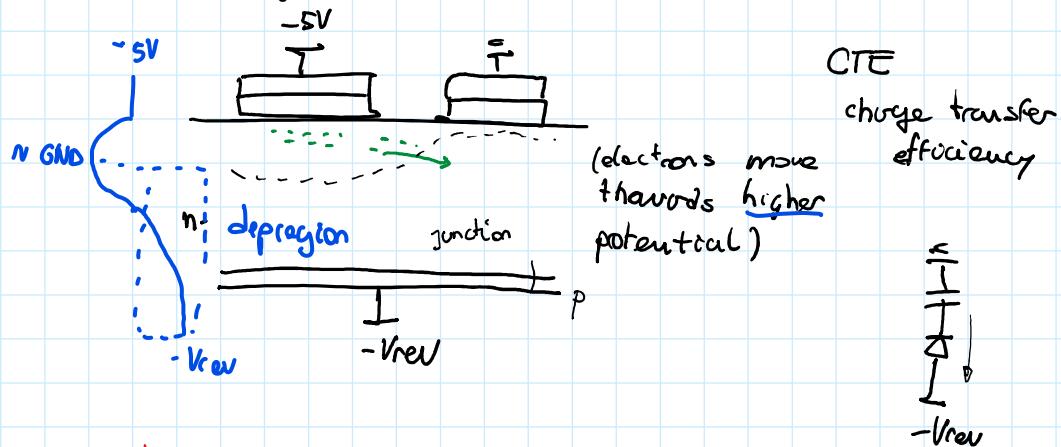
BURIED MOS





the collected charge accumulates under the "active" or gate

the transfer happens moving the potential on the gates



- also drift in buried mos happens
 - at the center depth of the channel
 - not near the surface

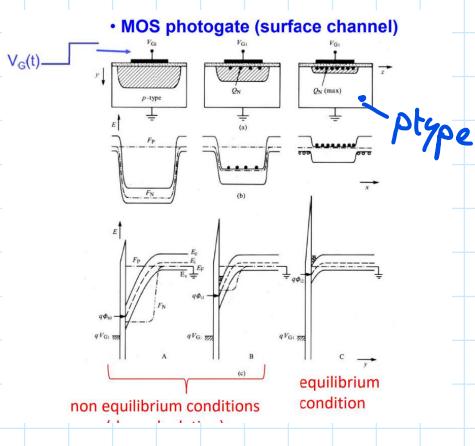
Mos PHOTOgate

We could replace the pn fully depleted junction directly with the mos capacitor

loop

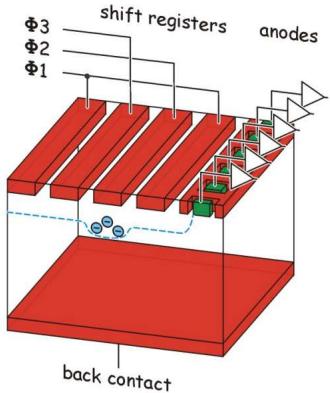
We can create a depletion region using the mos field effect and collect the generated charge in the depletion region in the channel

then transfer it using the gate potentials like before

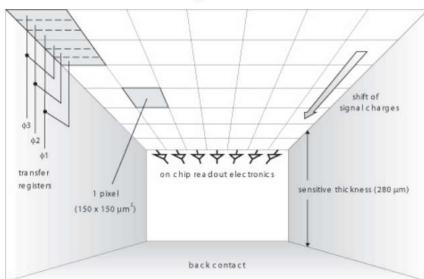
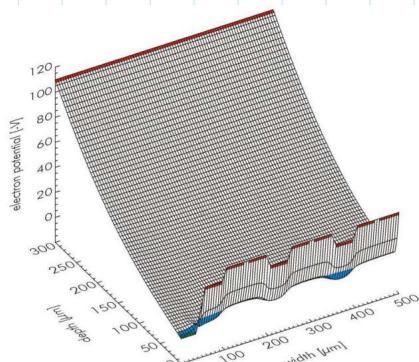


the transfer will happen near the mos surface (near SiO₂)
⇒ not good because of trapping

MOS CCD TYPES



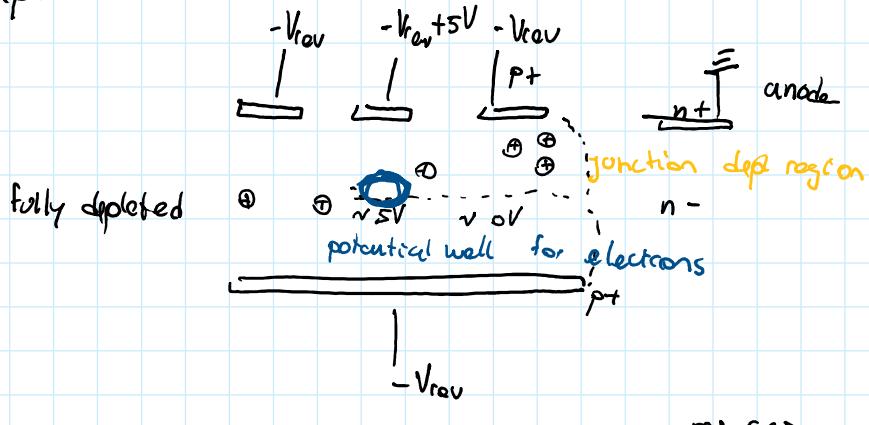
- full depletion (50 μm to 500 μm)
- back side illumination
- radiation hardness
- high readout speed
- pixel sizes from 30 μm to 1 mm
- charge handling: more than 10^6 e/pixel
- high quantum efficiency



Another similar implementation of CCD can be done without a MOS capacitor.

Exploiting the already present bias p+ strips in SDD (Silicon Drift detectors)

Voltage can be modulated like in a CCD on those strips.

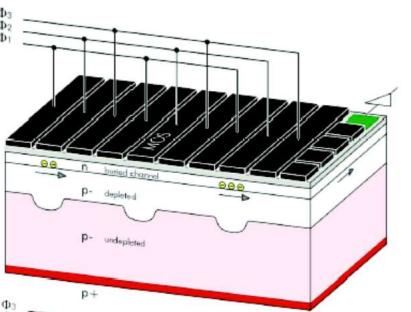


pn-CCD vs. MOS-CCD

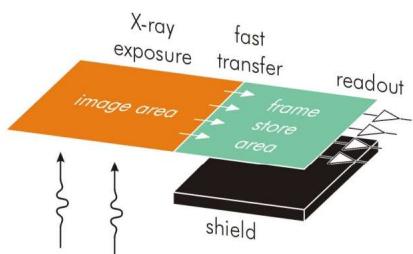
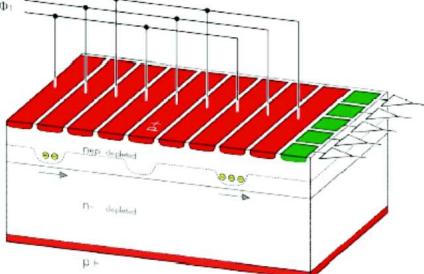
classic CCD
structure

- MOS transfer gates
 - pn junctions
- buried channel
 - deep transfer
- partial depletion
 - full depletion
- front-side illumination
 - backside entrance window
- serial readout
 - 1 preamp per channel

MOS CCD



pn CCD

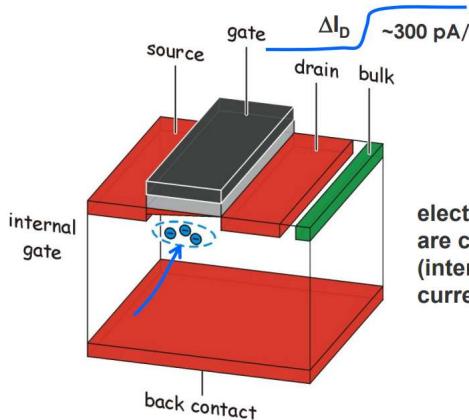


CCD readout can
also have smarler
configuration :

To allow integration
time simultaneously
to readout time,
we can store the
integrated frame
in another area for
a readout

PN CCD vs MOS CCD

DEPFET



electrons generated in the fully depleted bulk are collected underneath the transistor channel (internal gate) and modulate the source-drain current ($\sim 300 \text{ pA/e-}$).

collecting anode = internal gate \rightarrow no interconnection strays!
non-destructive readout \rightarrow readout on demand!

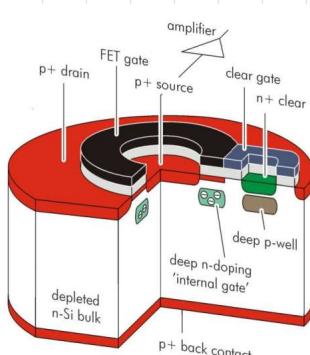
Instead of using MOSFETs for readout, we can use DEP-MOSFET.

The principle is similar to MOSFETs

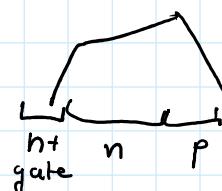
Instead of having only the external gate DEP MOS also have an internal gate residing underneath the channel

\rightarrow n+ internal gate

this internal gate is composed on an n+ region



charge is stored on n+ internal gate
(like in a pin junction)



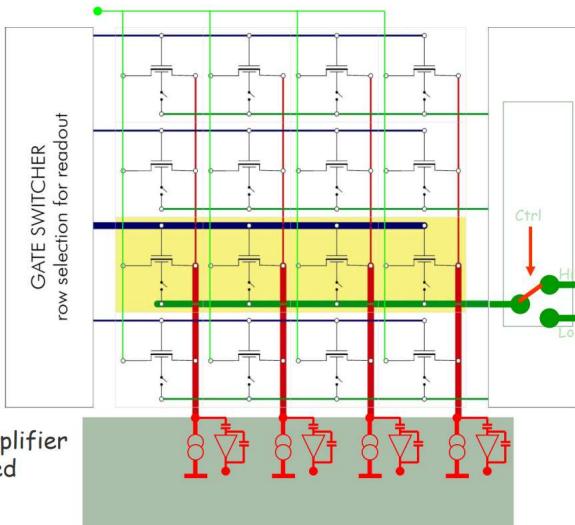
\Rightarrow this charge opens or closes the MOSFET channel using field effect

DEPFET matrix prototypes

- Global drain contact
- Sources connected column-wise
- Gate, Clear & Cleargate connected row-wise
- Source follower readout: Column biased by current source

CAMEX 64 G:

64 channel low noise voltage amplifier
8-fold CDS-filter and integrated sequencer

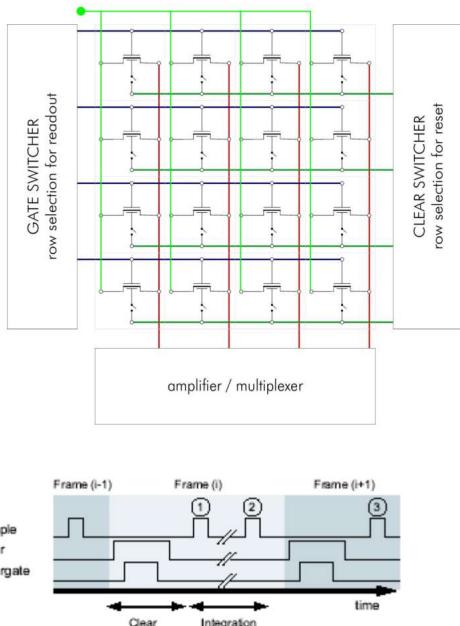


Switcher II:

Control chip with 64 channels a 2 ports & integrated sequencer
AMS high voltage CMOS process (up to 20 V)

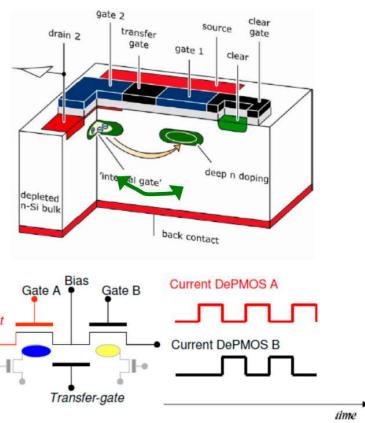
DEPFETs as pixel detectors

- matrix arrangement allows to turn on transistors individually
- readout of charge in place of origin
 - no "charge transfer loss"
 - no "out-of-time" events
- continuous row-by-row readout (through serial or parallel readout)
- followed by clear of row
- no waiting (charge collection) period needed



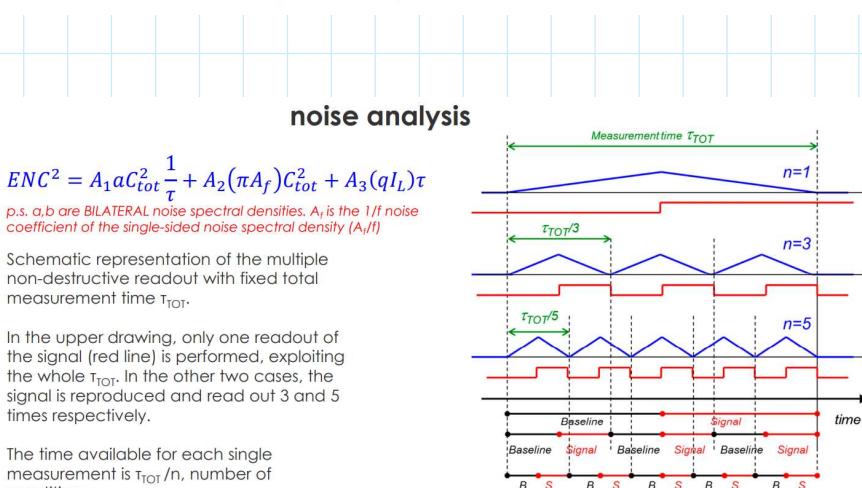
DEPFET detectors are used in configuration with 1 DEPFET per pixel, as charge can be collected in the internal gate.

Multiple readout (ping-pong)



- Noise reduction technique based by repetitive readout of signal charge
→ ENC ultimate limitation can be broken
- Signal charge measured by current difference between filled and empty internal gate
- Moving signal charge in and out internal gate N times
 - noise reduction !
- We will analyse it for white and 1/f series noise

The ENC can in principle be lowered below the limit of single-pulse (S/N) analysis



The reduction of the shaping time for each single measurement (look at the blue weighting functions) turns out in an increased r.m.s. value of the white noise of the individual measurements. The averaging effect of the n readouts (3 and 5 respectively) compensates this noise increment. Therefore, the ENC component related to the white voltage noise does not change with the number of measurements in a fixed time interval.

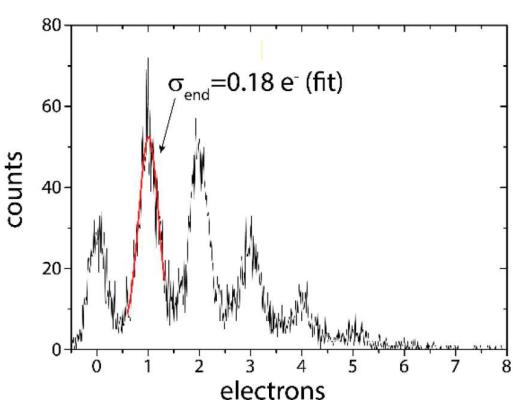
For the 1/f noise the situation is different. The r.m.s. value of noise of one measurement is independent from the measurement time. This means a single measurement of time length T_{TOT}/n would result in the same r.m.s. noise.

In this case, the averaging effect of n measurements makes the ENC go down with approximately \sqrt{n} .

$$\text{white } A_1 \cdots \frac{1}{\sqrt{n}} = \text{ENC}_w^2 \cdot N$$

$$1/f \quad \text{ENC}^2 / N \Rightarrow \text{ENC}_w^2 \text{ constant}$$

$$\text{ENC}^2 \rightarrow \text{ENC}_w \sqrt{\frac{1}{N}}$$



- sub-electron noise achieved !
- increase of ENC for higher no. of readouts most likely related to parallel noise
 - single photon imaging in the visible !
 - absolute calibration of charge !

Single photon spectrum measured at low light intensity with a circular RNDR-DEPFET at a temperature of -55 °C. Due to the higher amplification the read noise of a single readout is only 3.1 e- rms and a minimum noise of 0.18 e- was obtained with only 300 readouts.