

Date		Topic covered	Deadline	Release
Tuesday	16-Jan	Introduction		
Thursday	18-Jan	Basic Concepts		
Tuesday	23-Jan	Real CPU examples		
Thursday	25-Jan	Instruction Pipelines		HW1 out
Tuesday	30-Jan	...		
Thursday	1-Feb	Dependencies & Interlocking		
Tuesday	6-Feb	...		
Thursday	8-Feb	Scoreboarding	HW1 due	HW2 out
Tuesday	13-Feb	...		
Thursday	15-Feb	Register Renaming		
Tuesday	20-Feb	...		
Thursday	22-Feb	Interrupts & Reorder buffers		
Tuesday	27-Feb	...	HW2 due	
Thursday	1-Mar	Superscalar		
Tuesday	6-Mar	Winter Break		
Thursday	8-Mar	...		
Tuesday	13-Mar	Mid-term Exam		HW3 out
Thursday	15-Mar	Branches	Project 1 due	
Tuesday	20-Mar	Branch Prediction		
Thursday	22-Mar	...		
Tuesday	27-Mar	Speculative Execution		
Thursday	29-Mar	Memory Systems		
Tuesday	3-Apr	Winter Break		
Thursday	5-Apr		Hw3 due	HW4 out
Tuesday	10-Apr	...		
Thursday	12-Apr	Cache memories		
Tuesday	17-Apr	Cache replacement policies		
Thursday	19-Apr	Cache coherency		
Tuesday	24-Apr	VLIW	HW4 due	
Thursday	26-Apr	Parallel & Multiprocessor		
Tuesday	1-May	...		
Thursday	3-May	Networks		
Thursday	10-May	Final exams		
Tuesday	16-Mar		Project 2 due	