KURVA VAMSHI KUMAR

Personal Information	Yalal, Tandur Rangareddy, Telangana , India - 501144	*Mobile:* +91-9550535220 *E-mail:* vamshikumarkurva@gmail.com

Research Interests Computer Vision, Machine Learning, Pattern recognition

Education

Indian Institute of Space Science and Technology, Valiyamala **Pursuing sem-II**
M.Tech, Machine Learning and Soft Computing
- *CGPA:* ***9.0***

Sreenidhi Institute of Science and Technology, Ghatkesar **2010 - 2014**
B.Tech, Electronics and Communication Engineering
- *Percentage:* ***84.14***

Sri Chaitanya Junior College, Ameenpur **2008 - 2010**
Intermediate, MPC
- *Percentage:* ***96.5***

A.P.Residential School, Keesaragutta **2008**
SSC
- *Percentage:* ***93***

Industrial Experiance

Design Engineer, Auviz Systems **September 2014 - July 2015**
Job Description : FPGA based implementation of Computer Vision algorithms.
Projects : Object Tracking, FFT, Fixed Point Coding
Keywords : FPGA, Computer Vision, Machine Learning

Relevant Courses

Electronics: Digital Electronics, Control Systems, Communication systems
Machine Learning: ANN, Datamining, Kernel Methods
Mathematics: Probability Theory and Random Processes, Optimization techniques,Descrete Mathematics and Differential Equations

Projects Undertaken

Image Compression using 2DPCA
M.Tech mini Project
- The aim of this project is to extract the features from an image removing the redundant information by measuring the correlation between rows and columns of the image using PCA.
- Tools: OpenCV, python

Object Tracking
Industry Project
- Given an object in the first frame of video, track it in the subsequent frames using the Mean Shift Algorithm.
- Tools: OpenCV, vivado HLS

Technical Skills

Programming Languages: C, C++, python
Software Packages: Matlab/Octave, HLS, OpenCV
VLSI Tools: Xilinx Vivado HLS, Proteus, Tina
Operating Systems: Linux(CentOS, Ubuntu), Windows

Talks **"Singular Value Decomposition(SVD)"**, M.Tech Seminar Talk, IIST **March 2016**

Activities

- Participated in **Hardware Training-Electronic System Design** conducted by **INSIGNIA LABS**.
- Co-ordinator for **ADASTRA 2012**, Tech-Fest conducted by SNIST.

References Available upon request