# P Vamsi Krishna

#### CAREER OBJECTIVE

To apply my skills, experience, and education to a challenging role in a reputable organization. My goal is to contribute to the success of the company by utilizing my technical abilities, critical thinking skills.

## Education

Electronics and Communication Engineering, B V RAJU INSTITUTE OF TECHNOLOGY

CGPA:8.47

2020 - 2024Narsapur

Intermediate-MPC, Narayana Jr College CGPA:9.88

2018 - 2020

High School-CBSE, St. Joseph's English School

Kurnool

2018

Percentage: 84

Kurnool

## **Skills**

C | Python | Java | HTML | CSS | JavaScript | SQL | VerilogHDL

## Certificates

- Learn to Code in Python 3: Programming beginner to advanced Udemy
- Programming for Everybody (Getting Started with Python) Coursera
- Learning Java 11 LinkedIn
- Java Object-Oriented Programming LinkedIn
- Learn Coding with Java from Scratch: Essential Training 2023 Udemy
- HTML Essential Training LinkedIn
- CSS Essential Training LinkedIn
- JavaScript Essential Training LinkedIn
- SQL Essential Training LinkedIn

## **Projects**

# Design of finite state machine of automated washing machine using Verilog HDL

Apr 2022 - Sep 2022

- The automated washing machine can be modeled using a finite state machine (FSM) in Verilog HDL, which can be implemented on an FPGA.
- The FSM will have several states, including start, water\_in ,Rotate and Water\_out, with transitions between states triggered by various inputs such as start, stop.

# **CO-CURRICULAR ACTIVITIES**

- Participated in L&T's TECHgium innovation challenge on Dec 7th, 2022, presenting a leading-edge idea on RF Energy Harvesting Implants, Utilizing radio frequency technology to power medical implants.
- Participated in HACK ELITE 1.0, a 30-hour hackathon hosted by BVRIT Narsapur, where we developed and presented a working model for an automatic live location-sharing device that shares the bus's location to passengers as soon as the bus starts.

## **WORKSHOPS**

• Attended a six-day workshop on "Industrial Exposure on Emerging Trends in VLSI Design" and gained insights into the latest technologies and trends in chip design, verification, and fabrication.

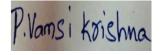
## **EXTRA CURRICULAR ACTIVITIES**

## Event Coordinator- Promethean 2K22, Department of ECE, BVRIT

Organized circuit innovation challenge event as part of Promethean, hosted by BVRIT Narsapur. Challenged
participants to make the necessary connections in a given circuit, testing their technical skills and problemsolving abilities.

## **Declaration**

I hereby declare that the information provided above is accurate to the best of my knowledge



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