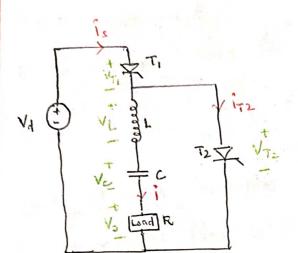
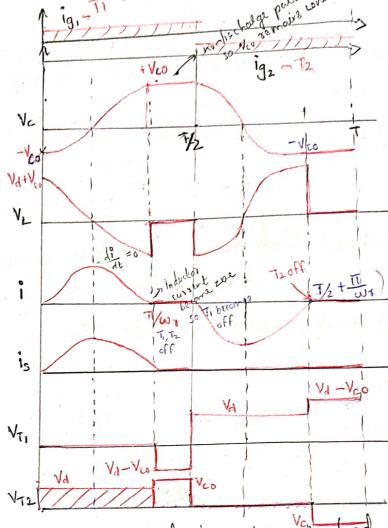
Series Invested

Application in high power, high freq applications.

Otherwise Land C will required will be high.





The commutating elements L&C are connected in societ with load.

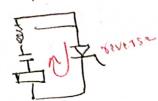
Load current flows continuously through L&C.

Hence, this ext is used in high freq applications.

Initially Ti is made to conduct for 1st half cycle.

cussent path

Then To is made to conduct for 2nd half cycle current path



LSC ONL extricted such that combination R-L-C is underdompted.

At t=0, T. is trunsed on To all off state

$$V_{A} = R_{1}(t) + L \frac{di(t)}{dt} + \frac{1}{C} \int_{0}^{1} (b) dt + V_{C}(0) \int_{0}^{1} (c) dt$$

a retending dest is at helpton in a

$$S = \frac{R}{2L}$$
, $W_s = \int \left(\frac{1}{JLC}\right)^2 - \left(\frac{R}{2L}\right)^2$, $W_o = \frac{1}{JLC}$

 $[1] - (0) = \sqrt{1 - (0)}$

$$V_{12} = V_d \quad \& \quad V_d = i(t)R$$

At
$$t = \frac{\pi}{100}$$
, $f(t) = 0$, T_1 is turned off

*

X

X

*

$$0 = RI(9) + L(SL(9) + I_0) \frac{di(t')}{dt} / t' = 0 = \frac{-V_{cmax}}{L}$$

$$+ \frac{1}{cs} (I(9)) + \frac{V_c(0)}{s}$$

$$\frac{L}{S^2 + R + S + L}$$

$$S = \frac{R}{2L} \quad w_{s} = \sqrt{\frac{1}{JLC}}^{2} - \left(\frac{R}{2L}\right)^{2} \quad w_{o} = \frac{1}{JLC}$$

$$V_{L}(t) = \frac{Ldi(t)}{dt} = -\frac{w_0}{w_s} V_{cmax} e^{-St'} cos (w_s t' + \phi)$$

$$V_{Tl} = -V_{\dot{q}}$$

Draw back of Series Invested

maximum inverted frequency is limited to a value less than circuit ringing frequency (wr).

to solve week

For very low values of invester frequencies, load voltage is lightly distorted.

L is choosen on bosis of attenuation factor.

there is no attenuation, then 1(t) would be

$$\frac{\log \ln (AF)}{8f_x L}$$

L & selected such that AF = 0.5

Design of C

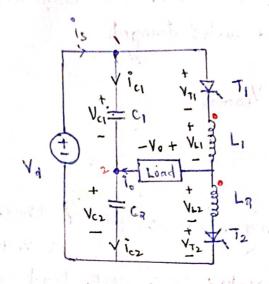
C is selected from value of ws.

$$w_{8} = \int \left(\frac{1}{LC} - \left(\frac{R}{2L} \right)^{2} \right)$$

$$w_{\epsilon}$$
 selected such that $\frac{w_{\epsilon}t_{\epsilon}}{\pi} < \frac{T}{2}$

$$w_{8} = \int \left(\frac{1}{LC} - \left(\frac{R}{2L}\right)^{2}\right) \qquad \Rightarrow \qquad C = \frac{1}{L} \left[\frac{1}{w_{1}^{2} + \left(\frac{R}{2L}\right)^{2}}\right]$$

If Load & variable, then c is selected for maximum possible value of R so that circuit is under damped. Voltage rating of C is that Violax Selection of Thyristor Forward blocking voltage rating must be greater than Vanax. +. Peak current rating must be greater than peak load current for minimur load rosistance. Vet Vco e 2L Zws to must be less than $t_c = \frac{T}{2} - \frac{\pi}{w_x}$ modified Sexies Invested igi. Îg2 Vc2 Load current io



Li=La

Ci=Ca

Li & La over tightly coupled-high coef of coupling (mutual inductance)

Foo téo Ta is on

Vc1 = Vcmax

Vcz = - Vemin

io à negotive

Vo = PoxR

ici à tre l'icz is negative

is it the (is = ic, when to conducting)

Vea regotive (Vez = -ve, load current -ve)

 $V_{L2} = V_{E2} - V_0$

VLI negotive

* At t = 0 T, is turned on

Ver à coupled across L1 = VLZ

Ta is commutated off.

c, discharges through L, and load

* Ver changer from Vermax to Vermin

Co gets changed through T, Le and Load

io = is - ic, kel at node;

ice = ice + io kel at rode 2

is = ice + io kel at rode 2

* At $t = \frac{T}{2}$, T_2 is turned on

Ver is compled across L2 = VLI

Till is commutated off

C2 discharges through L2 and load

Ver changes from Vermax to Vermin

C1 gets charged through load, T2, & L2

10 =

VT1 = Vd - VL1 - VL2 = Vd - 2 VL2

Advantages over series inverter.

Inverter freg can be less than singing freg

During -ve half cycle also source supplies power > Less distortion in source corrent.