MDSP END SEM Questions 2023

- Realisation of IIR filter
- Assembly language code to add two numbers with specified memory location and save the result to a specified memory location.
- Question on EXT instruction
- C code to implement IIR filter
- Write any four resource constraints with examples
- Difference between DSP and DSC with respect to their applications
- Disadvantages of more number of pipeline phases
- Difference between pipelining and VLIW
- Illustrate Pipelining.
- Explain ten features of DSC
- Memories associated with EDMA
- McBSP
- What is the use of Watchdog timer?
- Realistic versus ideal filter response with diagram
- Which processor can be used if we have to implement 4th order Butterworth filter and why ?
- Question on LDW instruction
- Explain the signal flow graph to implement 3rd order IIR filter.
- EDMA
- Explain the opcode and each section of functional unit .L
- To find out the no. Of fetch packet from given instructions with their respective excute packet.